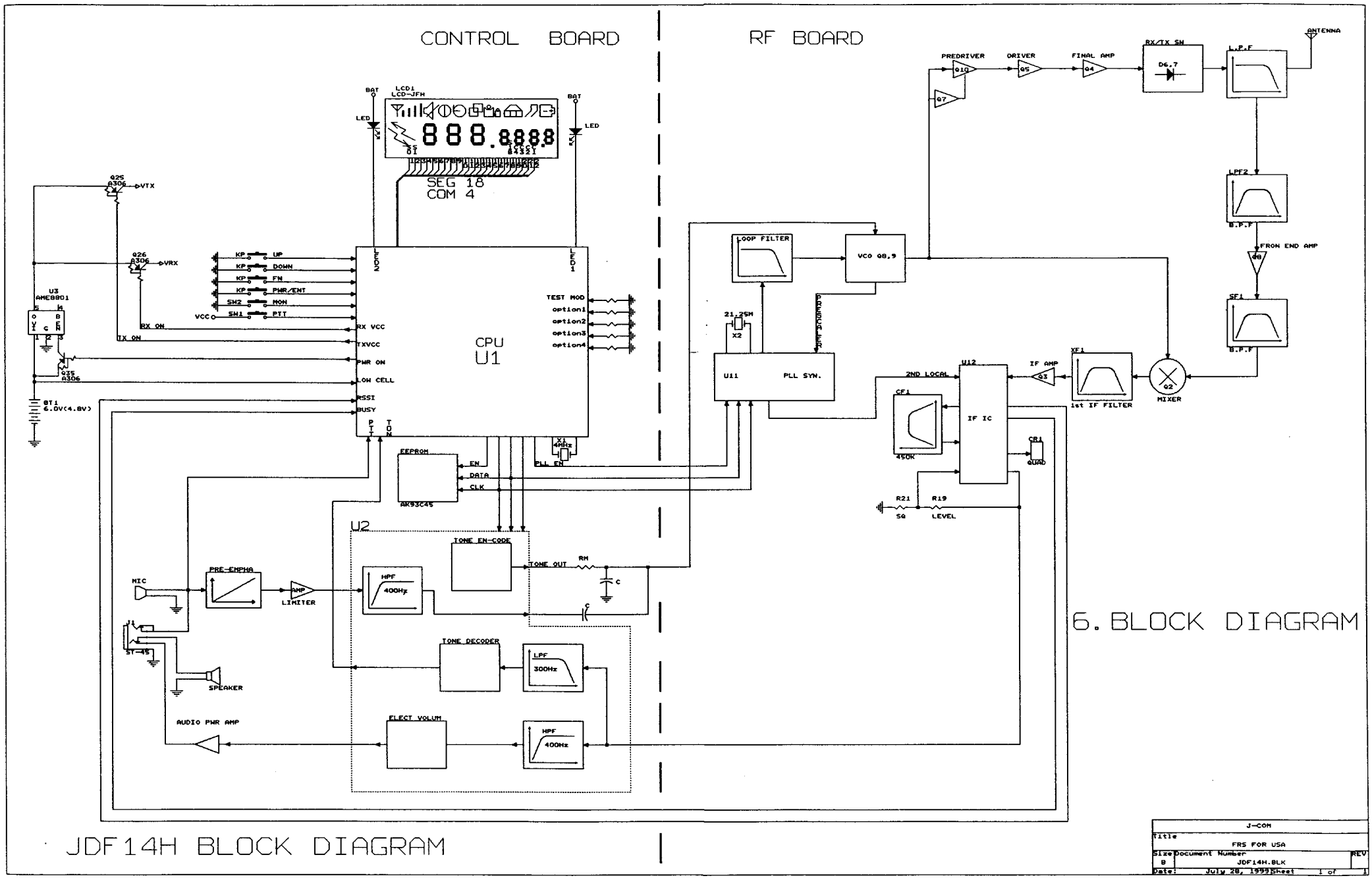
CONTROL BOARD
RF BOARD
LCD1
LCD-JFH
SEG 18
COM 4
BAT
LED
Q25
A306
VTX
Q26
A306
VRX
U3
AME8801
BT1
6.0V(4.8V)
KP UP
KP DOWN
KP FN
KP PWR/ENT
SW2 MON
SW1 PTT
VCC
RX ON
TX ON
Q35
A306
CPU
U1
RX VCC
TXVCC
PWR ON
LOW CELL
RSSI
BUSY
TEST MOD
option1
option2
option3
option4
EEPROM
AK93C45
EN
DATA
CLK
PLL EN
X1
4MHz
U2
TONE EN-CODE
TONE OUT
RM
C
HPF
400Hz
PRE-EMPHA
MIC
AMP
LIMITER
J1
ST-45
SPEAKER
TONE DECODER
LPF
300Hz
ELECT VOLUM
AUDIO PWR AMP
LOOP FILTER
VCO Q8,9
21.25M
X2
U11
PLL SYN.
PREDRIVER
DRIVER
FINAL AMP
Q10
Q5
Q4
Q7
RX/TX SW
D6,7
L.P.F
ANTENNA
LPF2
B.P.F
FRON END AMP
Q6
SF1
B.P.F
XF1
1st IF FILTER
Q2
MIXER
IF AMP
Q3
U12
IF IC
2ND LOCAL
CF1
450K
CR1
QUAD
R21
R19
SQ
LEVEL
6. BLOCK DIAGRAM
JDF14H BLOCK DIAGRAM
J-COM
Title
FRS FOR USA
Size
B
Document Number
JDF14H.BLK
REV
Date: July 28, 1999
Sheet 1 of 1