CIRCUIT DESCRIPTION

Model: GH9402/GH9404

1 Base

a. RF Transmitter Section – RF Board

Compressed audio signal is frequency modulated through the varactor diode D3. Diode D3, choke coil L4 and the external components formed the voltage controlled oscillator circuit for the transmitter part. This circuit generates the TX VCO frequency. A portion of this signal is fed back to the PLL IC's pin1 (FIN1) for phase comparison. Once the phase of oscillation stabilized, the PLL circuit generates the error voltage necessary for the VCO to oscillate at the desired transmitter's RF frequency. The VCO circuit impedance is matched with the succeeding circuit through the transistor Q2 that also acts as the buffer amplifier. RF amplifier Q7 boosts the signal for transmission. This amplified RF signal is trimmed to the desired frequency band by BPF2475 so as not to interfere with the receiver circuit. The transmitter RF signal is then propagated through the antenna.

b. RF Receiver Section - RF Board

The Base Unit antenna receives RF signal. Band Pass Filter BPF906 trims the signal to the desirable frequency band. Transistor Q6 is a low noise amplifier that boosts the RF signal to a specific level for mixing. PLL IC1 (KB8825) is used as a Universal Phase Lock Loop circuit. The frequency from the Voltage Controlled Oscillator (VCO) D1, and L5 is fed back to the PLL IC through pin 16 (FIN2) for phase comparison. During channel scanning or turning the unit on, once the phase of oscillation stabilized (locked), the PLL circuit generates the first local oscillator frequency for down-converting the received RF signal into the first IF frequency 10.7MHz. This process is accomplished through the IF mixer circuit Q3. Q1 is used for matching the impedance of the mixer circuit with the succeeding circuits. The resulting IF signal is kept constant by the IF Filter FL1 to 10.7MHz which is then mixed with the second local oscillator frequency 11.150MHz (derived from X1 & C47) to produced a much lower IF frequency. This lower IF frequency if further filtered by IF Filter FL4 to produce a more stable signal of 450KHz. Quadrature signal detection is accomplished internally by the Narrow-band Detector IC2 (KA3361) with the IF coil L7. The recovered audio frequency can be taken from IC2 audio output pin9. Double conversion of received signal is utilized to improve the image frequency rejection of the unit.

c. Transmitter Audio Section – Main Board

Audio Frequency signal from the telephone line is compressed through the compressor part of IC4 to minimize the transmission noise. The degree of compression depends on the external RC combinations. AGC is also utilized by IC4 to avoid shock noise caused by abrupt change of audio levels. The compressed audio is filtered and amplified for better acoustical performance.

d. Receiver Audio Section – Main Board

The compressed Audio Frequency signal is passed through passive RC filters for acoustic compliance. The filtered audio is then fed to the Compander IC4 for expansion thus retrieving the original Audio signal with noise filtered out. Q1 are used as buffer circuit. Transistor Q3

(A94) isolates the high-voltage telephone line to the rest of the circuit. Q3 (A94) is also used as a hybrid transformer to create a two-way path for audio transmission to and reception from the telephone line.

2 Handset

a. RF Transmitter Section – RF Board

Refer to portion 1.a for this section. All circuit performance is the same except that Band Pass Filter BPF2475 is changed to BPF906 for the handset transmission.

b. RF Receiver Section – RF Board

Refer to portion 1.b for this section. All circuit performance is the same except that Band Pass Filter BPF906 is changed to BPF2475 for the handset reception.

c. Transmitter Audio Section – Main Board

Audio Frequency signal from the handset or from the headset microphone is compressed through the compressor part of IC2 to minimize the transmission noise. The degree of compression depends on the external RC combinations. AGC is also utilized by IC2 to avoid shock noise caused by abrupt change of audio levels. The compressed audio is filtered and amplified for better acoustical performance. Q5 is a switching transistor that controls the power supply for the TX RF part.

d. Receiver Audio Section – Main Board

The compressed Audio Frequency signal is passed through passive RC filters for acoustic compliance. The filtered audio is then fed to the Compander IC2 for expansion thus retrieving the original Audio signal with noise filtered out. Q3 act as audio amplifier to sufficiently drive the handset speaker. Q1, Q6 and Q6 are switching transistors that control the power supply for the RF part, the Compander part and the AF amplifier respectively. An earphone jack is provided for an optional headset unit for handsfree conversation on the handset.

3 OTHERS (Handset):

a. Charging and Reset Controls

Recharging the handset battery is accomplished by putting the handset on the cradle. Q200 detects this action and sends a command to the CPU for proper exchange of security code. Switching SW4 to the RING OFF mode can extend Battery life.

b. Ring Detection

When the handset receives the ring command from the base unit, the CPU will send buzzer signal to the ringer amplifier Q201 that drives the Buzzer.

4 [redacted] OTHERS (Base):

a. Hook Switching and Dialing

Hook switching and pulse dialing is accomplished by U1D, which is controlled by the CPU. DTMF signal from the ladder circuit R67-R74 to the CPU is filtered and amplified by U1A, and Q1.

b. Over-voltage Protection

Fuse F1 and varistor Z1 act as high current and high voltage protectors for the telephone line interface. In case of presence of voltage surge across the telephone line, Z1 decreases its resistance and dumps the line voltage to a safe level. Fuse F1 opens when excessive current is present on the line thus protecting both the user and the line interface.

c. Battery Charging & Code Setting

Battery charging commences when transistor Q7 detects the presence of the handset on cradle. D25 & C29 form the reset circuit in conjunction with the charge detects circuit to command the CPU to change the security code. When the reset circuit is activated, the CPU will send a new security code to the handset selecting among 65536 combinations.

d. Ring Detection

Incoming ring signal is detected by the LM324 U1D. Diode D1-D4 and R8 set the level of signal detection. The CPU checks the frequency of the ring signal, and when valid, sends the ringing command to the speaker or to the Handset.

e. Power Supplies

Diode D11 ensures uniform polarity for the entire circuit. IC1 regulates the voltage to +9Vdc for the rest of the circuit. Transistor Q8 controls the power supplied to the TX part of the RF circuits.

f. Squelch Detection

In conjunction with the 3361 IC (IC2 of the Base RF), fixed resistor sets the level of signal detection and U1C acts as the comparator circuit whose composite output is the RSSI signal for the CPU.

g. RX Data

Commands from the Handset is filtered and re-constructed by the Schmitt trigger circuit U1B. The composite output is the RX Data that is input to the CPU for validation and processing.

- END -

GH9402/GH9404

2.4GHz/900MHz NEW FREQUENCY CHART

BASE			HANDSET		BASE			HANDSET	
CH	TX	RX	TX	RX	CH	TX	RX	TX	RX
1	**2400.59179**	922.01181	**922.01181**	2400.59179	26	**2403.00173**	923.61844	**923.61844**	2403.00173
2	**2400.68818**	922.07608	**922.07608**	2400.68818	27	**2403.09813**	923.68271	**923.68271**	2403.09813
3	**2400.78458**	922.14035	**922.14035**	2400.78458	28	**2403.19452**	923.74697	**923.74697**	2403.19452
4	**2400.88098**	922.20461	**922.20461**	2400.88098	29	**2403.29092**	923.81124	**923.81124**	2403.29092
5	**2400.97738**	922.26888	**922.26888**	2400.97738	30	**2403.38732**	923.87550	**923.87550**	2403.38732
6	**2401.07376**	922.33314	**922.33314**	2401.07376	31	**2403.48372**	923.93977	**923.93977**	2403.48372
7	**2401.17017**	922.39741	**922.39741**	2401.17017	32	**2403.58012**	924.00403	**924.00403**	2403.58012
8	**2401.26657**	922.46167	**922.46167**	2401.26657	33	**2403.67651**	924.06830	**924.06830**	2403.67651
9	**2401.36297**	922.52594	**922.52594**	2401.36297	34	**2403.77291**	924.13257	**924.13257**	2403.77291
10	**2401.45937**	922.59020	**922.59020**	2401.45937	35	**2403.86931**	924.19683	**924.19683**	2403.86931
11	**2401.55576**	922.65447	**922.65447**	2401.55576	36	**2403.96571**	924.26110	**924.26110**	2403.96571
12	**2401.65216**	922.71873	**922.71873**	2401.65216	37	**2404.06210**	924.32536	**924.32536**	2404.06210
13	**2401.74856**	922.78300	**922.78300**	2401.74856	38	**2404.15850**	924.38963	**924.38963**	2404.15850
14	**2401.84496**	922.84726	**922.84726**	2401.84496	39	**2404.25490**	924.45389	**924.45389**	2404.25490
15	**2401.94135**	922.91153	**922.91153**	2401.94135	40	**2404.35130**	924.51815	**924.51815**	2404.35130
16	**2402.03775**	922.97579	**922.97579**	2402.03775	41	**2404.44770**	924.58242	**924.58242**	2404.44770
17	**2402.13415**	923.04005	**923.04005**	2402.13415	42	**2404.54409**	924.64669	**924.64669**	2404.54409
18	**2402.23053**	923.10432	**923.10432**	2402.23053	43	**2404.64049**	924.71095	**924.71095**	2404.64049
19	**2402.32695**	923.16859	**923.16859**	2402.32695	44	**2404.73689**	924.77522	**924.77522**	2404.73689
20	**2402.42334**	923.23285	**923.23285**	2402.42334	45	**2404.83329**	924.83948	**924.83948**	2404.83329
21	**2402.51974**	923.29712	**923.29712**	2402.51974	46	**2404.92968**	924.90375	**924.90375**	2404.92968
22	**2402.61614**	923.36138	**923.36138**	2402.61614	47	**2405.02608**	924.96801	**924.96801**	2405.02608
23	**2402.71254**	923.42563	**923.42563**	2402.71254	48	**2405.12248**	925.03228	**925.03228**	2405.12248
24	**2402.80893**	923.48991	**923.48991**	2402.80893	49	**2405.21888**	925.09654	**925.09654**	2405.21888
25	**2402.90533**	923.55418	**923.55418**	2402.90533	50	**2405.31527**	925.16081	**925.16081**	2405.31527