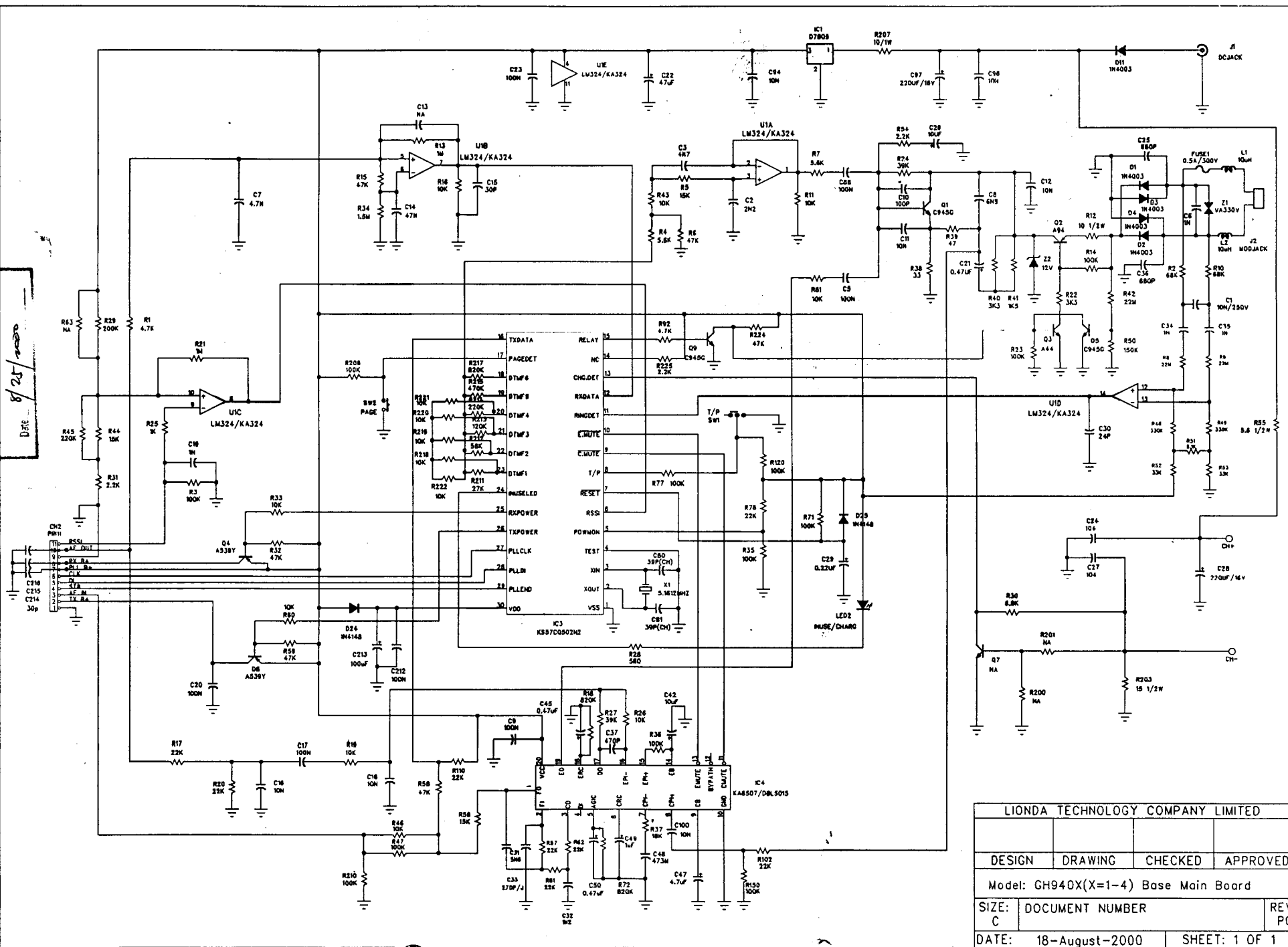


Date: 8/25/2000



LIONDA TECHNOLOGY COMPANY LIMITED			
DESIGN	DRAWING	CHECKED	APPROVED
Model: GH940X(X=1-4) Base Main Board			
SIZE: C	DOCUMENT NUMBER		REV P0
DATE:	18-August-2000	SHEET: 1 OF 1	

ANT

C44 0.5P

C87 0.5P

C80 1P

C84 2P

C85 10N

C82 24P

C88 10P

R48 100K

R50 22K

D40 24P

C53 10P

R26 100K

R46 22K

C93 10N

C89 1P

C81 0.5P

C86 0.5P

C83 2P

C50 0.5P

D10 9M43RF01

Q2 9M43RF01

F2 BPF2475

C82 0.5P

C56 1P

R37 100R

C70 5P

C90 1P

C91 1P

C92 1P

C93 1P

C94 1P

C95 1P

C96 1P

C97 1P

C98 1P

C99 1P

C100 1P

C101 1P

C102 1P

C103 1P

C104 1P

C105 1P

C106 1P

C107 1P

C108 1P

C109 1P

C110 1P

C111 1P

C112 1P

C113 1P

C114 1P

C115 1P

C116 1P

C117 1P

C118 1P

C119 1P

C120 1P

C121 1P

C122 1P

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C372 1P

C373 1P

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C375 1P

C376 1P

C377 1P

C378 1P

C379 1P

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C386 1P

C387 1P

C388 1P

C389 1P

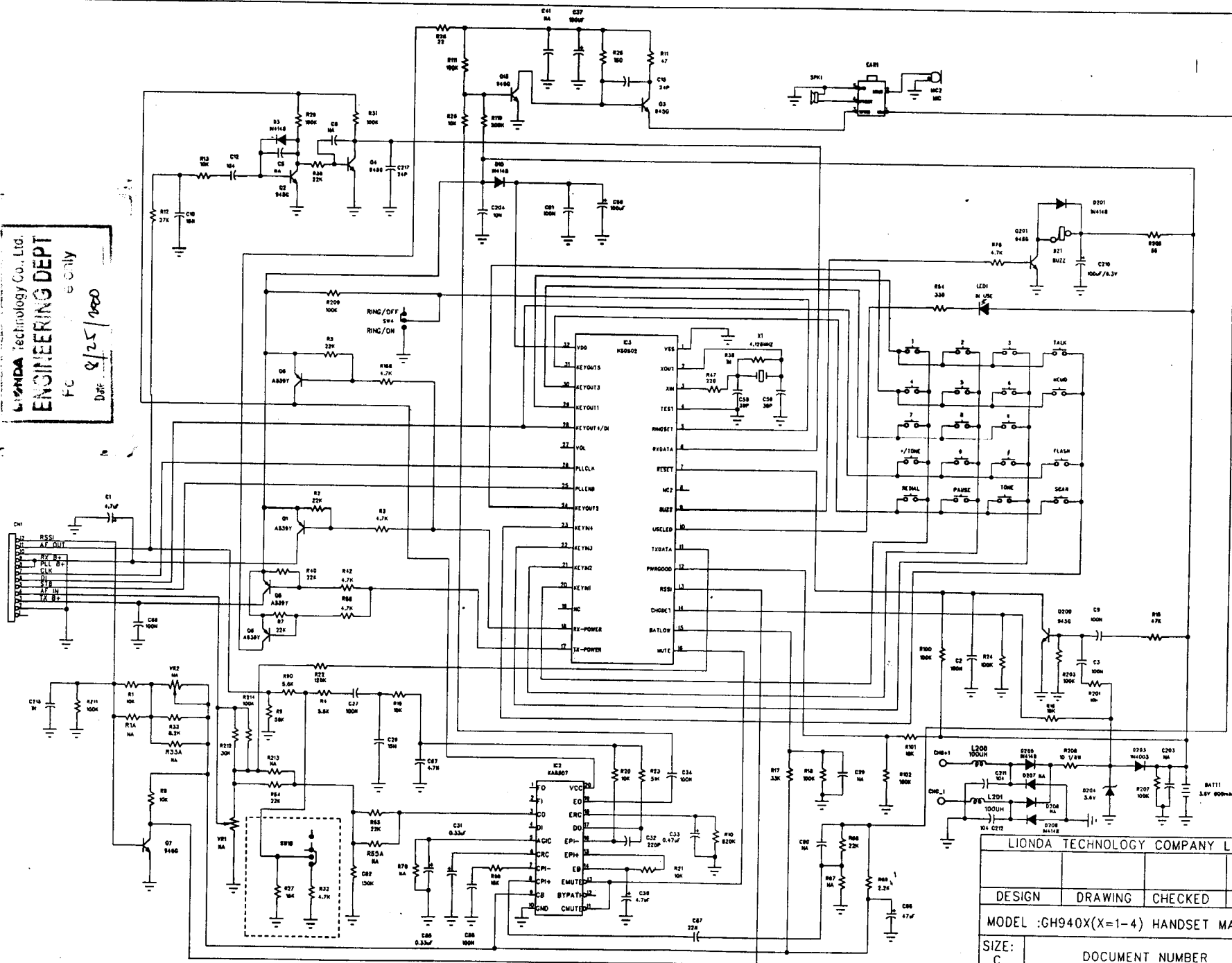
C390 1P

C391 1P

LIIONDA Technology Co., Ltd.
ENGINEER IN CHARGE
8/25/2002

LIONDA TECHNOLOGY COMPANY LIMITED			
DESIGN	DRAWING	CHECKED	APPROVED
TITLE : GH9402(SERIES) BASE RF SCHEMATIC			
SIZE : C	2.4GHZ 50-CHANNEL ANALOG		REV P01
DATE: AUGUST-18-2000		SHEET: 1 OF 1	

LIANDA Technology Co., Ltd.
ENGINEERING DEPT
 For use only
 Date: 8/25/2000



LIONDA TECHNOLOGY COMPANY LIMITED

DESIGN	DRAWING	CHECKED	APPROVED
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MODEL :GH940X(X=1-4) HANDSET MAIN BOARD

SIZE: C	DOCUMENT NUMBER	REV PO
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DATE: 18-AUGUST-2000	SHEET: 1 OF 1
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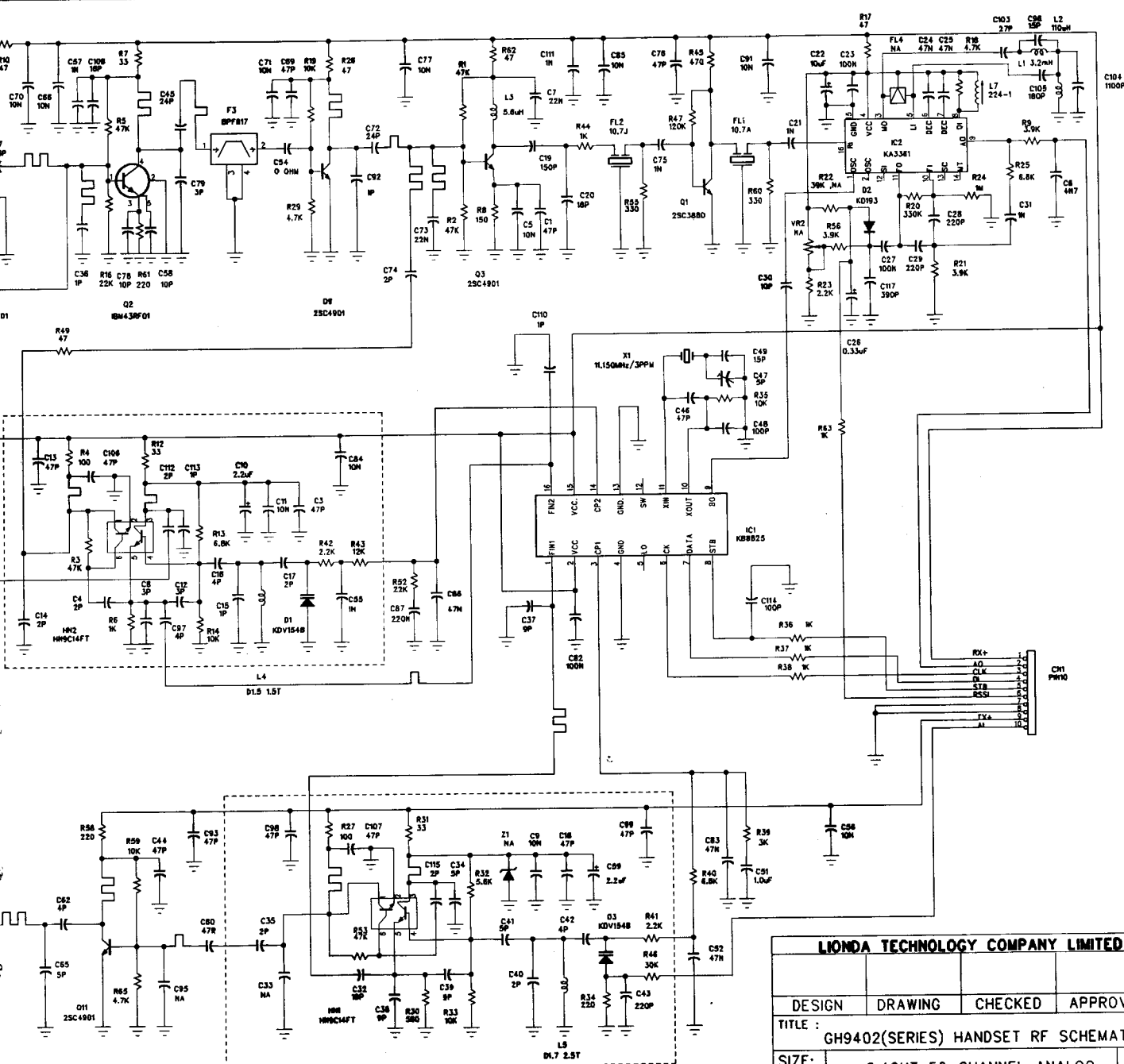
The schematic diagram illustrates the RF circuitry of a handset, organized into several functional blocks:

- Input Section:** Features an antenna (ANT) connected to a matching network (C116, C81, C100, C101) and a bandpass filter (F1, BPF2473). The signal path includes a matching network (C87, C94) and a variable capacitor (Q1, 25C3880).
- Low Noise Amplifier (LNA):** Utilizes a 2N43901 transistor (Q2) to amplify the received signal. It includes a matching network (R16, C78, R61, C58) and a feedback network (R29, C79).
- Intermediate Frequency (IF) Amplifier:** Employs a 2N43901 transistor (Q3) for further signal amplification. It includes a matching network (R7, C73, R2, C74) and a feedback network (R8, C5, C1, C20).
- Mixer and Local Oscillator (LO):** The mixer section uses a 2N43901 transistor (Q4) to combine the RF signal with a 11.150MHz/3PPM local oscillator (X1). The LO is driven by a 2N43901 transistor (Q5) and includes a matching network (R49, C110).
- Baseband Processor:** This section includes a 2N43901 transistor (Q6) and a 2N43901 transistor (Q7) for baseband processing. It includes a matching network (R51, C81, R50, C84) and a feedback network (R2, C73, R2, C74).
- Power Amplifier (PA):** Utilizes a 2N43901 transistor (Q8) to drive the antenna. It includes a matching network (R51, C81, R50, C84) and a feedback network (R2, C73, R2, C74).
- Control and Monitoring:** Includes a 2N43901 transistor (Q9) for control and monitoring, a 2N43901 transistor (Q10) for monitoring, and a 2N43901 transistor (Q11) for control and monitoring.

The diagram also includes a table for component values and a section for the LIONDA Technology Co. Ltd. logo and contact information.

DESIGN	DRAWING	CHECKED	APPROV
TITLE: CH9402(SERIES) HANDSET RF SCHEMATIC			

LISNDA Technology Co., Ltd
ENGINEERING DEPT
For Internal Use Only
Date: 8/25/2000



LIONDA TECHNOLOGY COMPANY LIMITED			
DESIGN	DRAWING	CHECKED	APPROVED
TITLE : GH9402(SERIES) HANDSET RF SCHEMATIC			
SIZE : C	2.4GHZ 50-CHANNEL ANALOG		REV P01
DATE:	AUGUST-18-2000	SHEET: 1 OF 1	