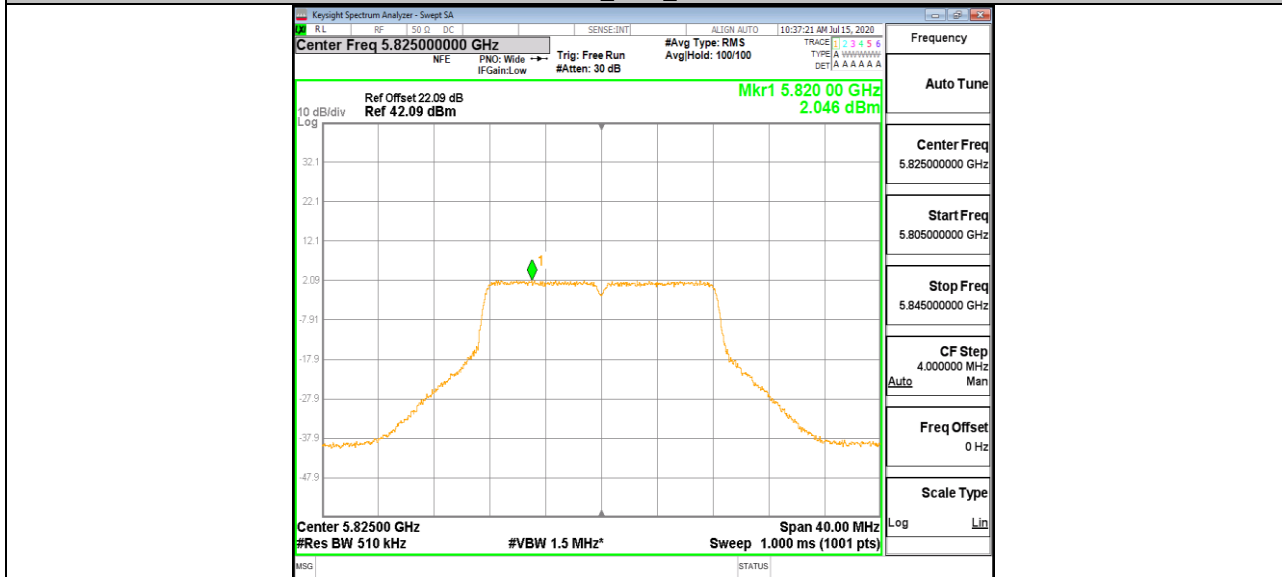
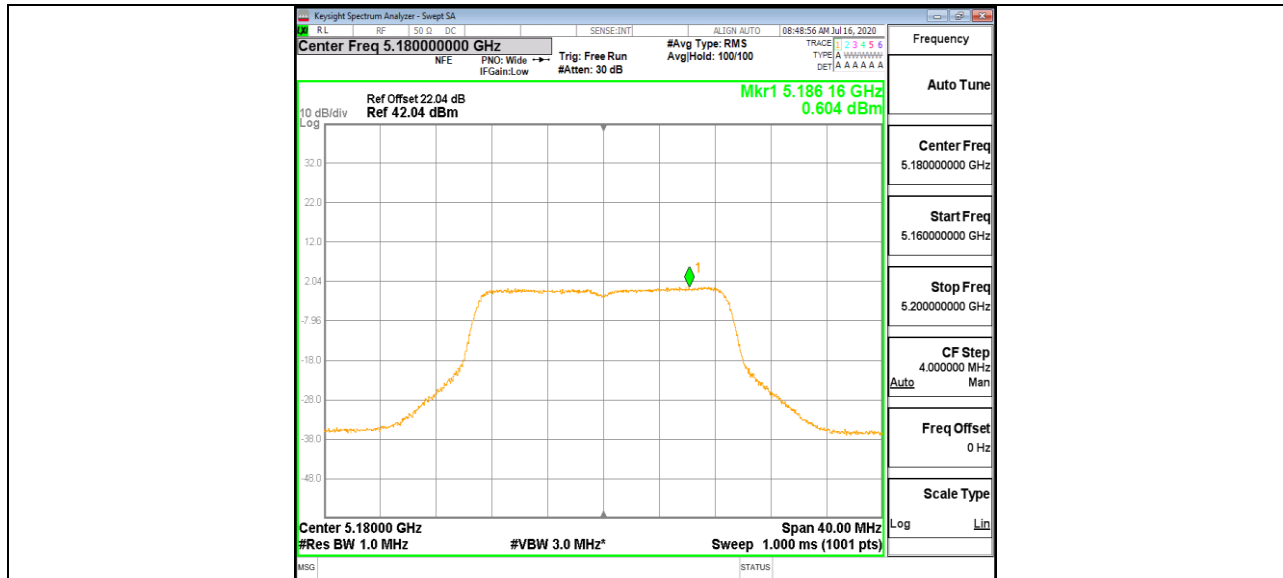


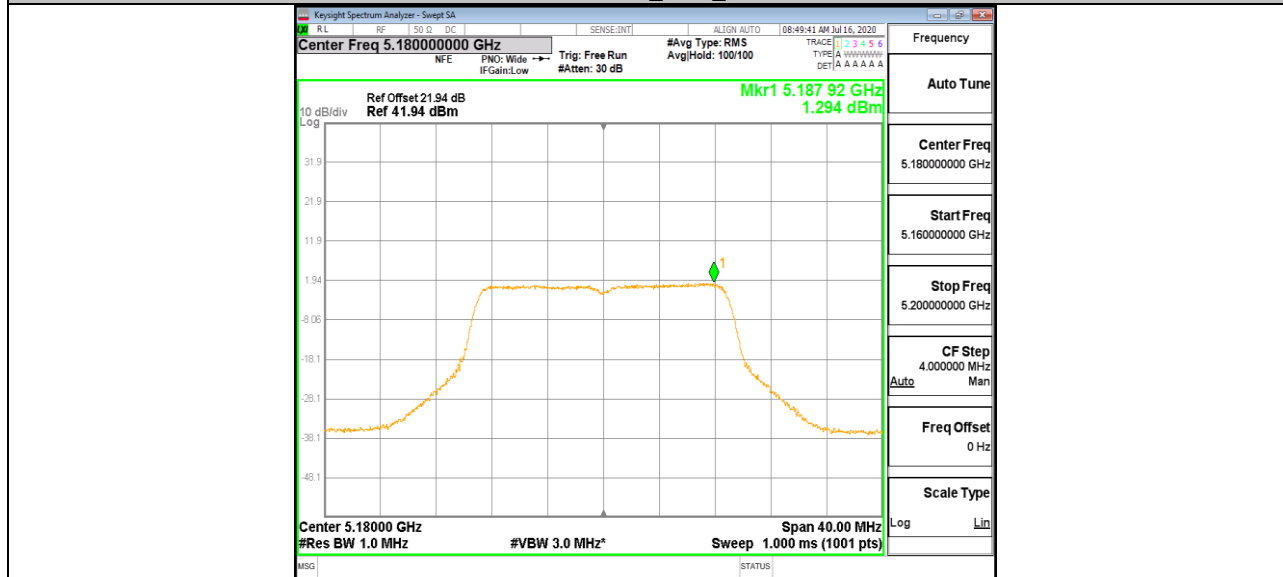
11A\_Ant2\_5825



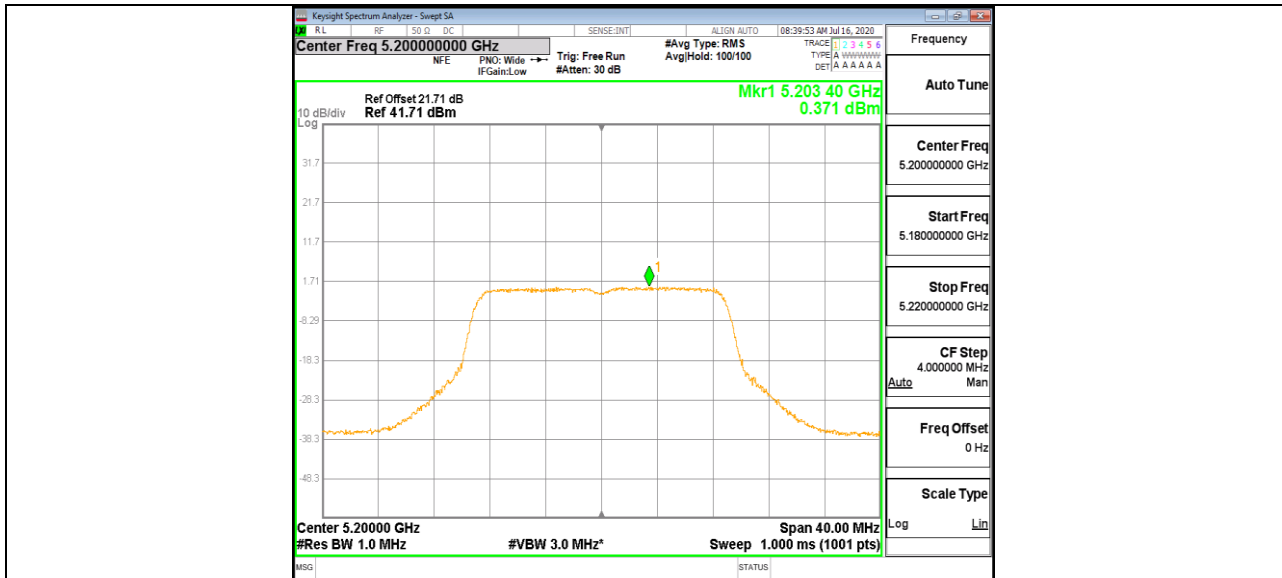
11N20MIMO\_Ant1\_5180



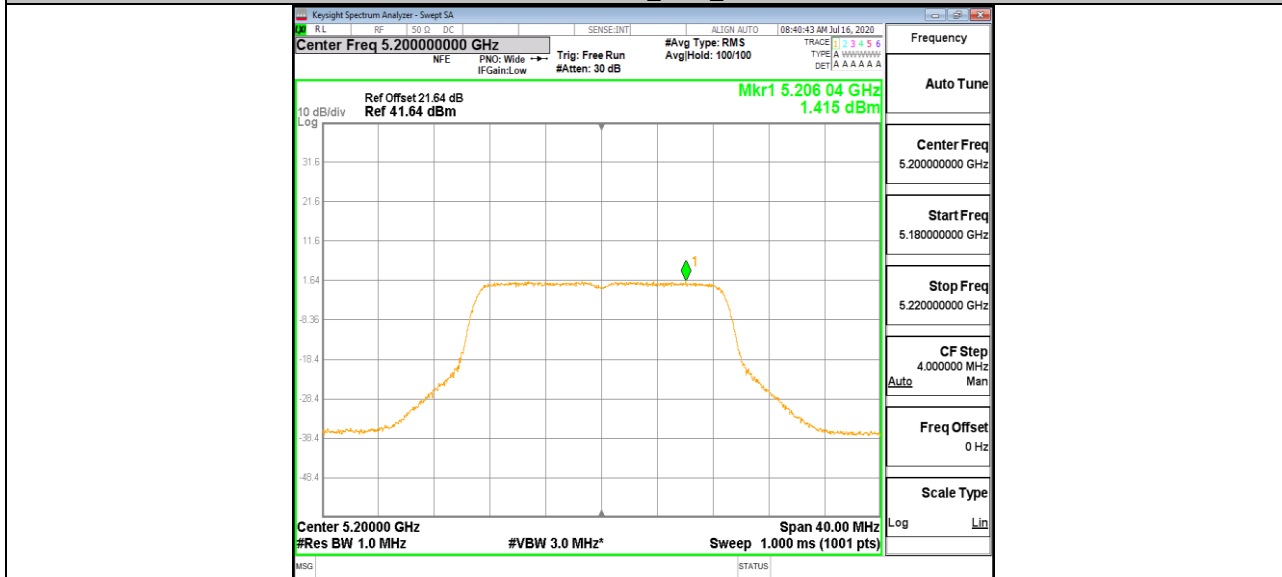
11N20MIMO\_Ant2\_5180



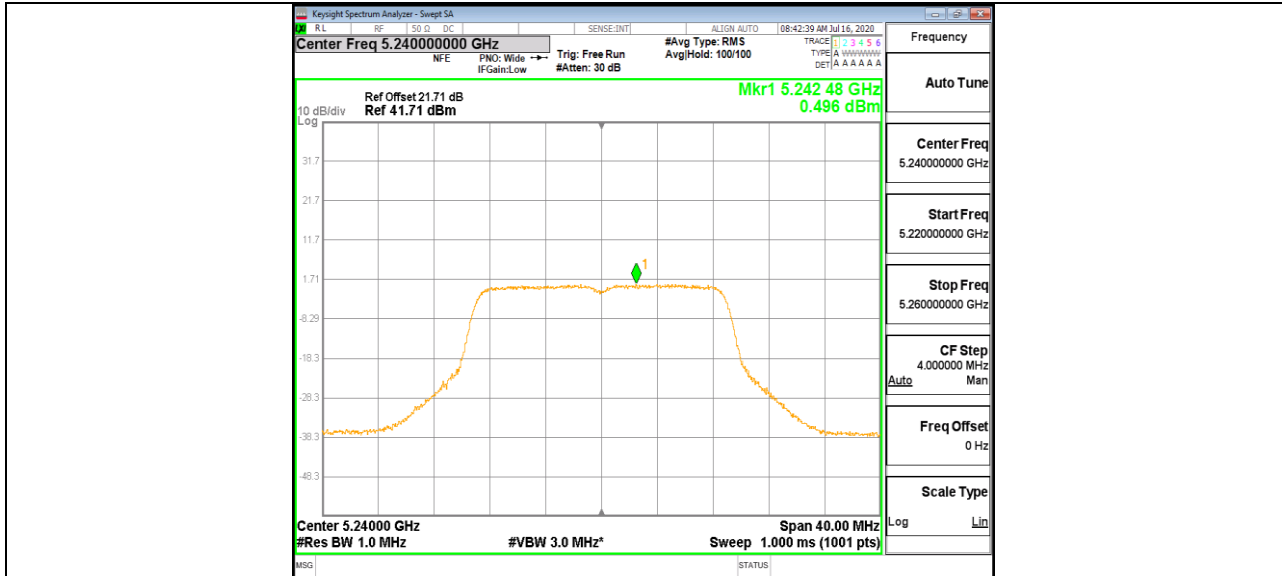
11N20MIMO\_Ant1\_5200



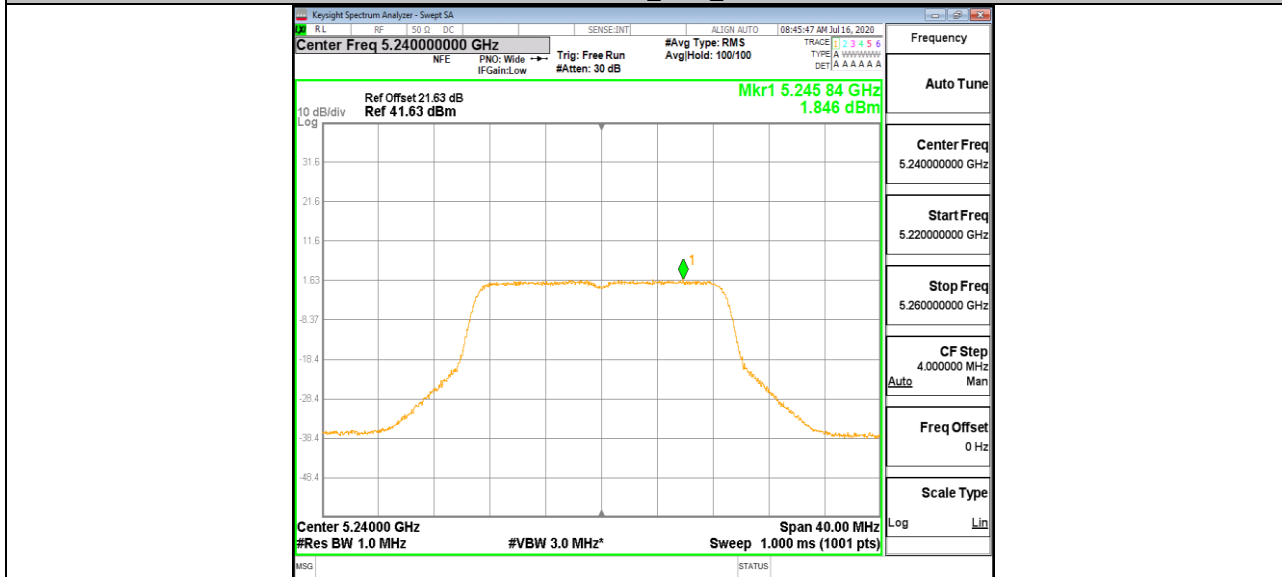
11N20MIMO\_Ant2\_5200



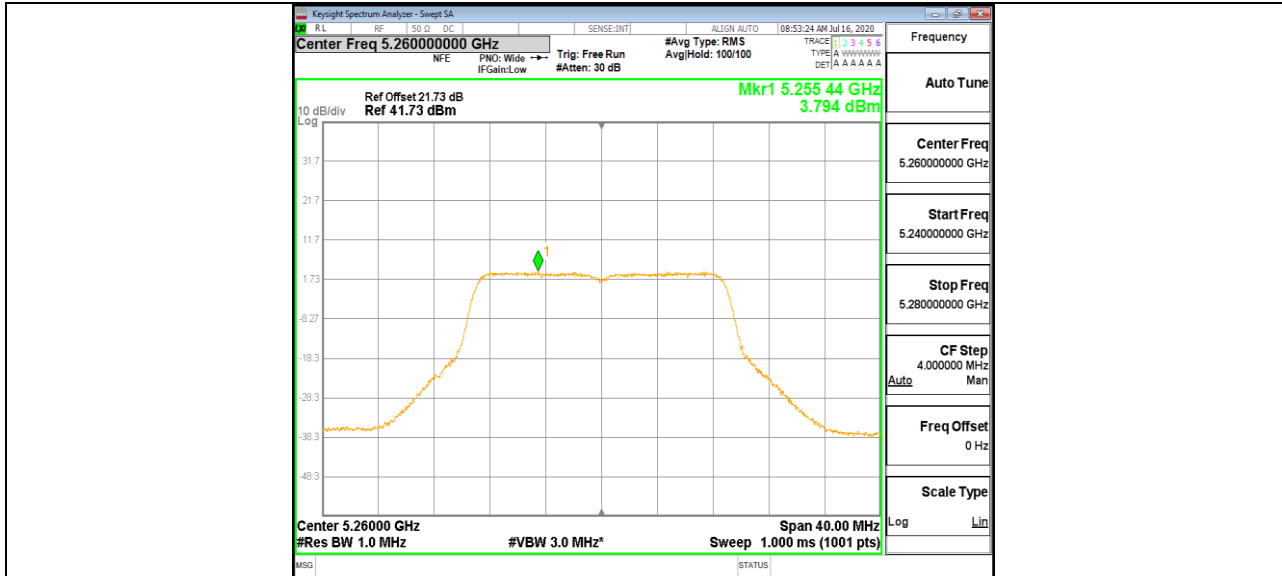
11N20MIMO\_Ant1\_5240



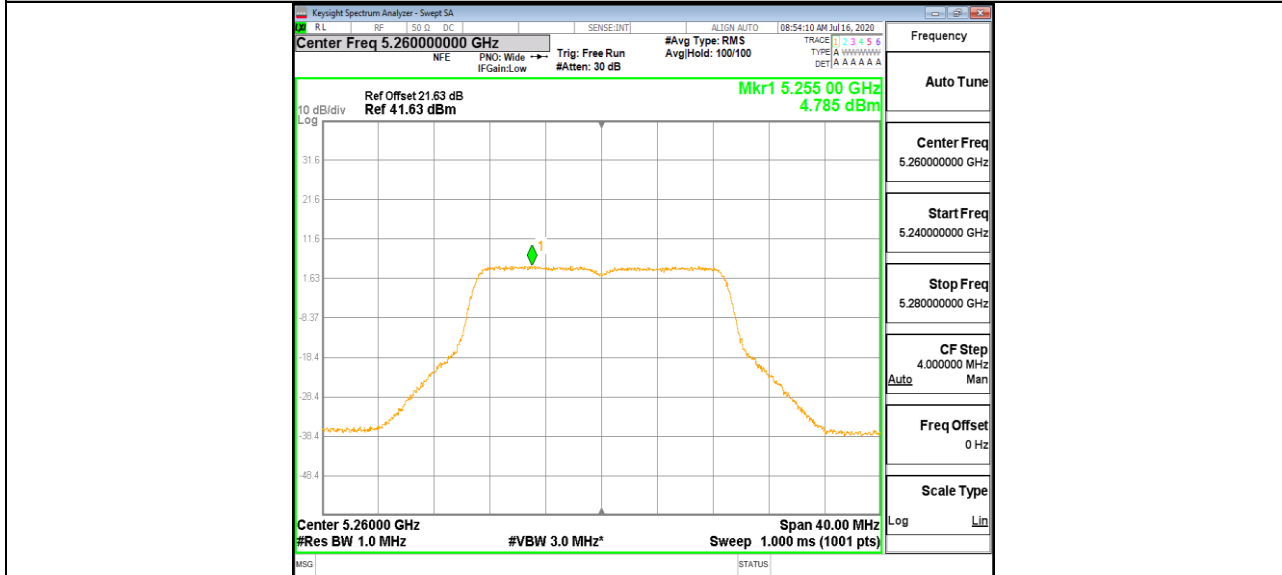
11N20MIMO\_Ant2\_5240



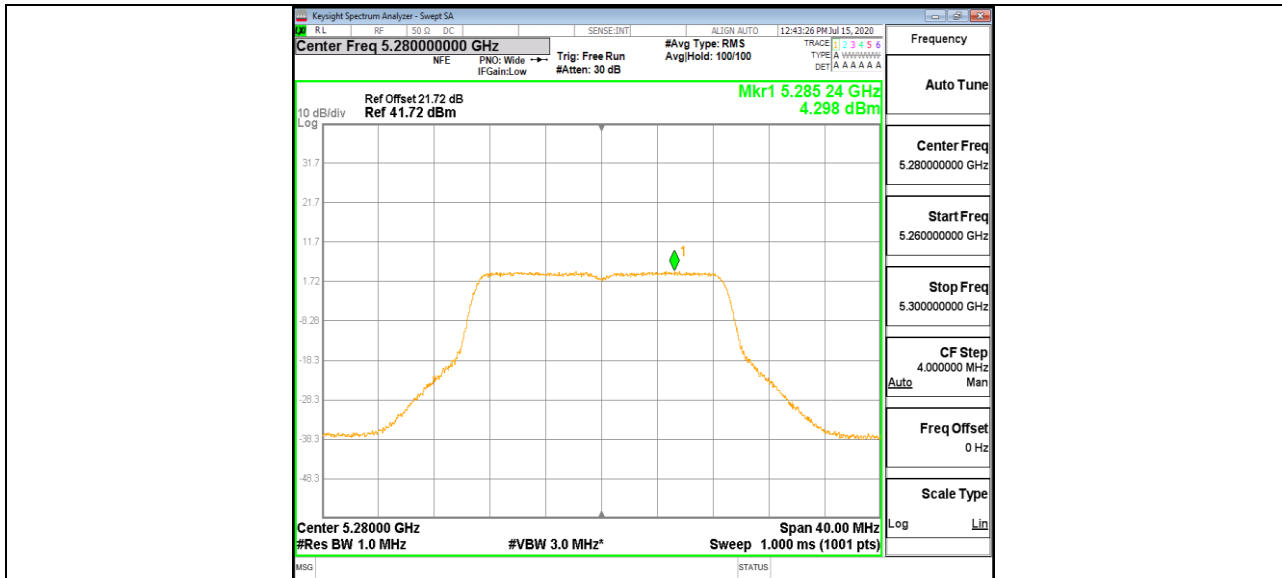
11N20MIMO\_Ant1\_5260



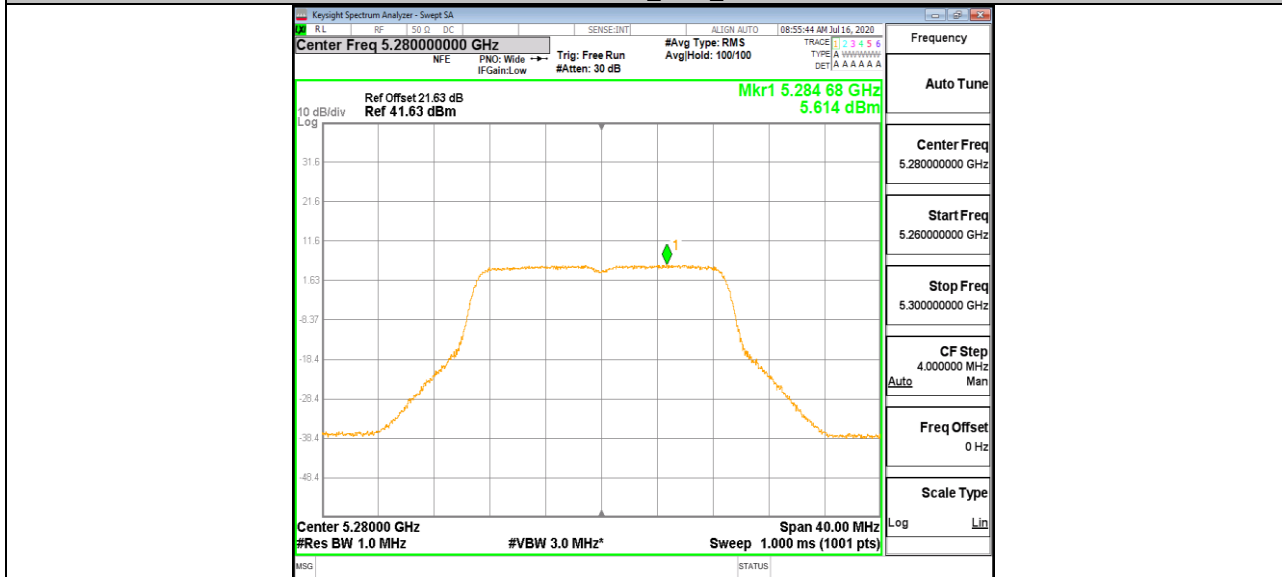
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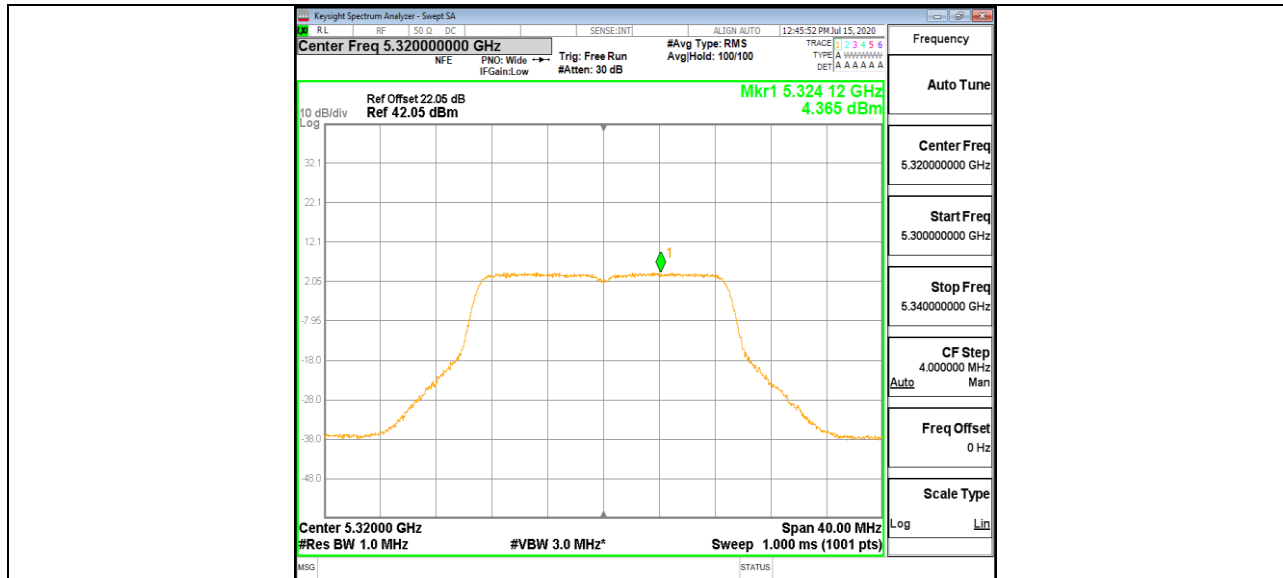
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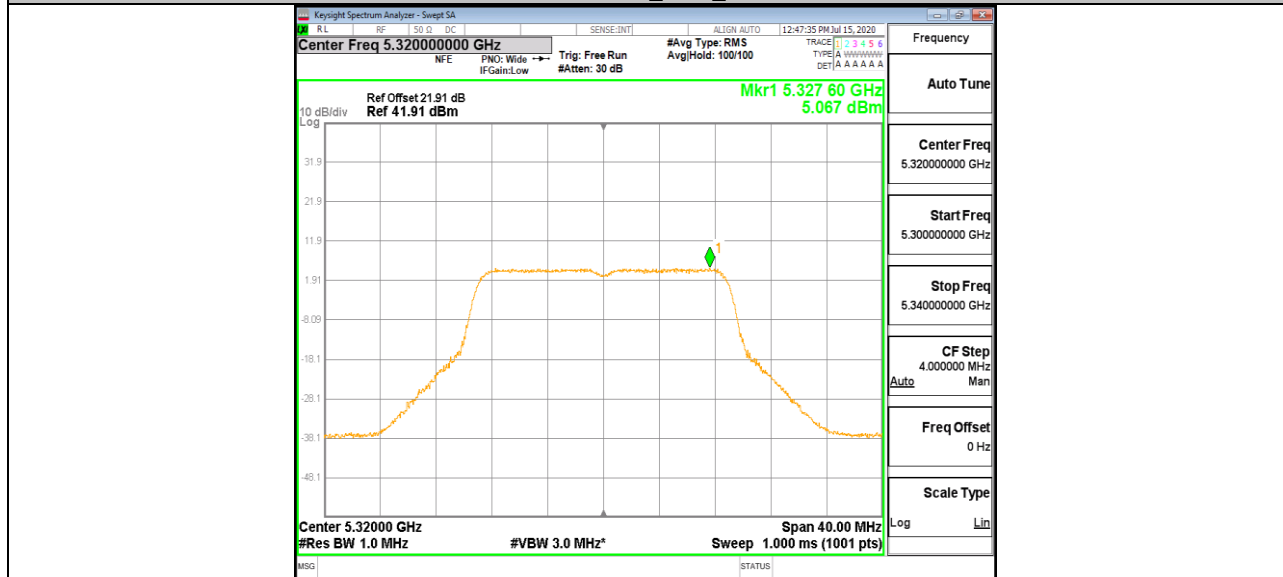
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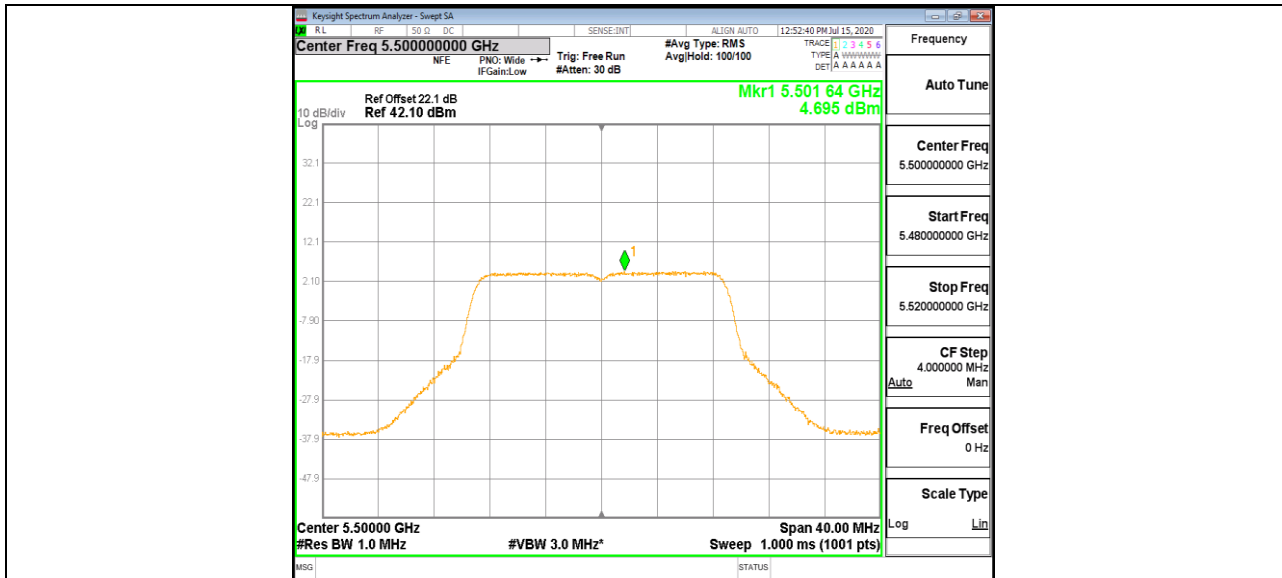
11N20MIMO\_Ant1\_5320



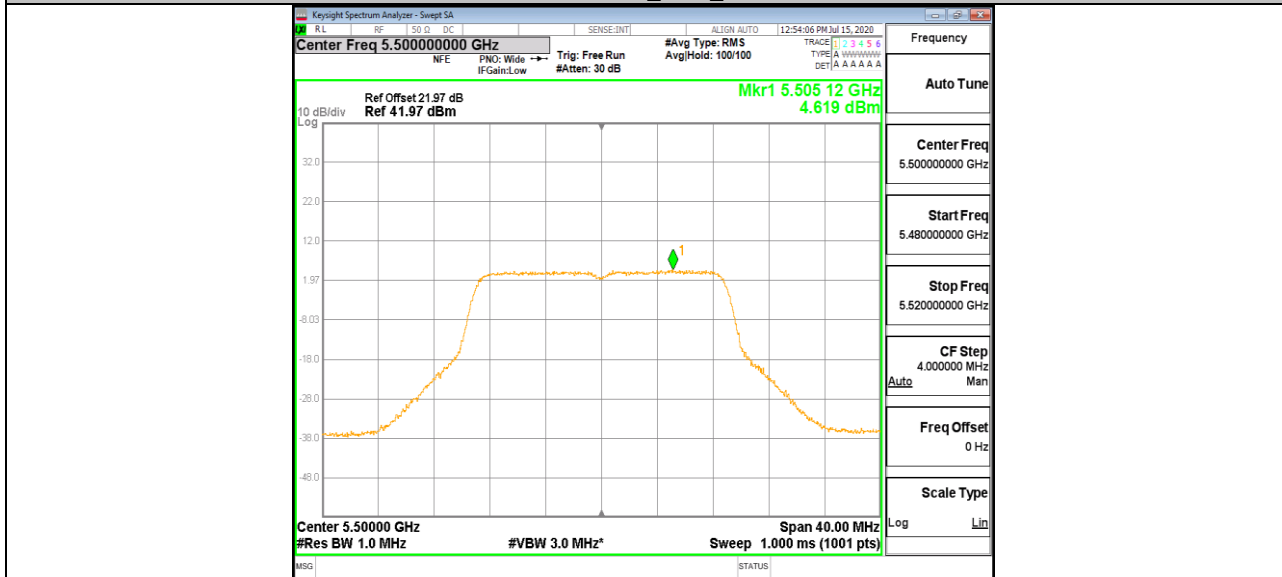
11N20MIMO\_Ant2\_5320



11N20MIMO\_Ant1\_5500

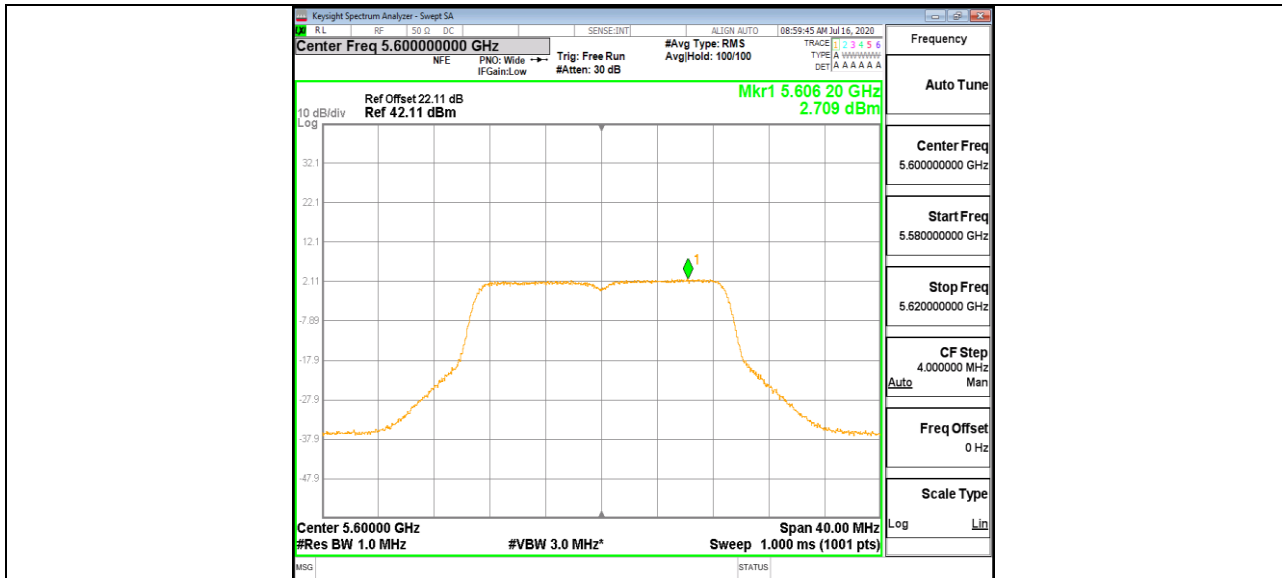


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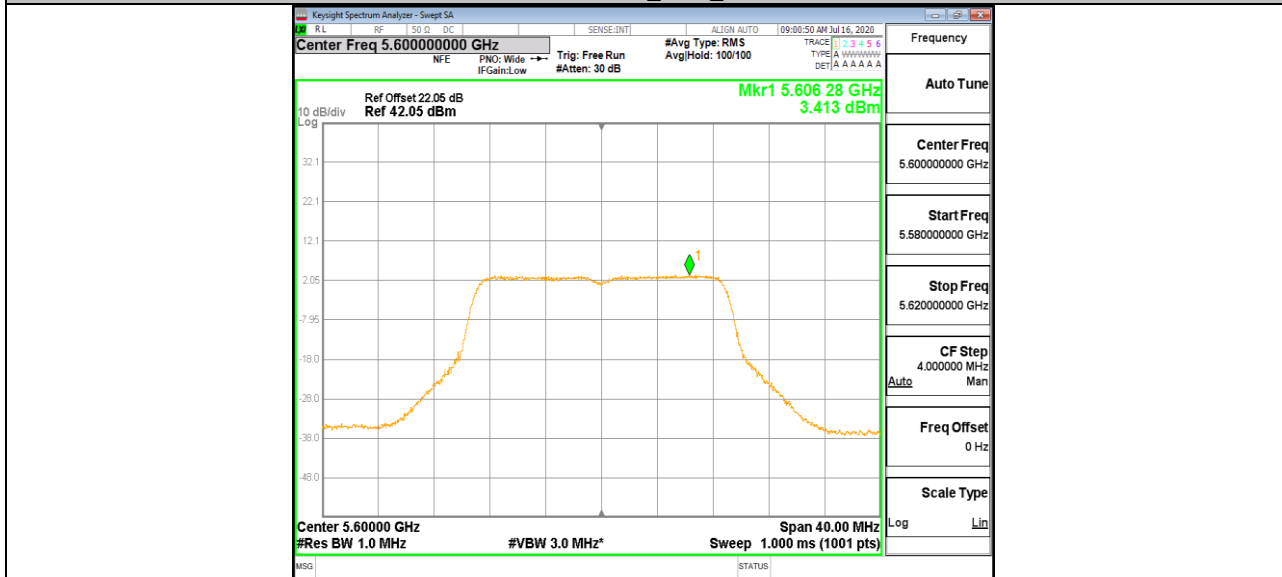


11N20MIMO\_Ant1\_5600

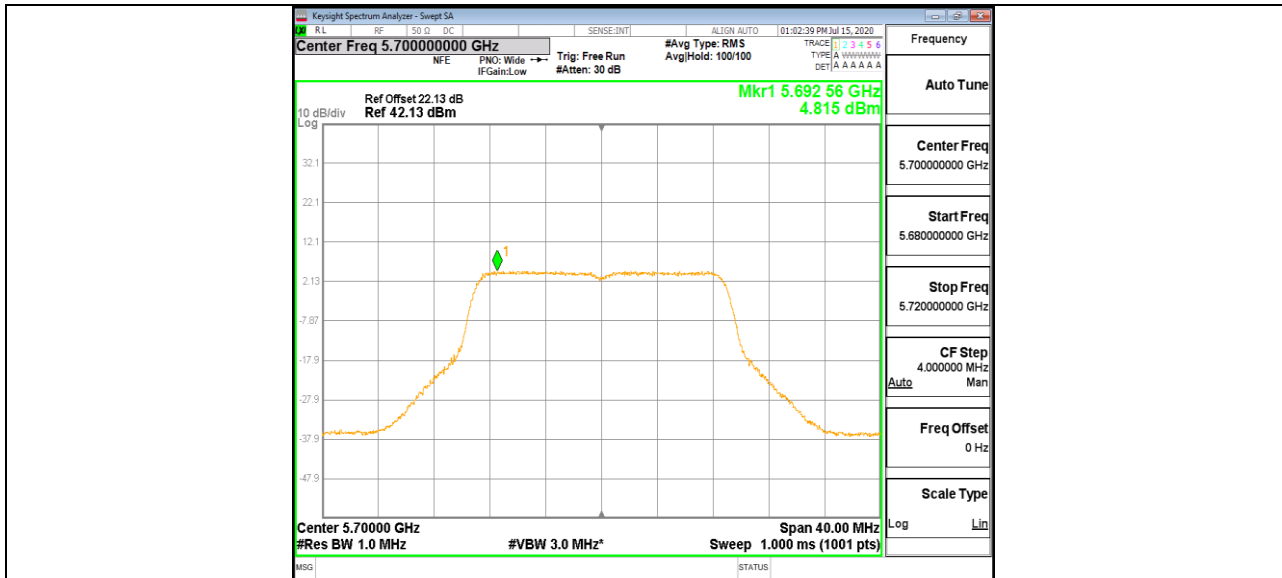




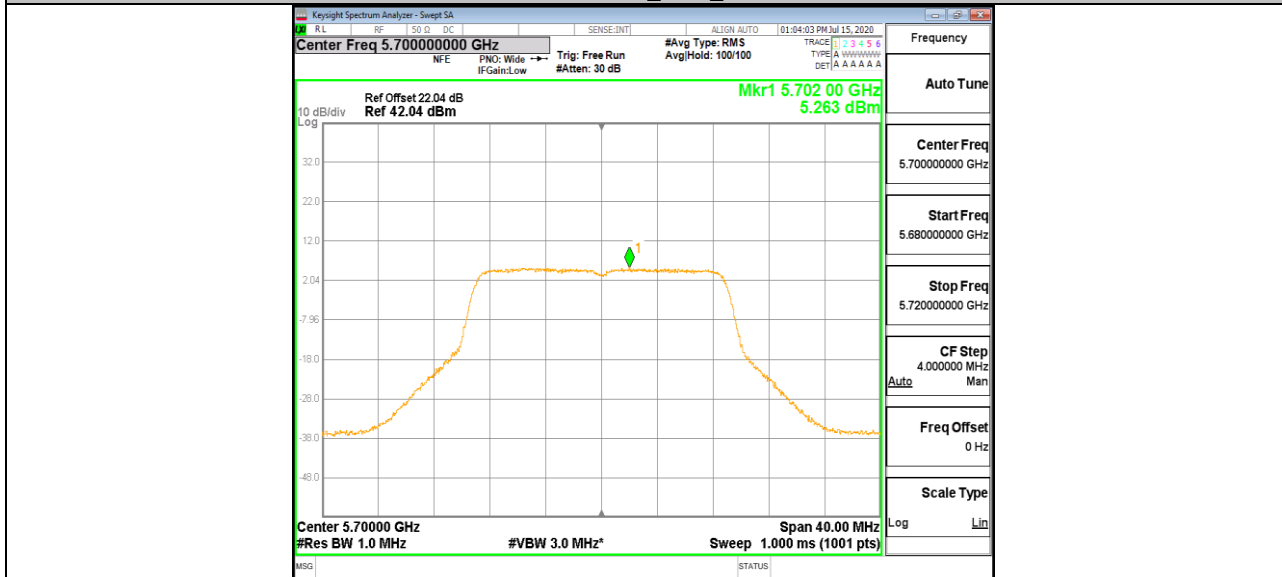
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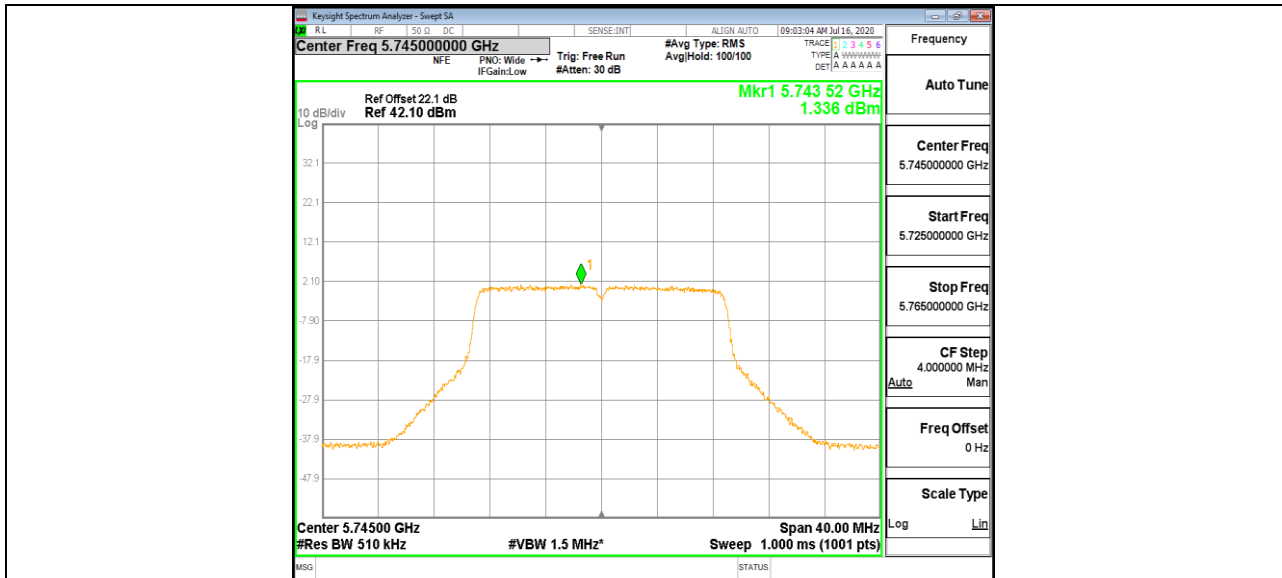
11N20MIMO\_Ant1\_5700



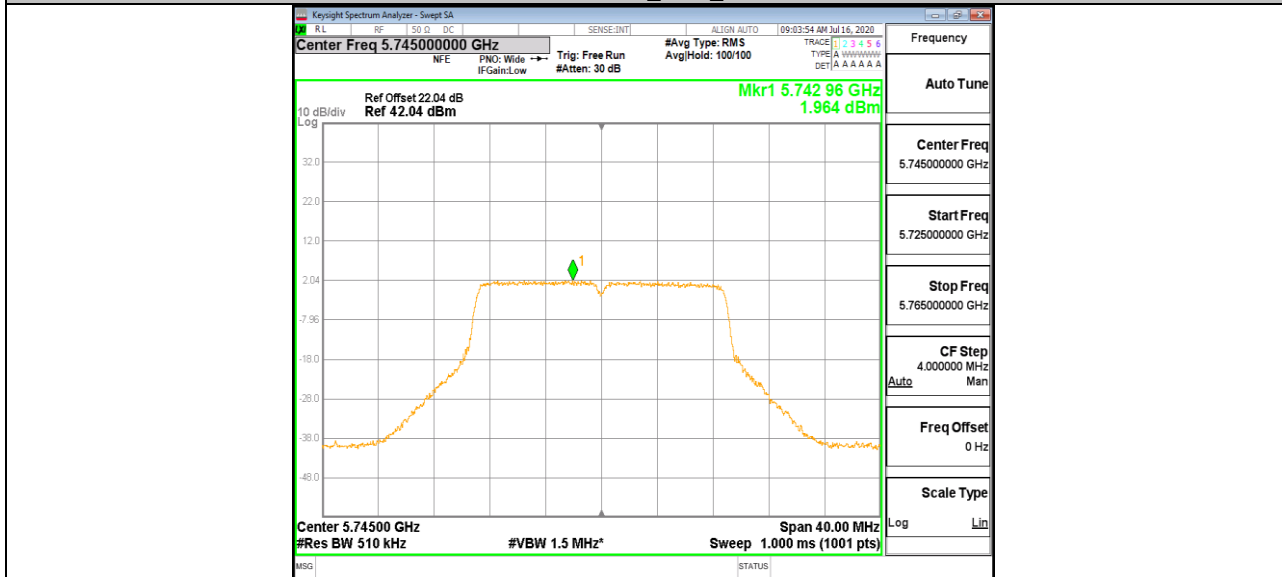
11N20MIMO\_Ant2\_5700



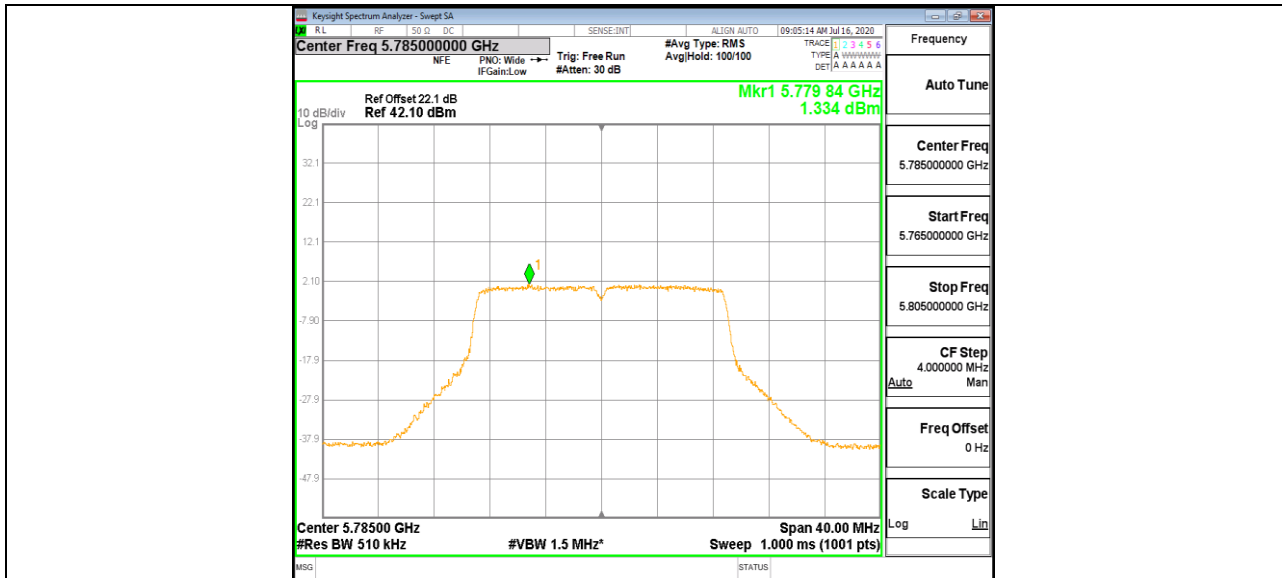
11N20MIMO\_Ant1\_5745



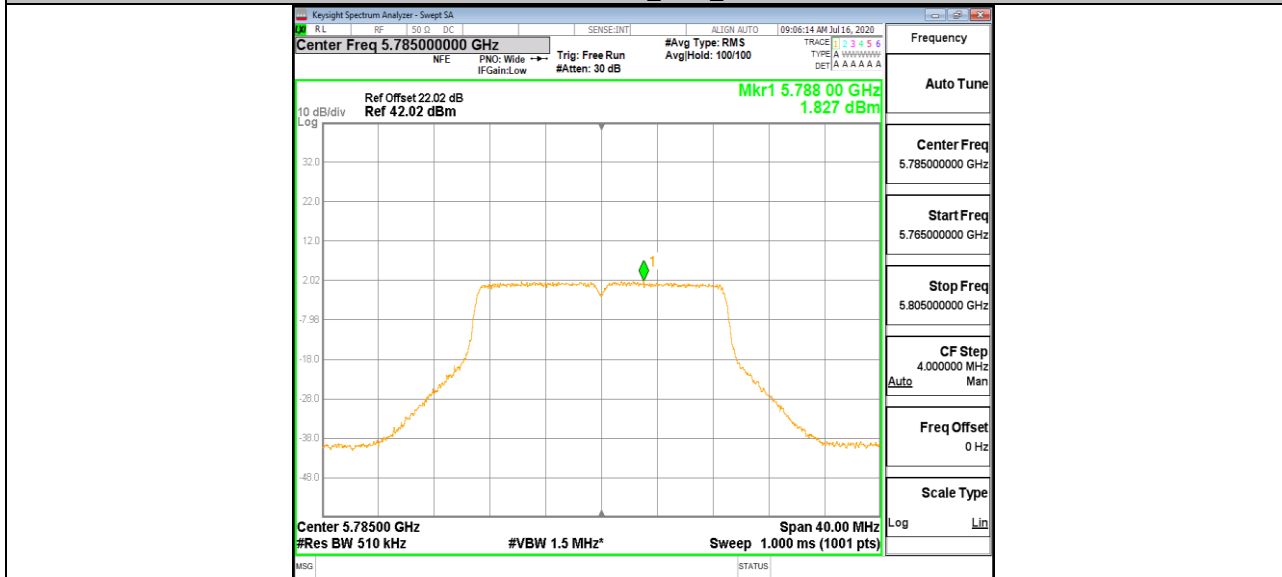
11N20MIMO\_Ant2\_5745



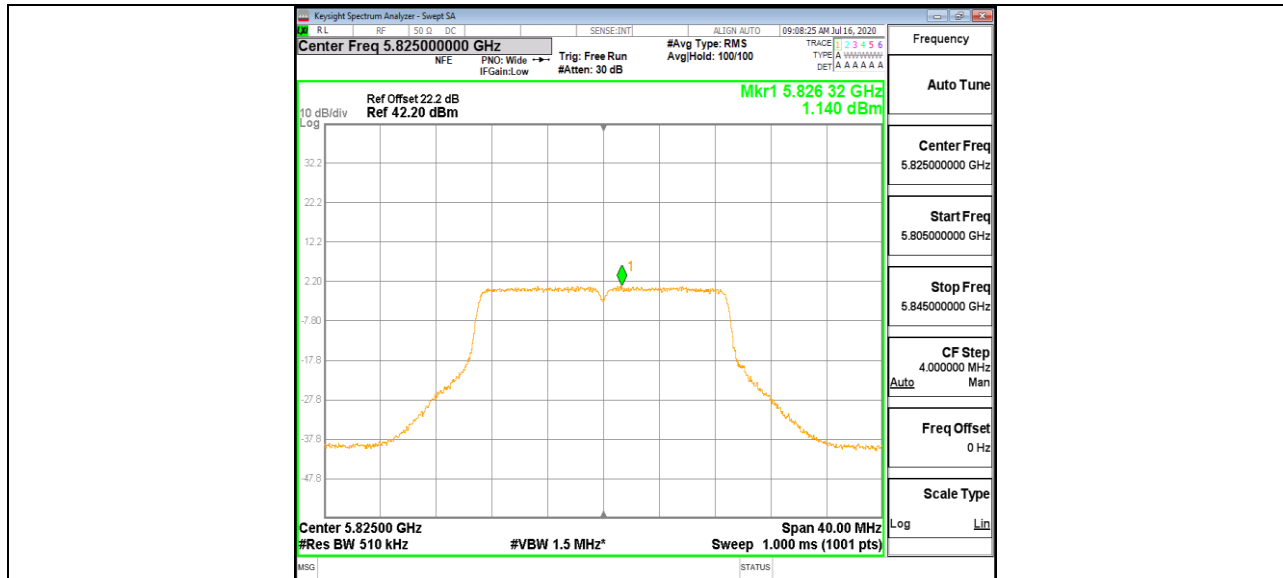
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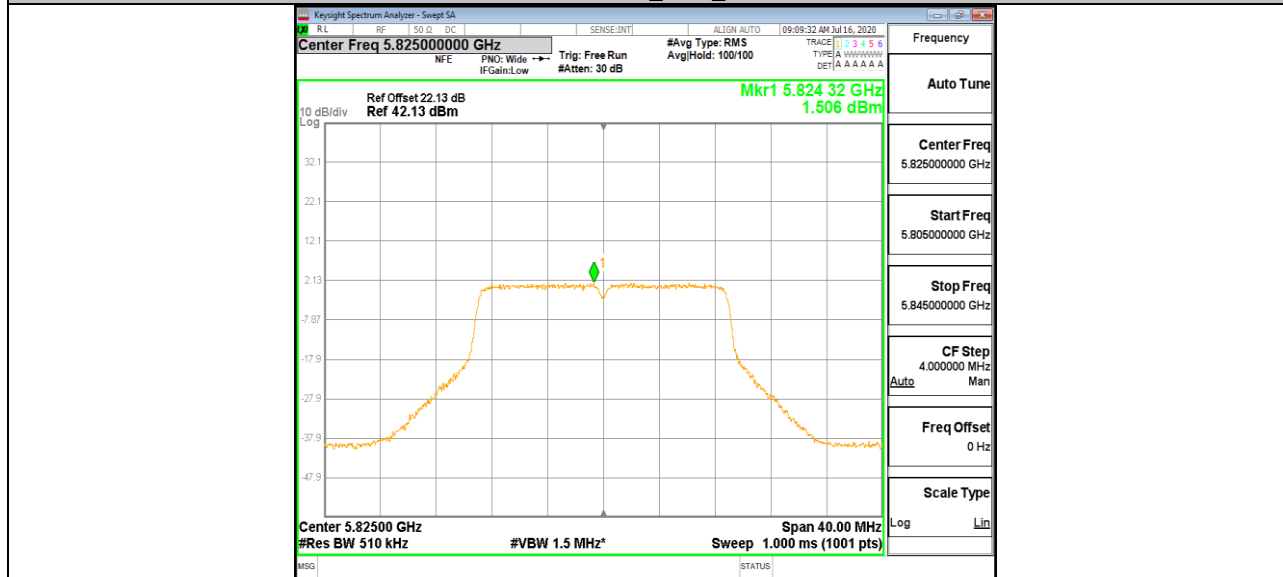
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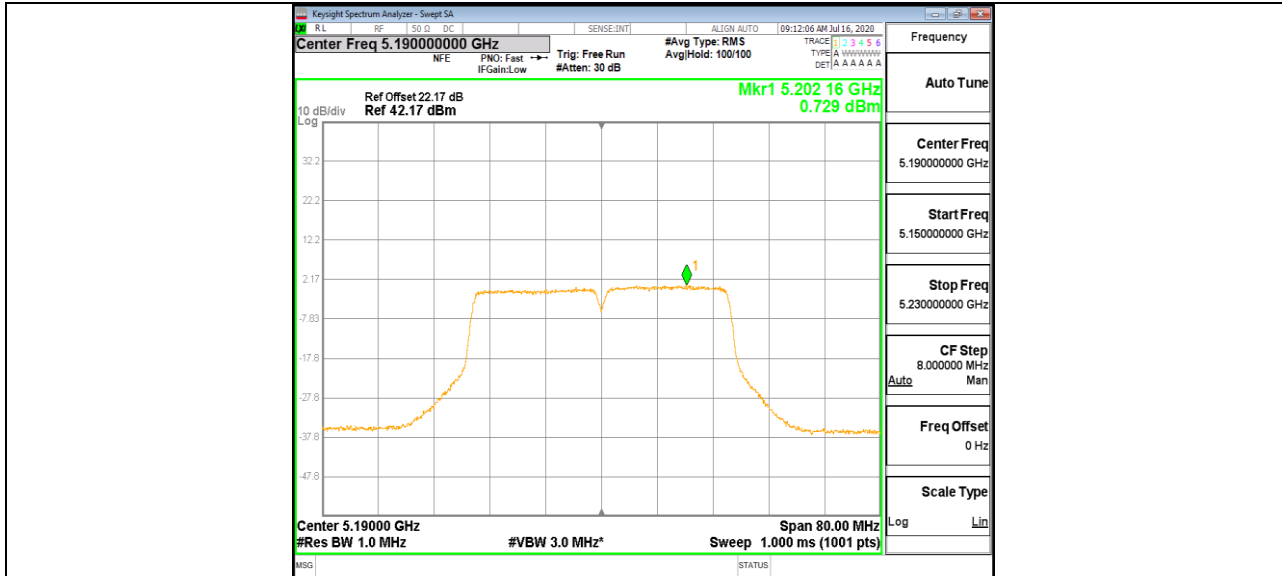
11N20MIMO\_Ant1\_5825



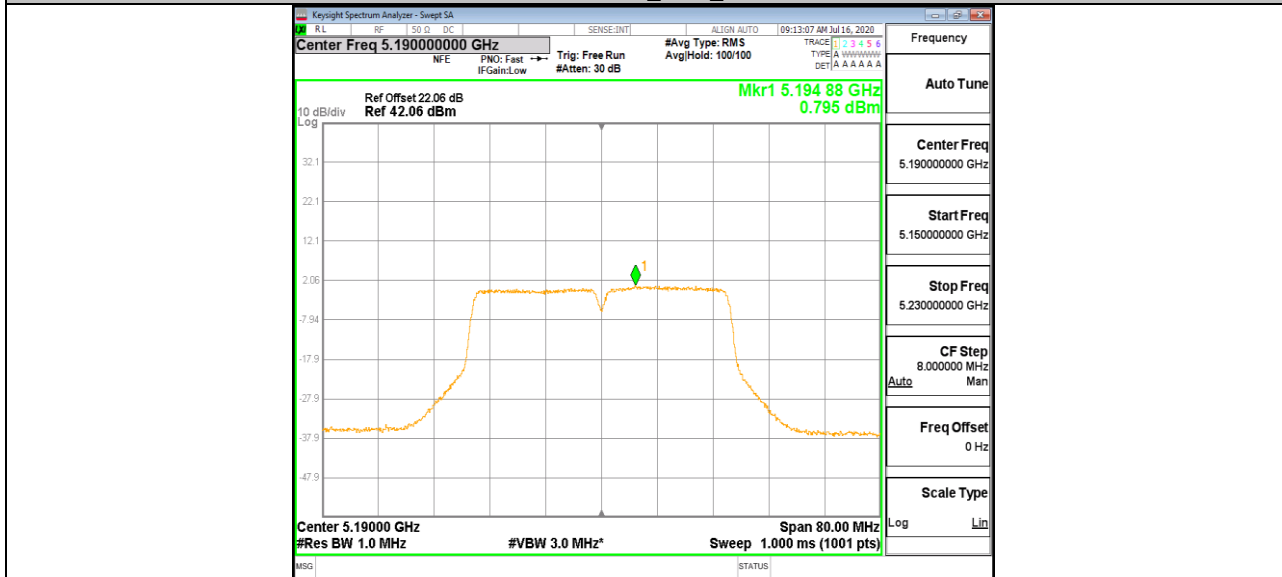
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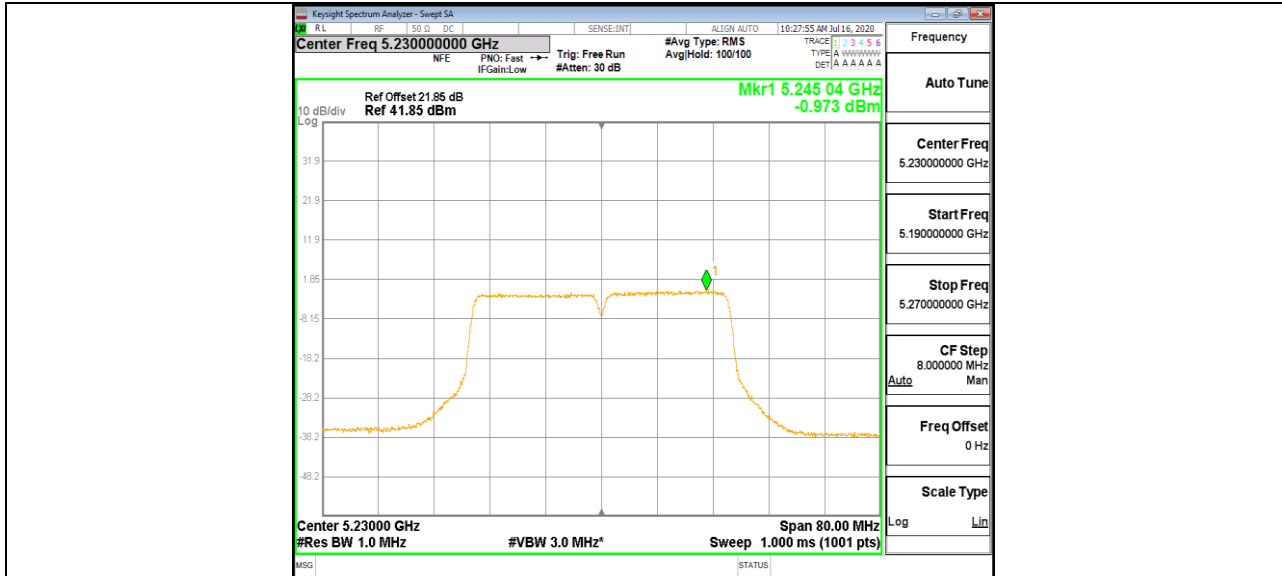
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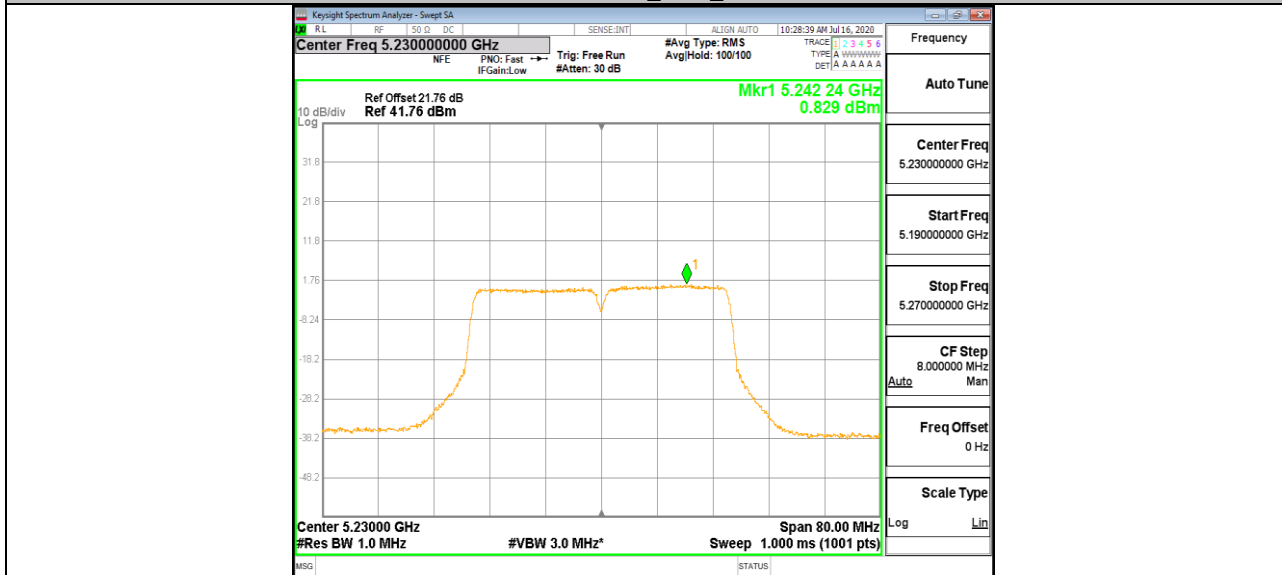
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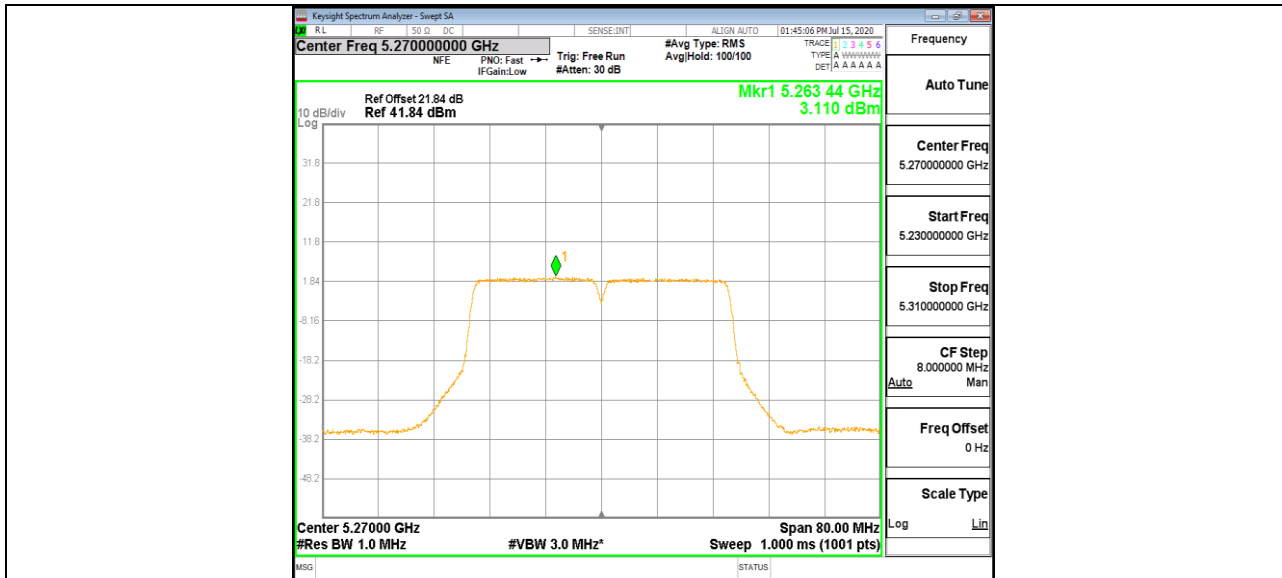
11N40MIMO\_Ant1\_5230



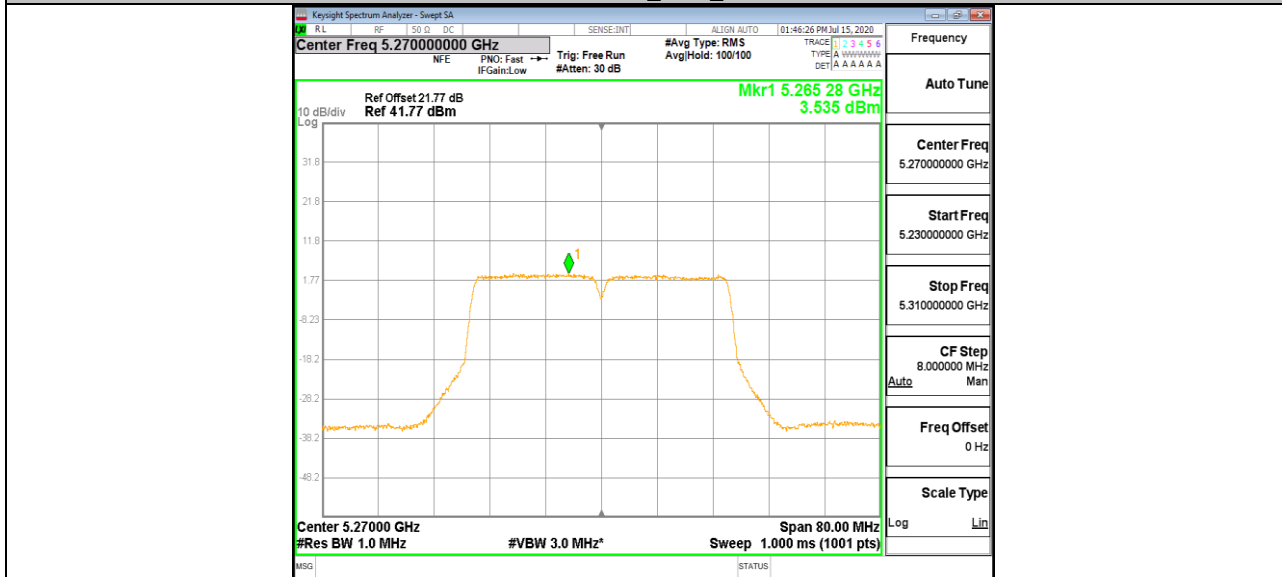
11N40MIMO\_Ant2\_5230



11N40MIMO\_Ant1\_5270

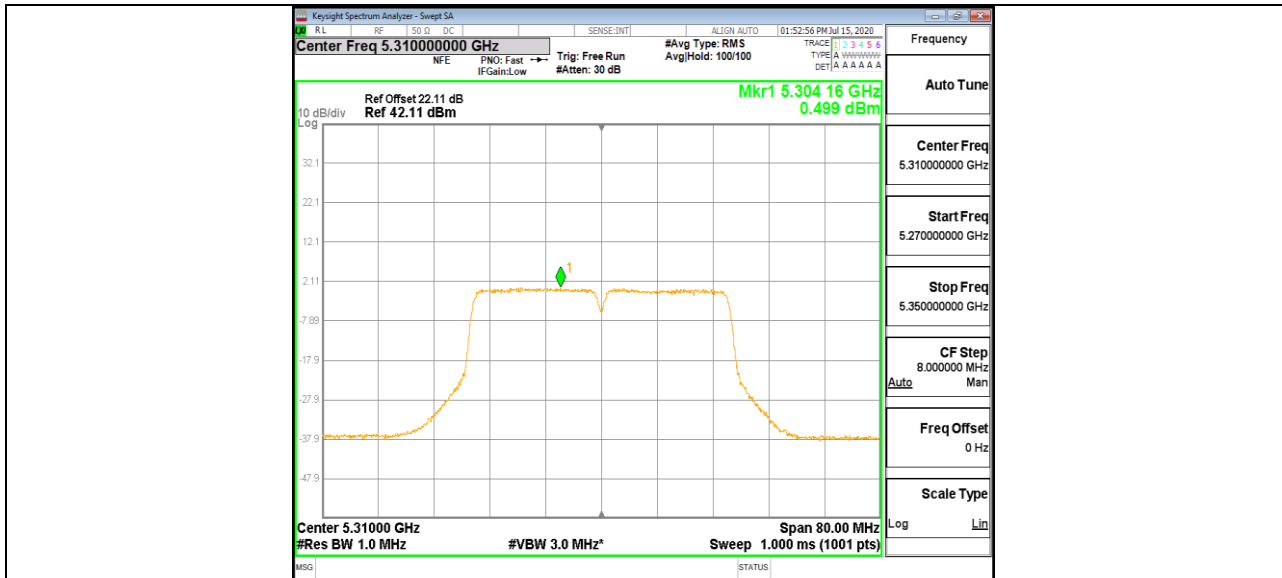


11N40MIMO\_Ant2\_5270

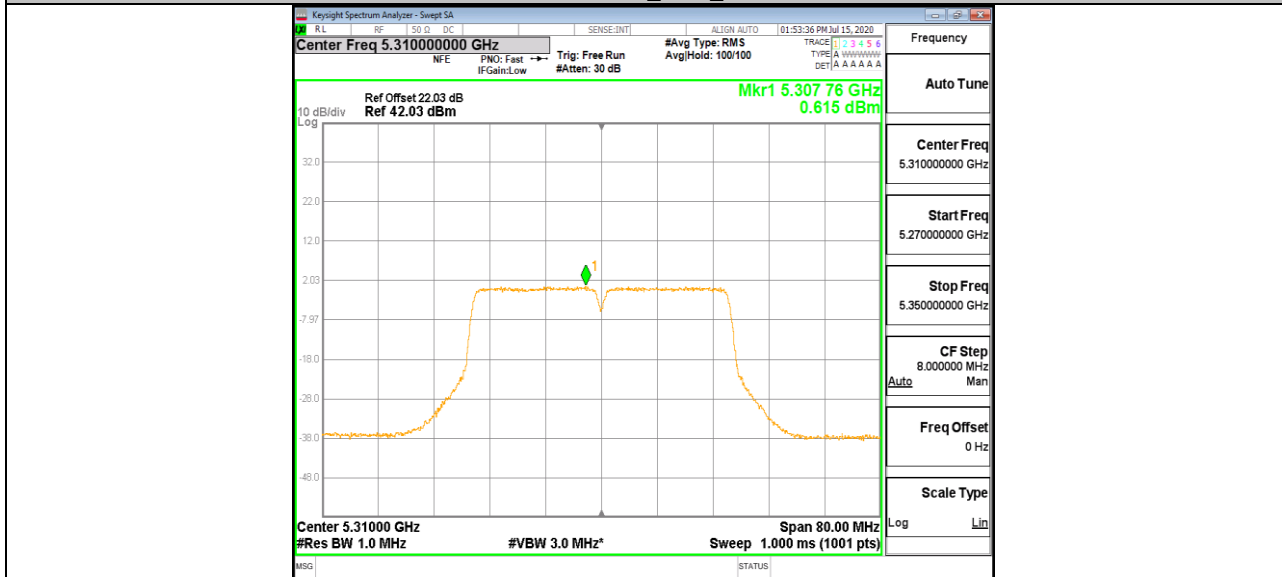


11N40MIMO\_Ant1\_5310

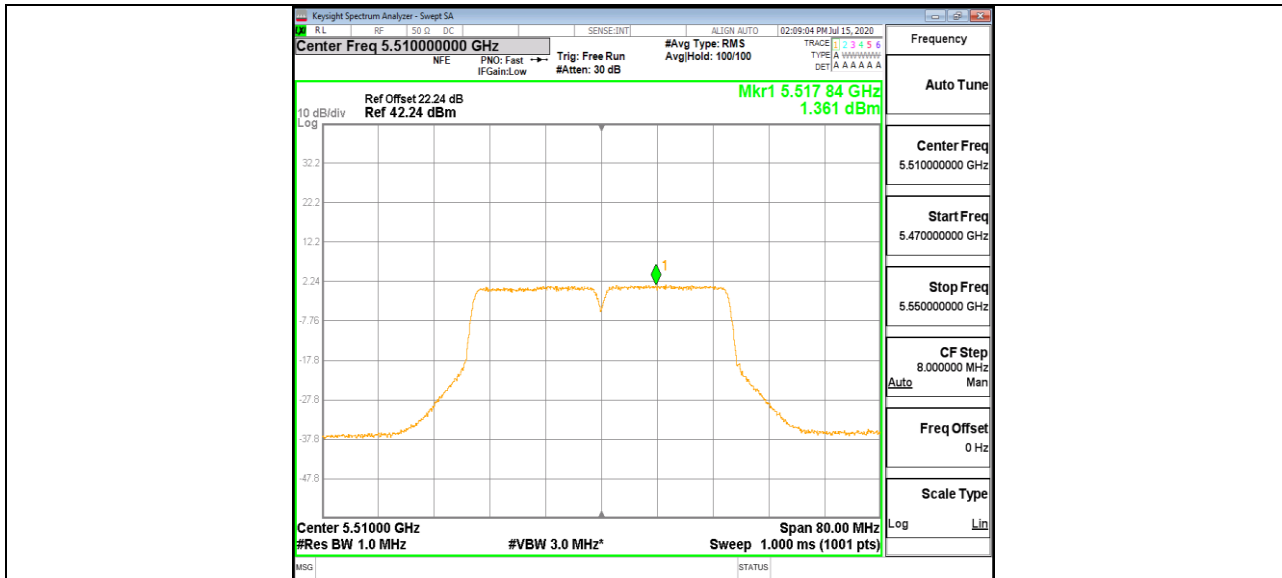




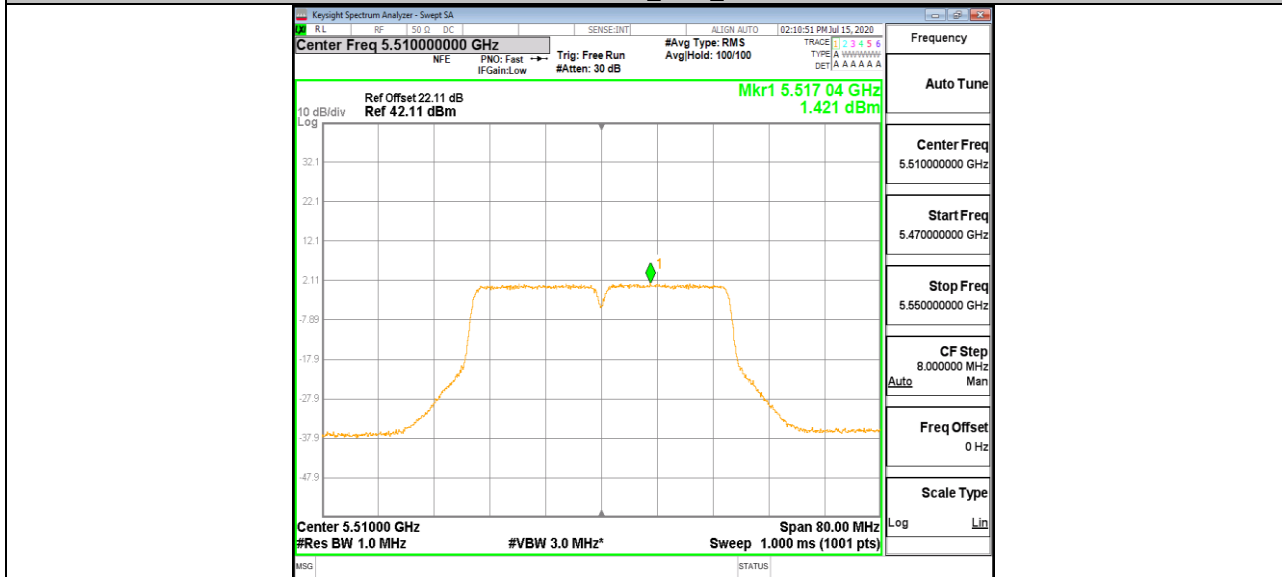
11N40MIMO\_Ant2\_5310



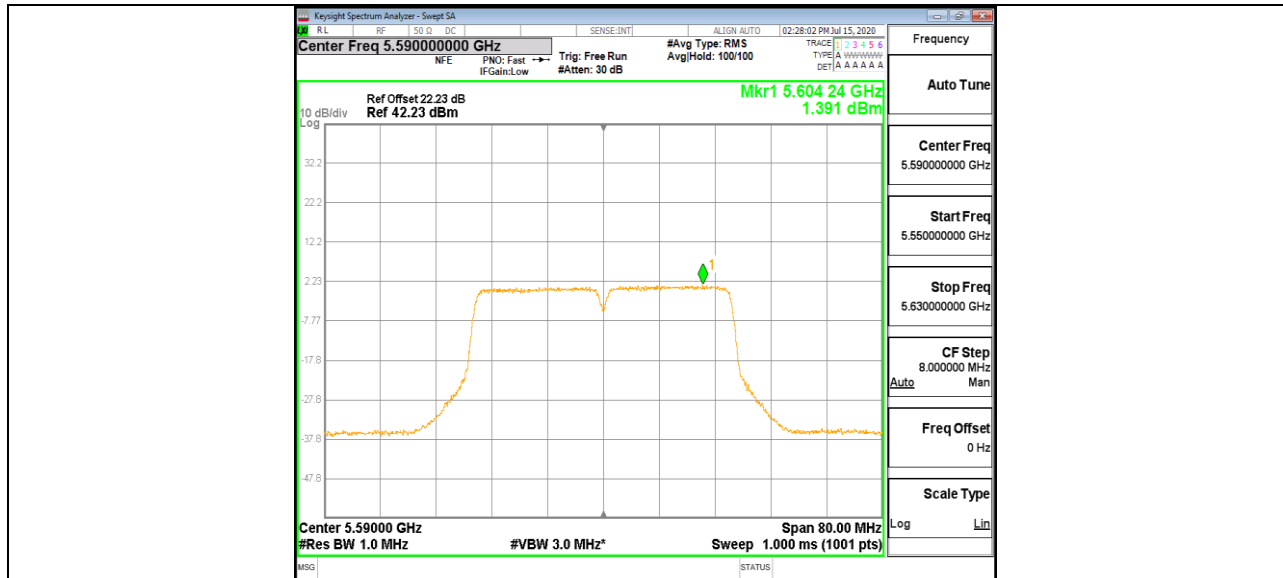
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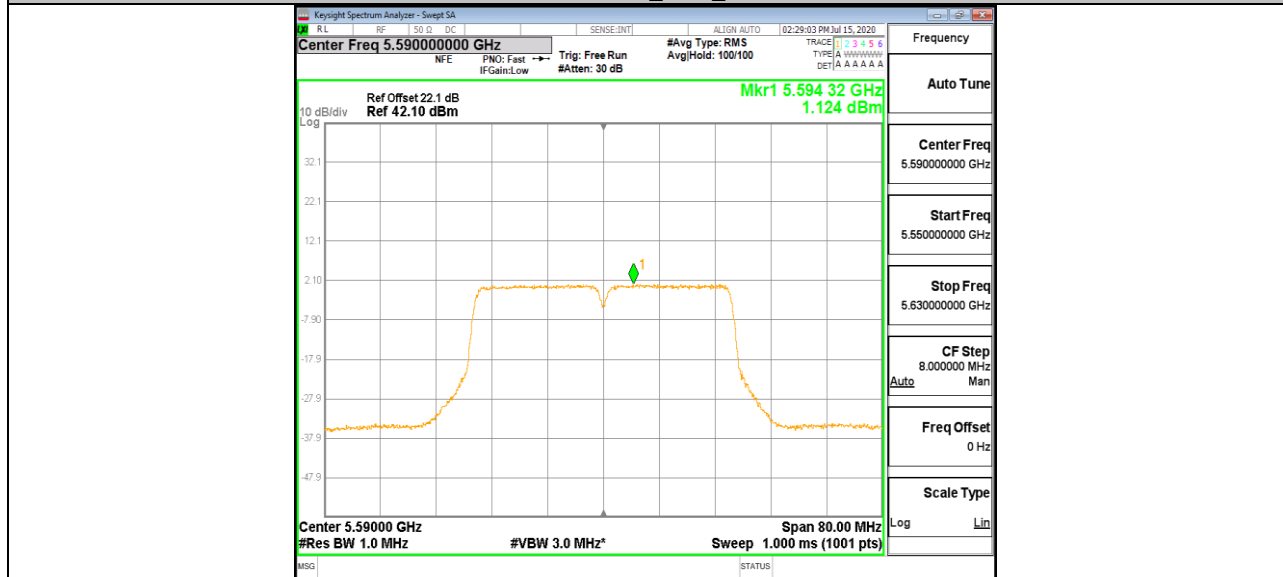
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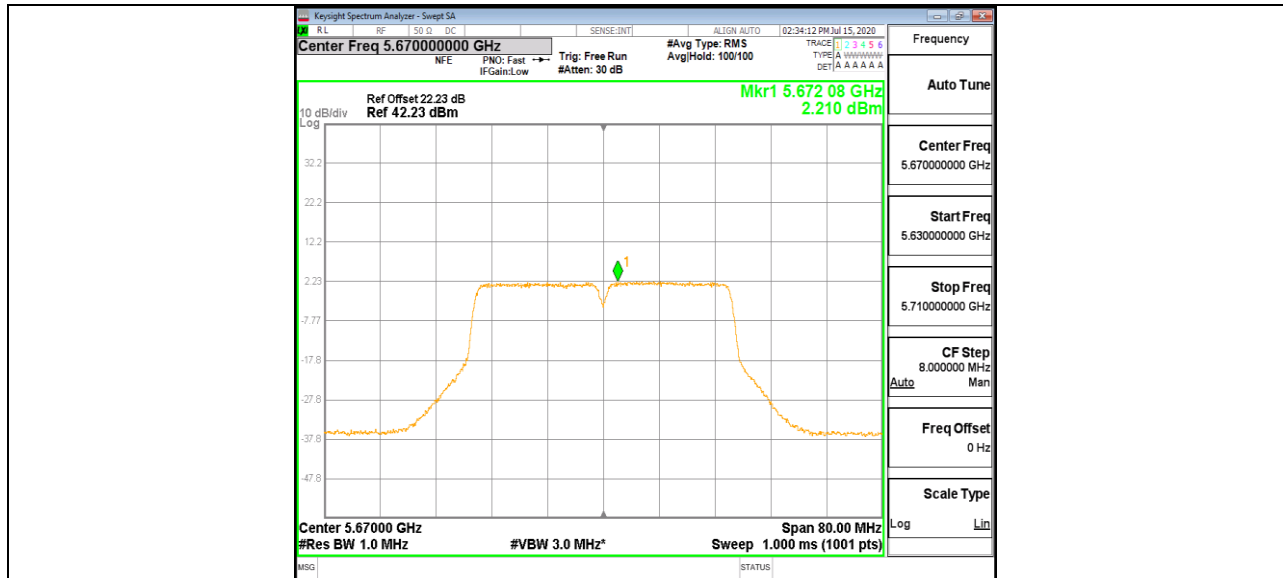
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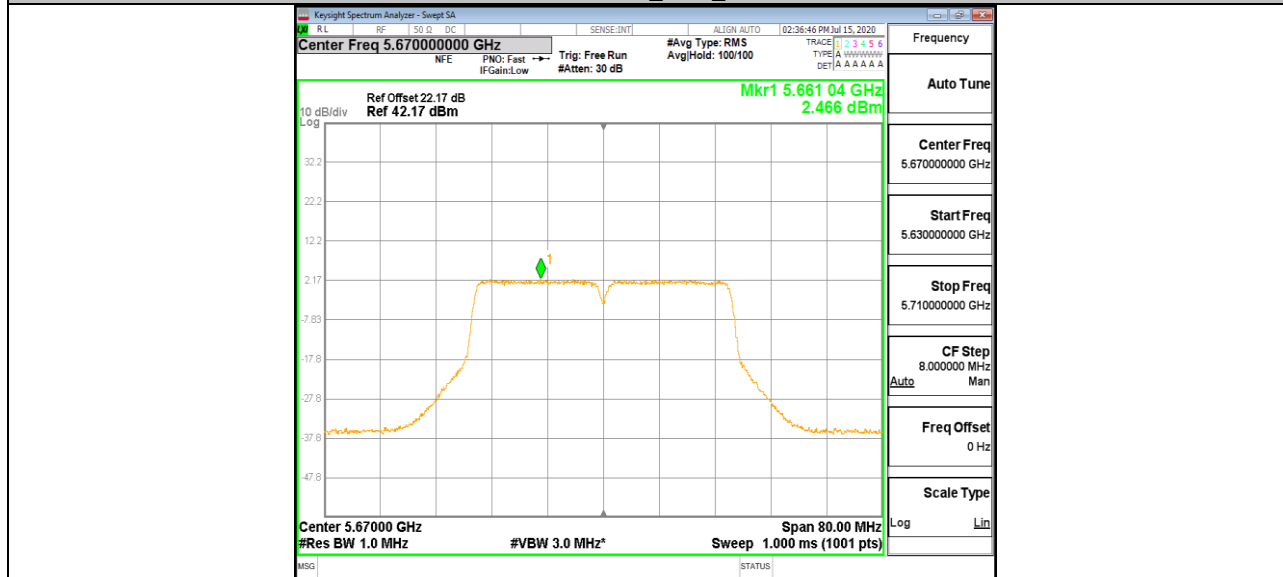
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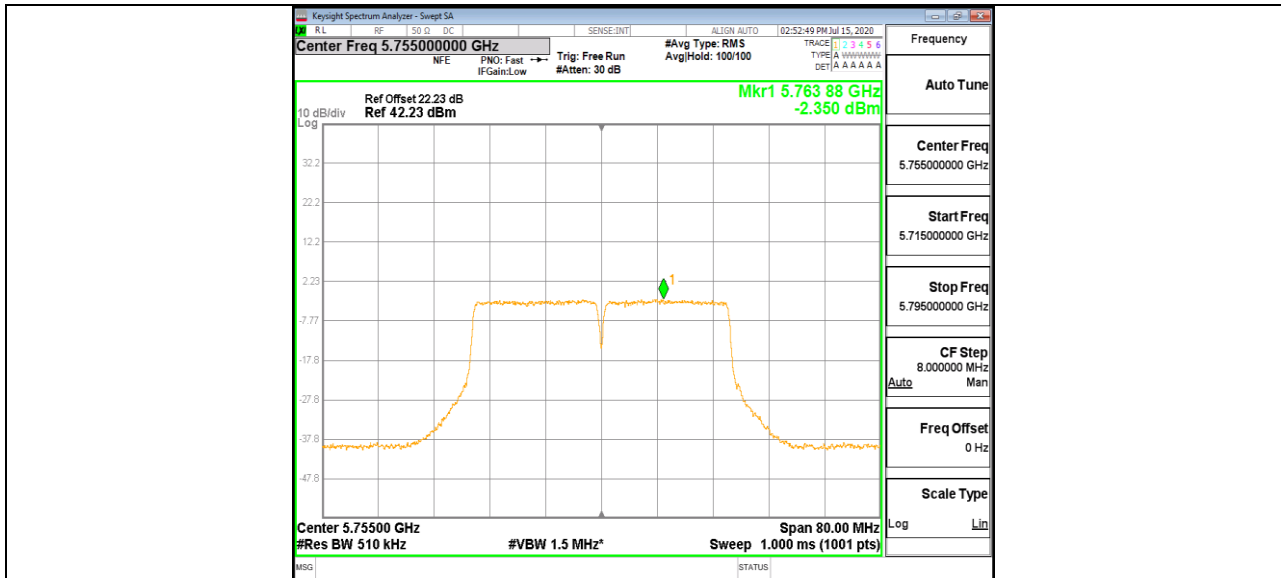
11N40MIMO\_Ant1\_5670



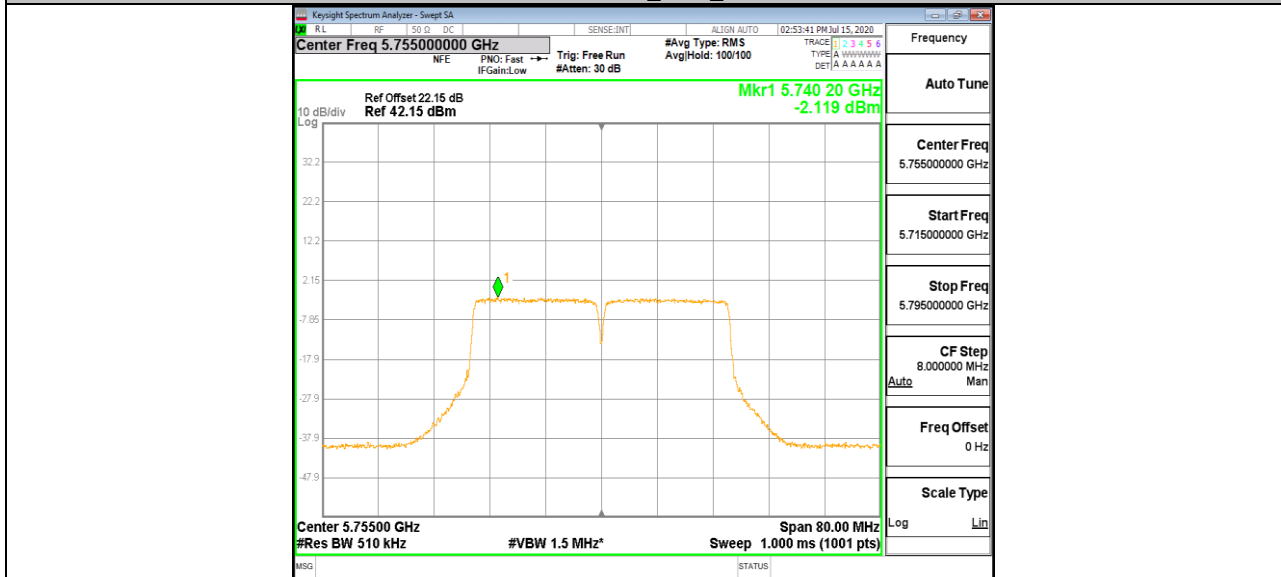
11N40MIMO\_Ant2\_5670



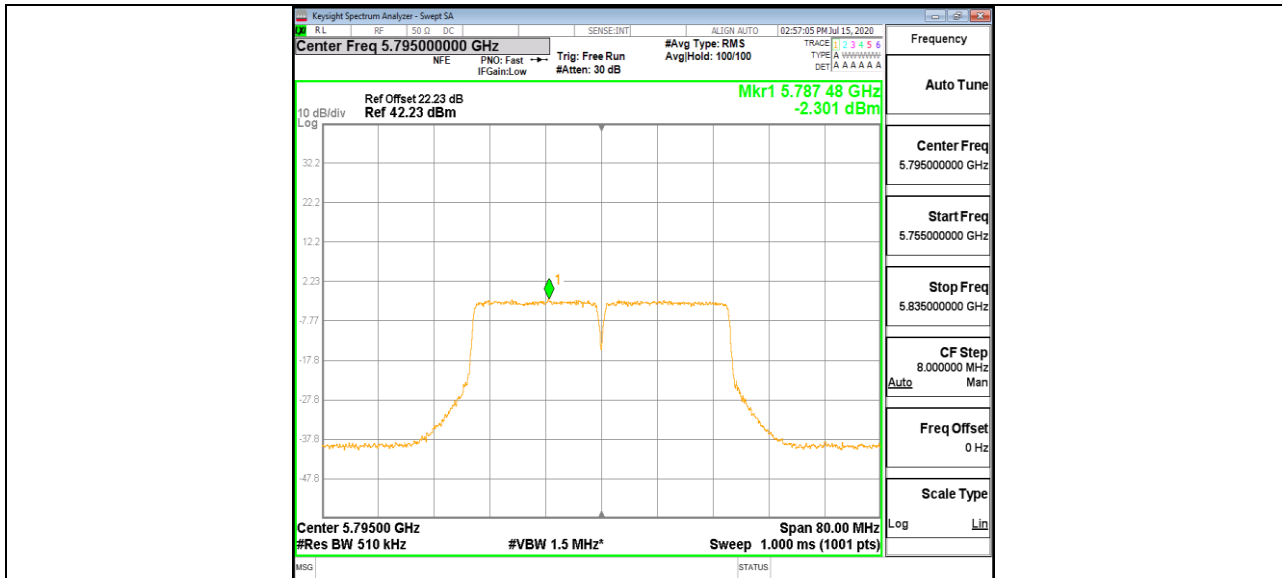
11N40MIMO\_Ant1\_5755



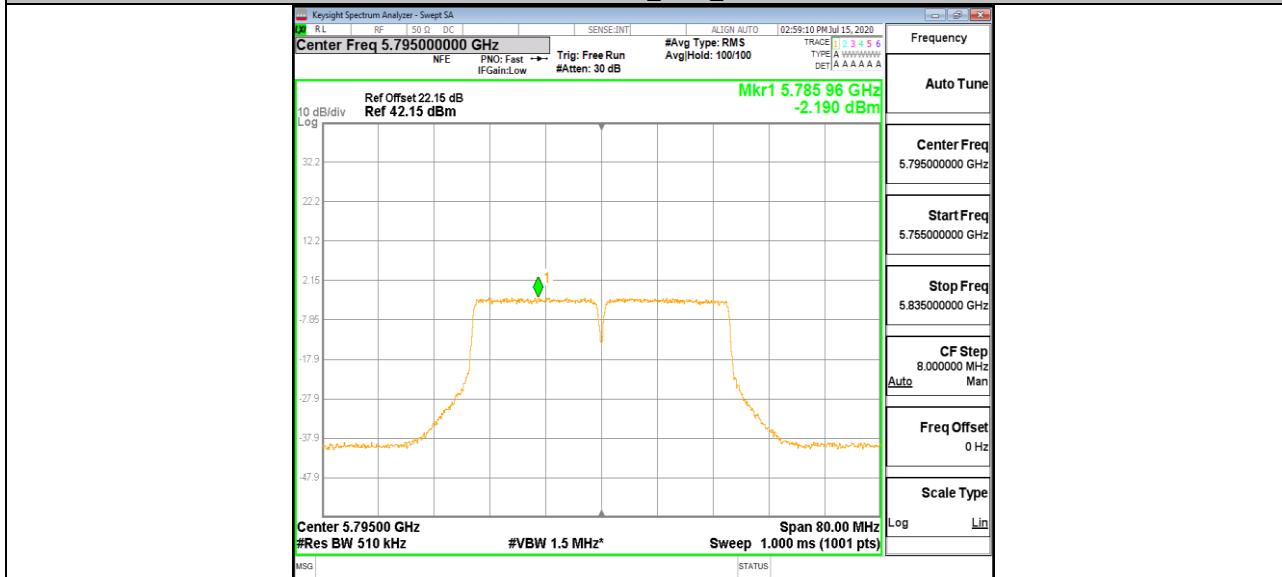
11N40MIMO\_Ant2\_5755



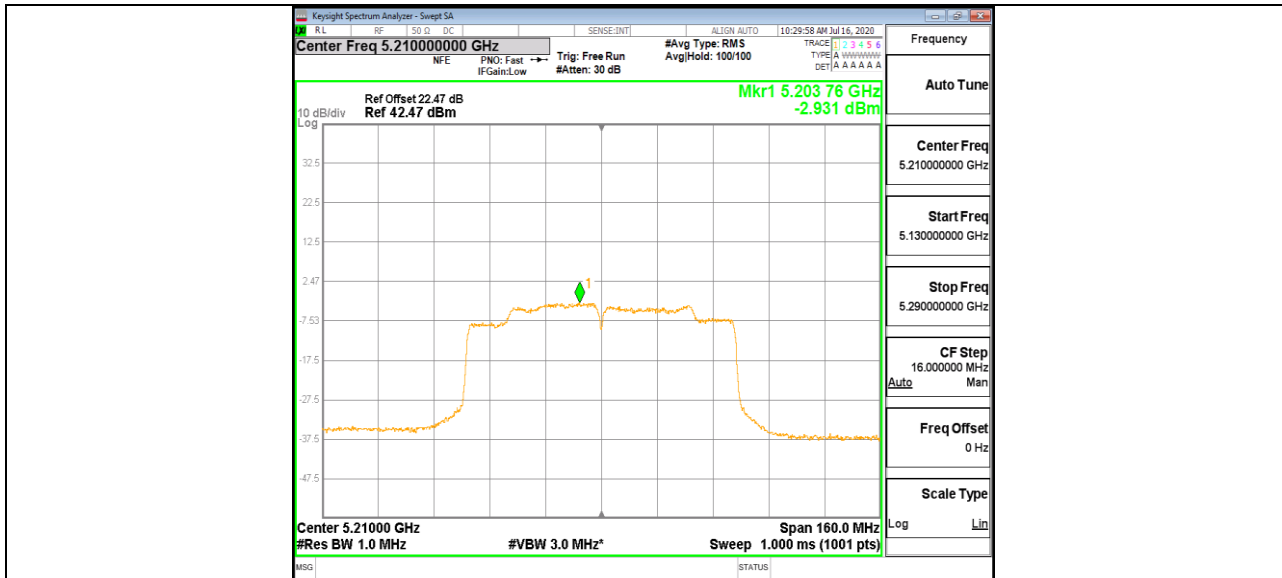
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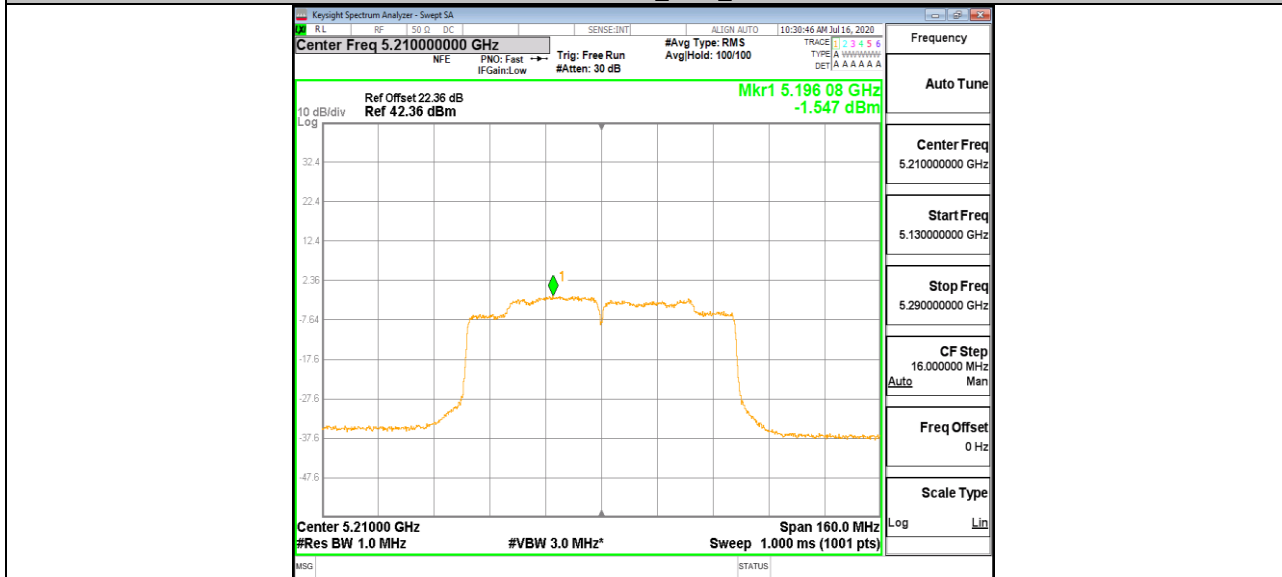
11N40MIMO\_Ant2\_5795



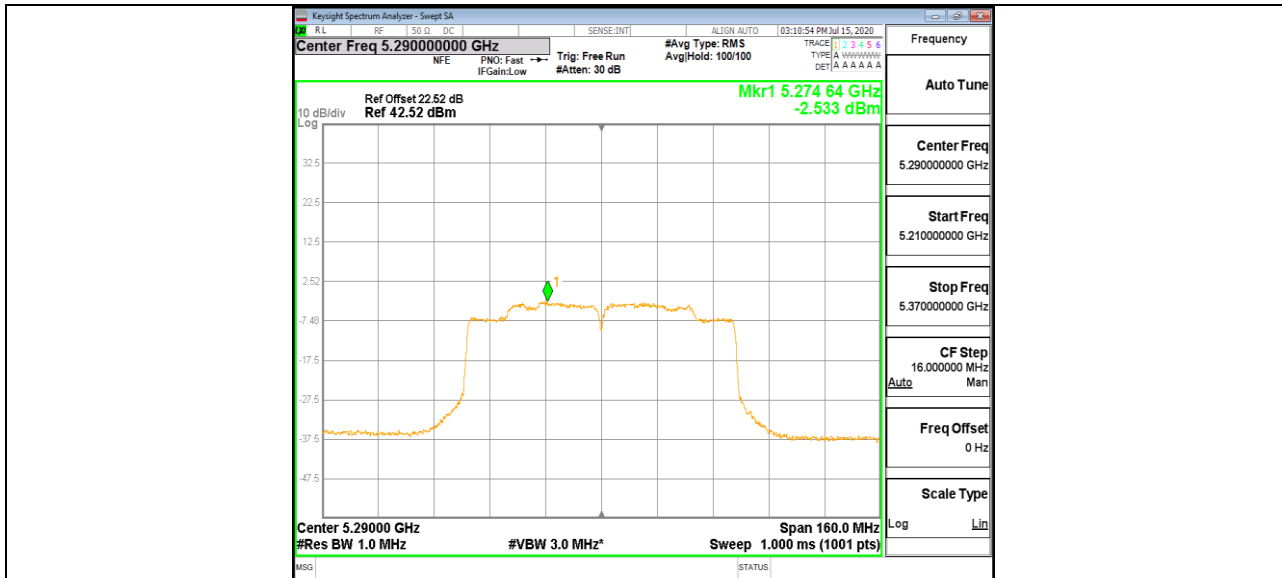
11AC80MIMO\_Ant1\_5210



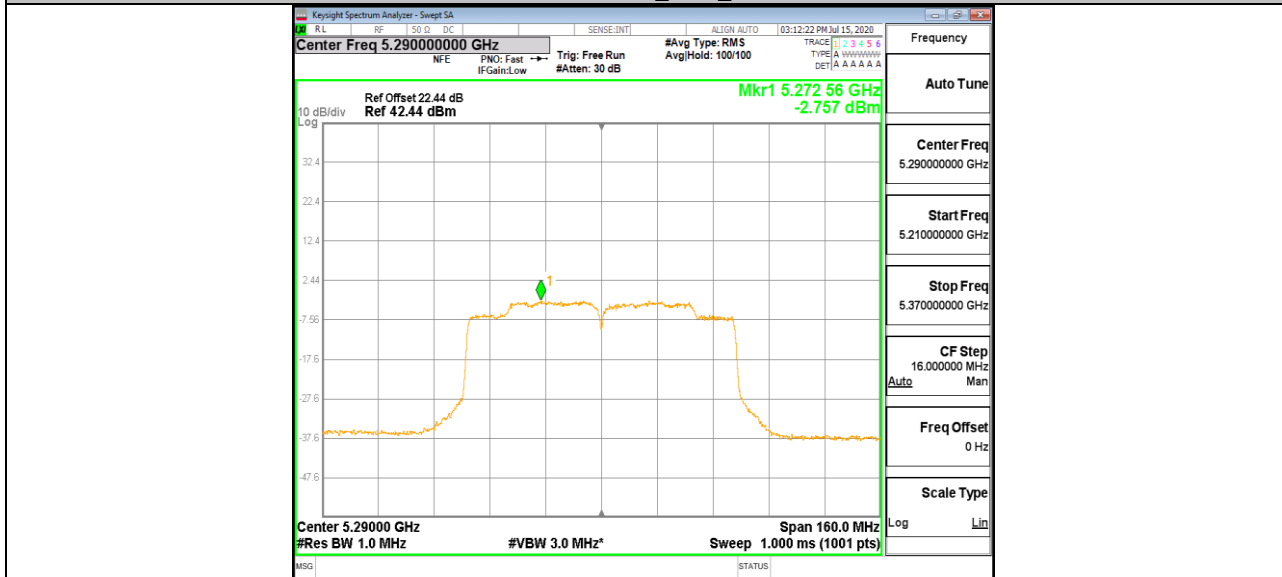
11AC80MIMO\_Ant2\_5210



11AC80MIMO\_Ant1\_5290

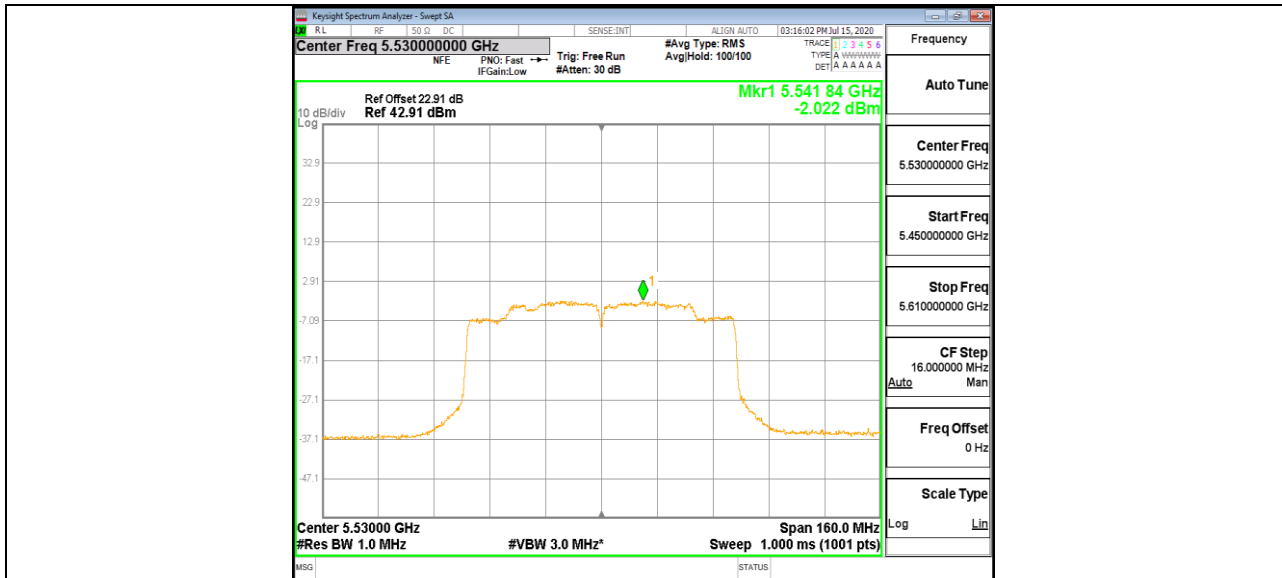


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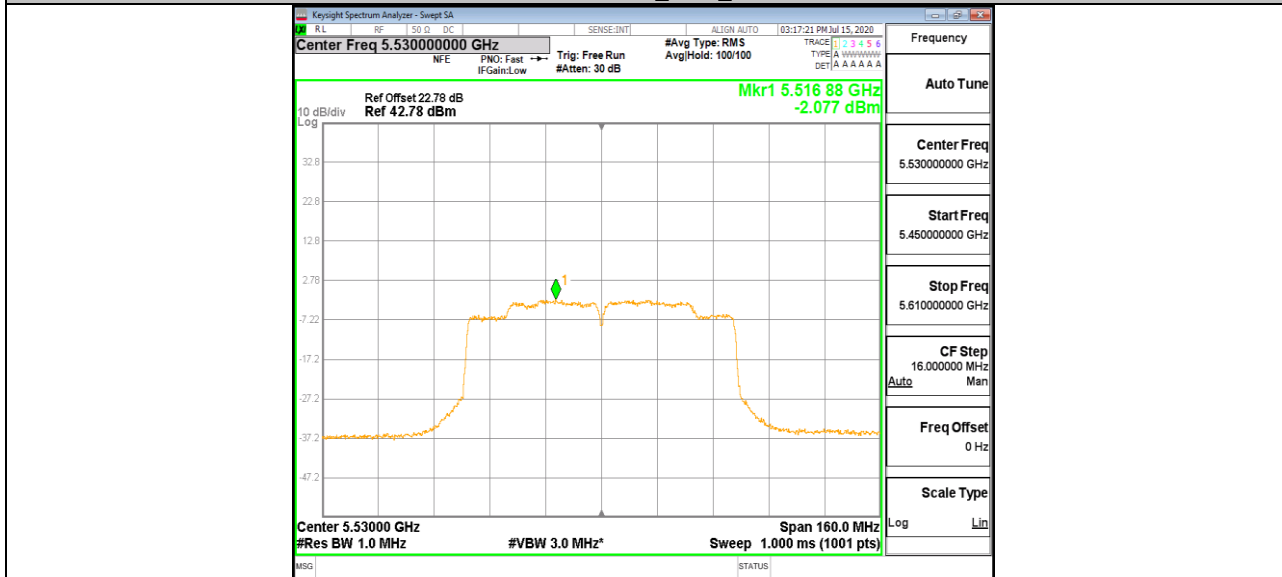


11AC80MIMO\_Ant1\_5530

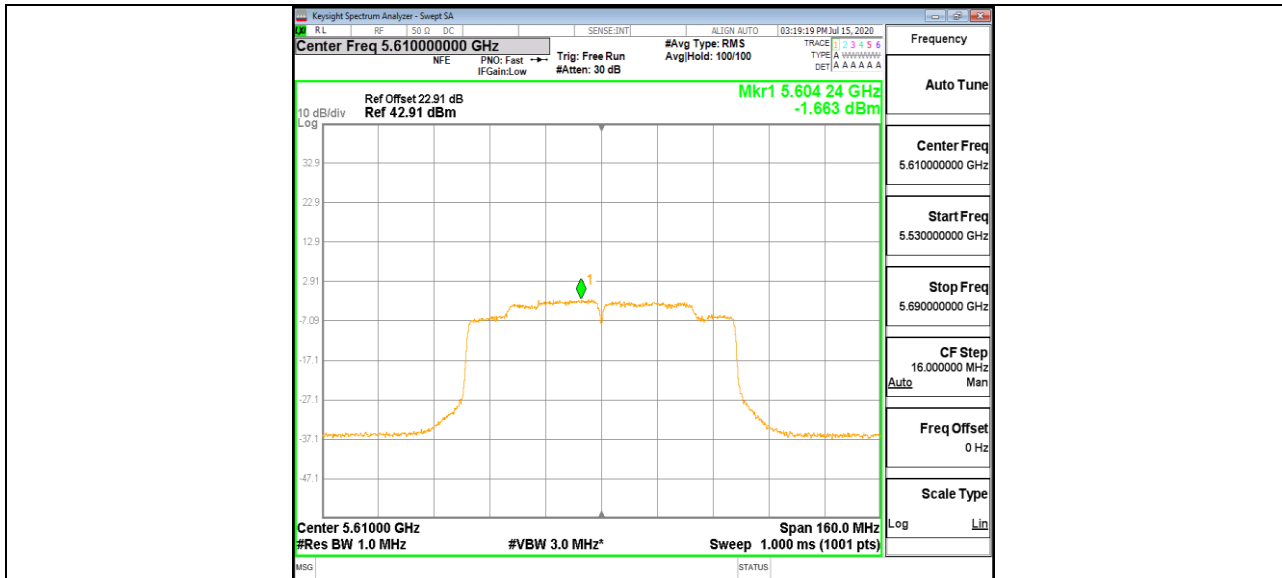




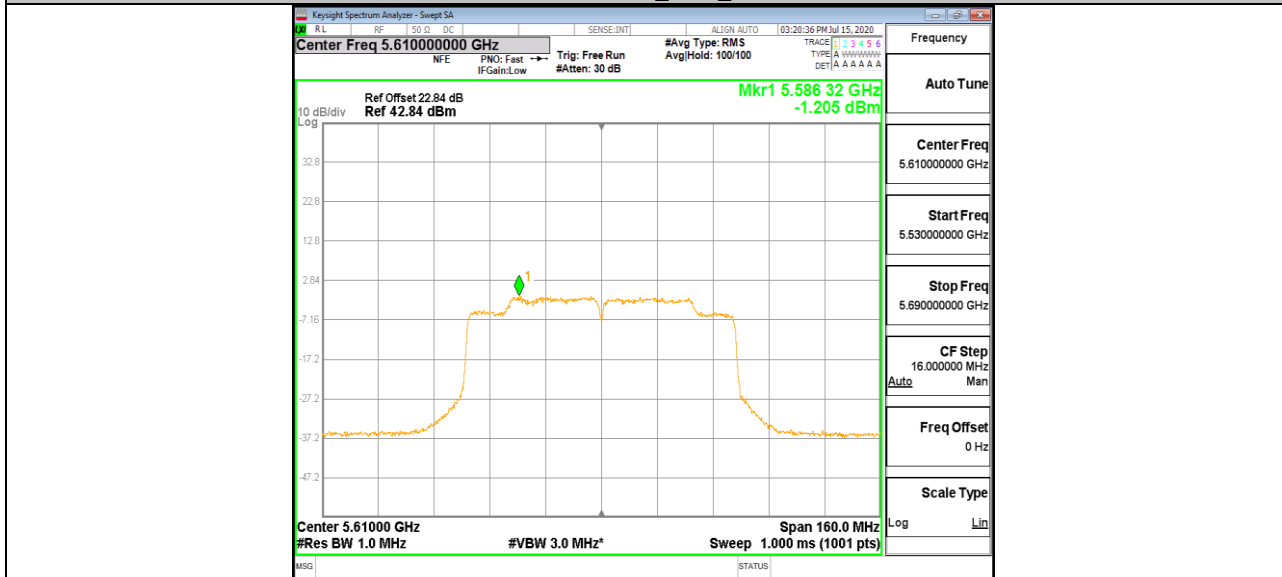
11AC80MIMO\_Ant2\_5530



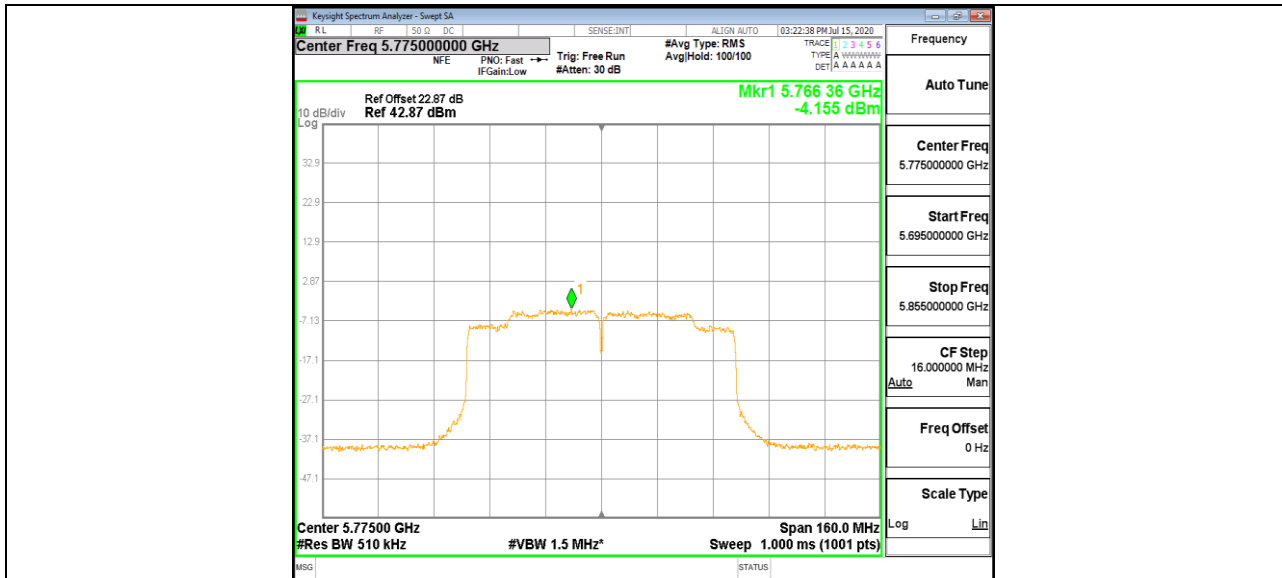
11AC80MIMO\_Ant1\_5610



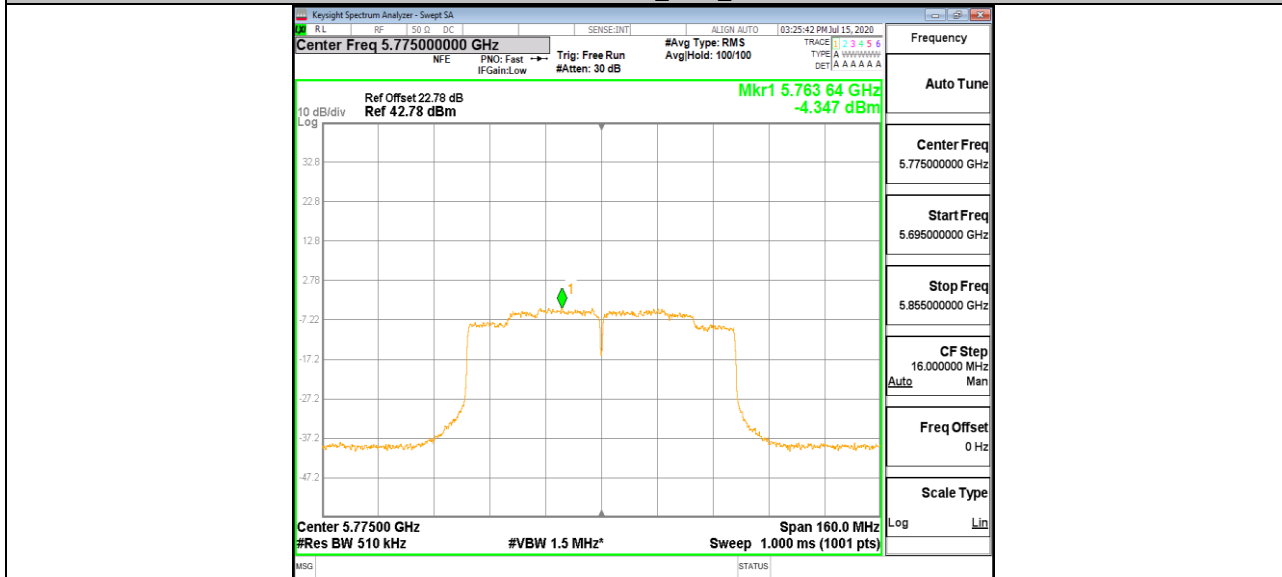
11AC80MIMO\_Ant2\_5610



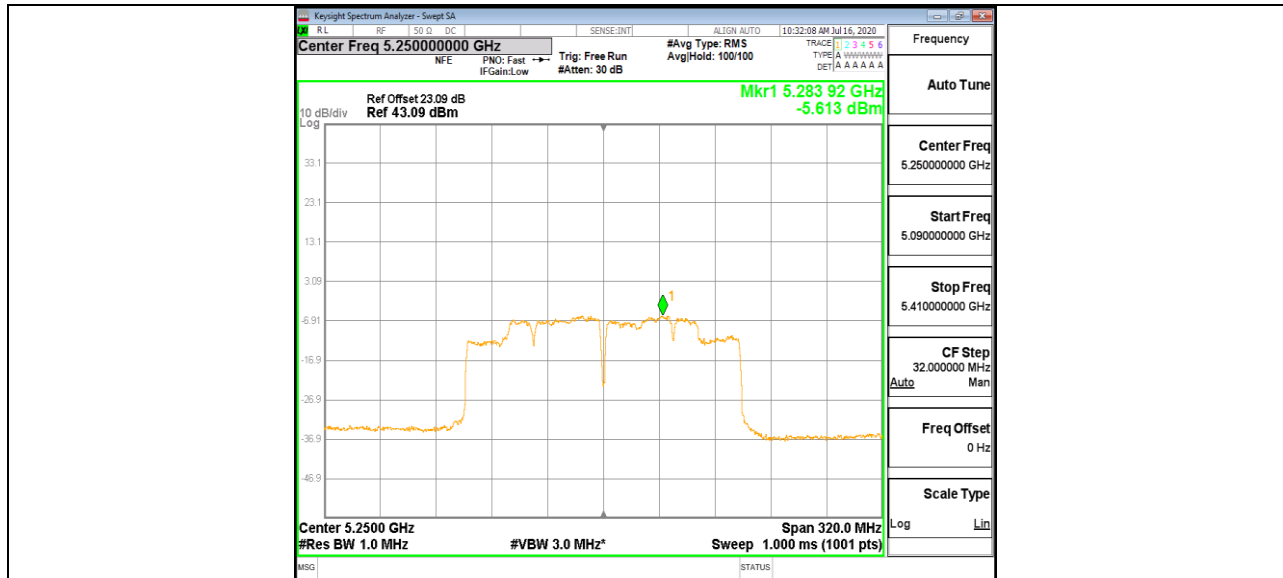
11AC80MIMO\_Ant1\_5775



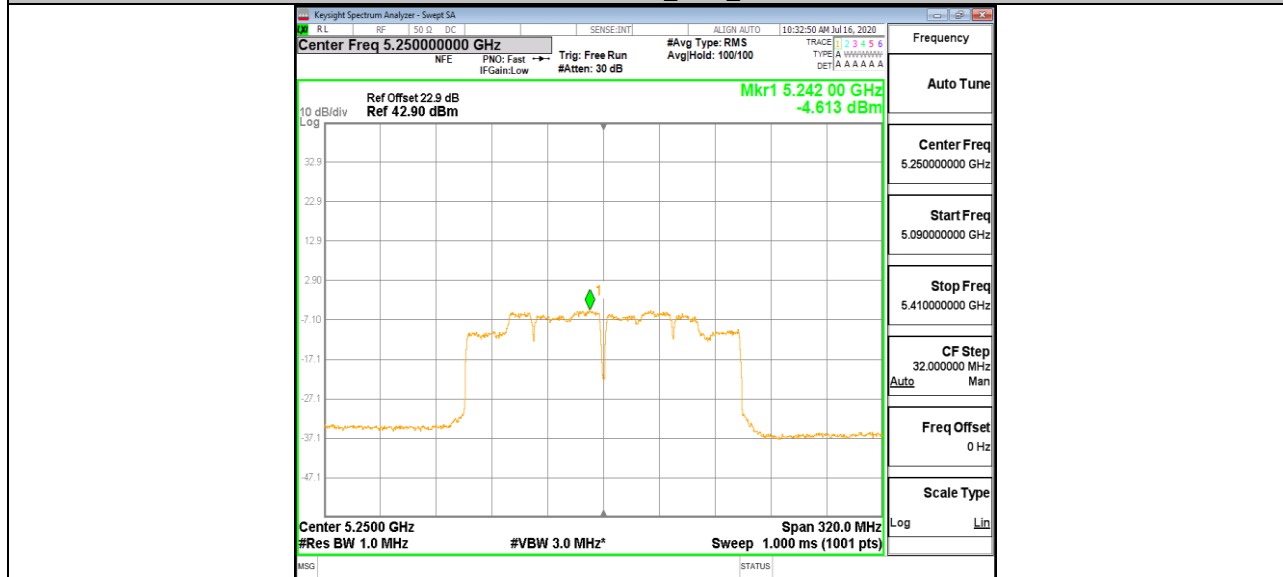
11AC80MIMO\_Ant2\_5775



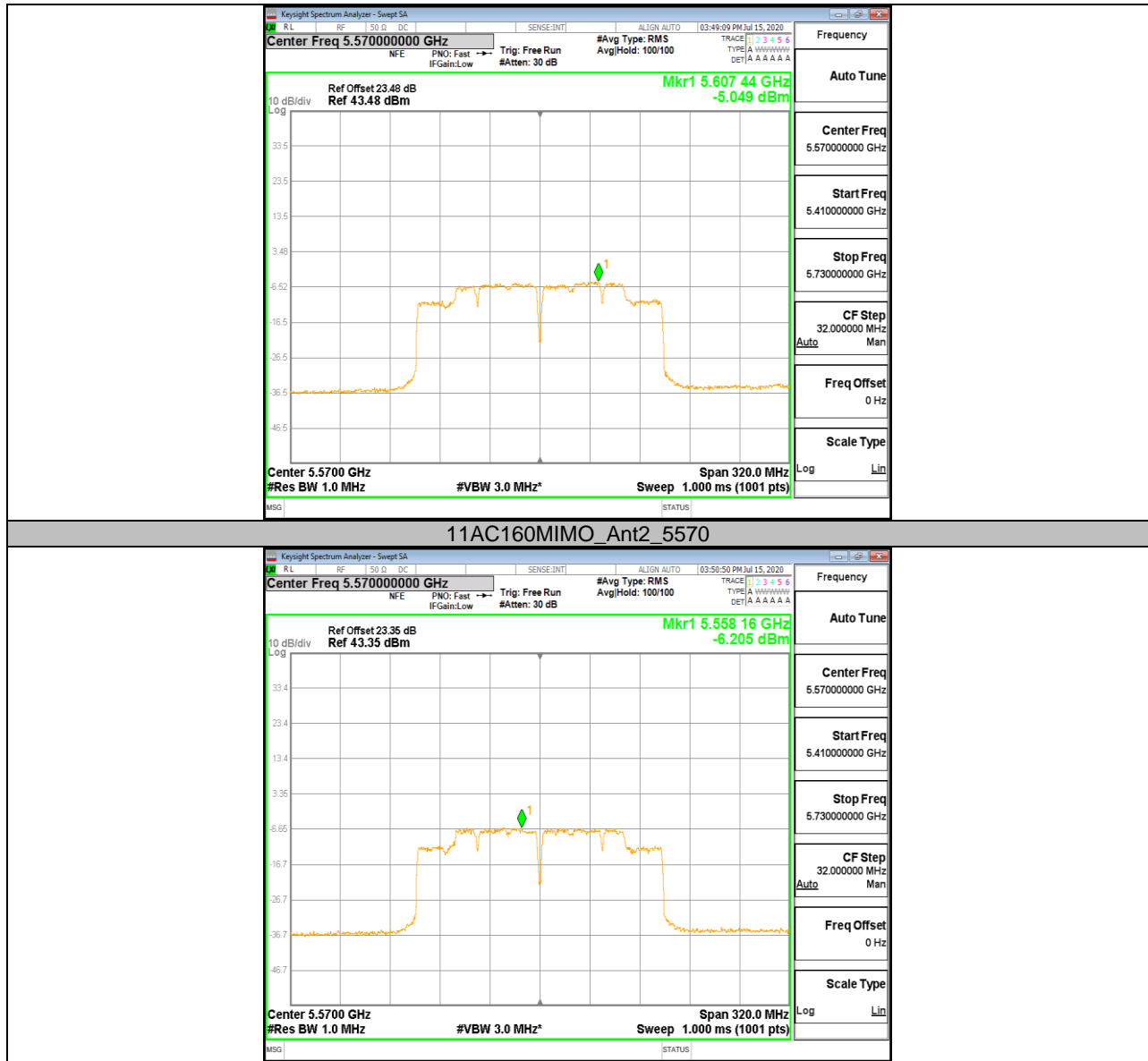
11AC160MIMO\_Ant1\_5250



11AC160MIMO\_Ant2\_5250



11AC160MIMO\_Ant1\_5570





### Appendix C: Duty Cycle Test Result

Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (db)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A	2.05	2.09	0.981	98.1 %	0.083	0.49	1
11N20MIMO	1.91	1.96	0.974	97.4 %	0.114	0.52	1
11N40MIMO	0.93	0.99	0.939	93.9 %	0.273	1.08	2
11AC80MIMO	0.25	0.31	0.806	80.6 %	0.937	4.00	5
11AC160MIMO	0.15	0.21	0.714	71.4 %	1.463	6.67	7

Note:

Duty Cycle Correction Factor= $10\log(1/x)$ .

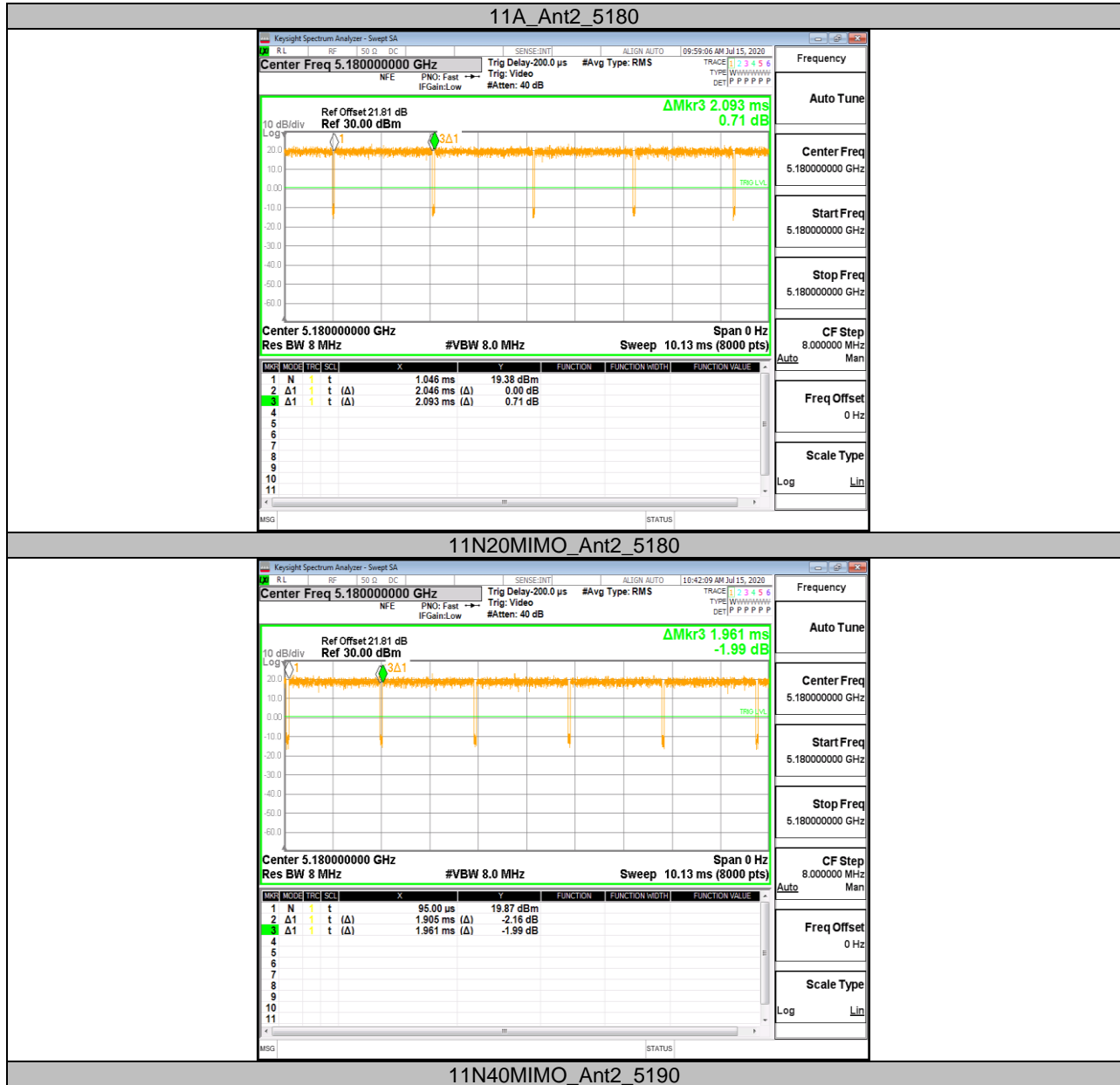
Where: x is Duty Cycle (Linear)

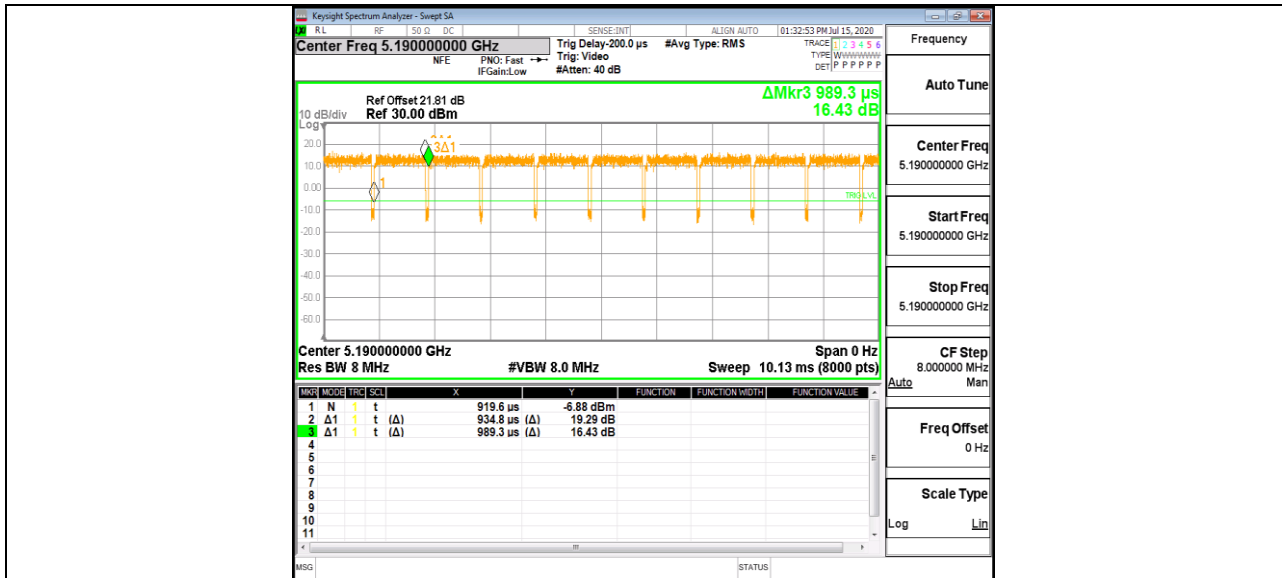
Where: T is On Time (transmit duration)

If that calculated VBW is not available on the analyzer then the next higher value should be used.

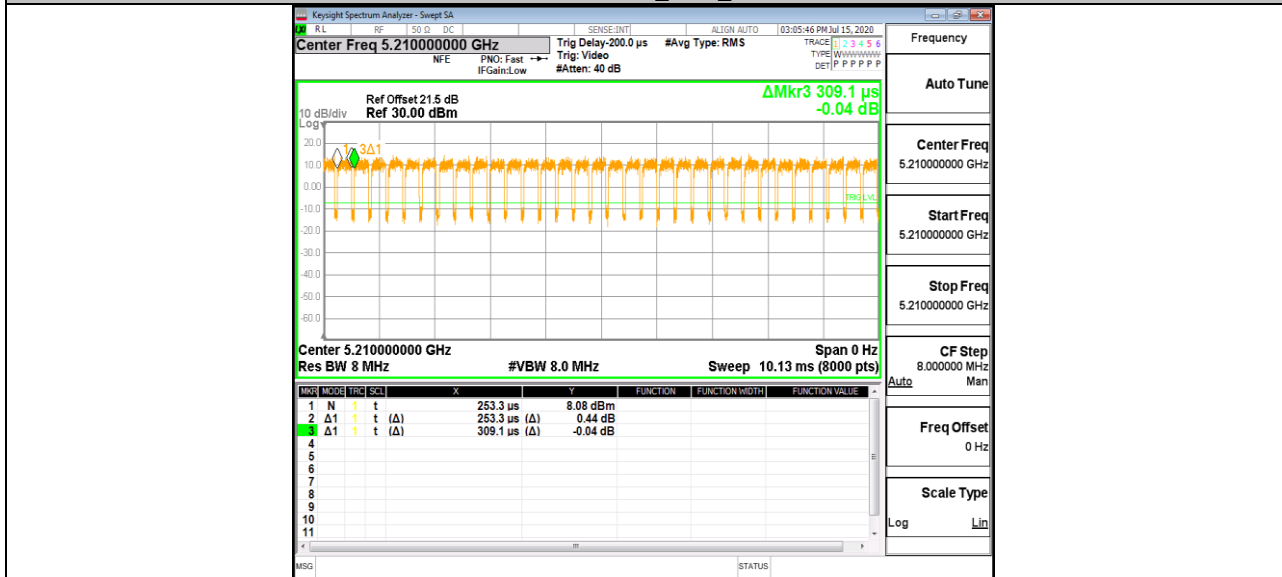


### Test Graphs





11AC80MIMO\_Ant2\_5210



11AC160MIMO\_Ant2\_5250