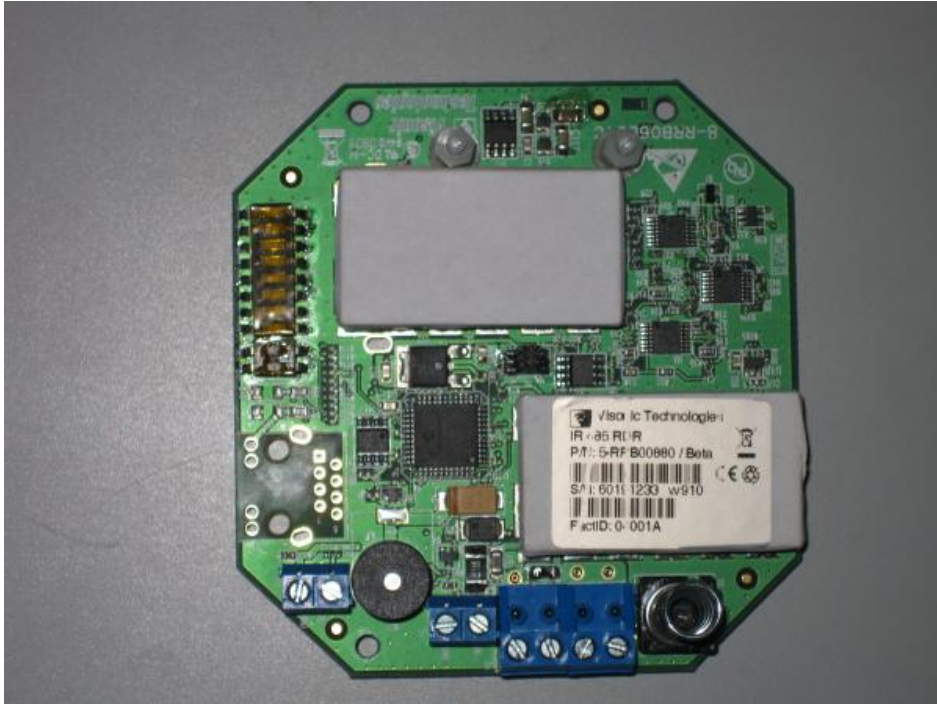


Top of PCB with card removed



Top



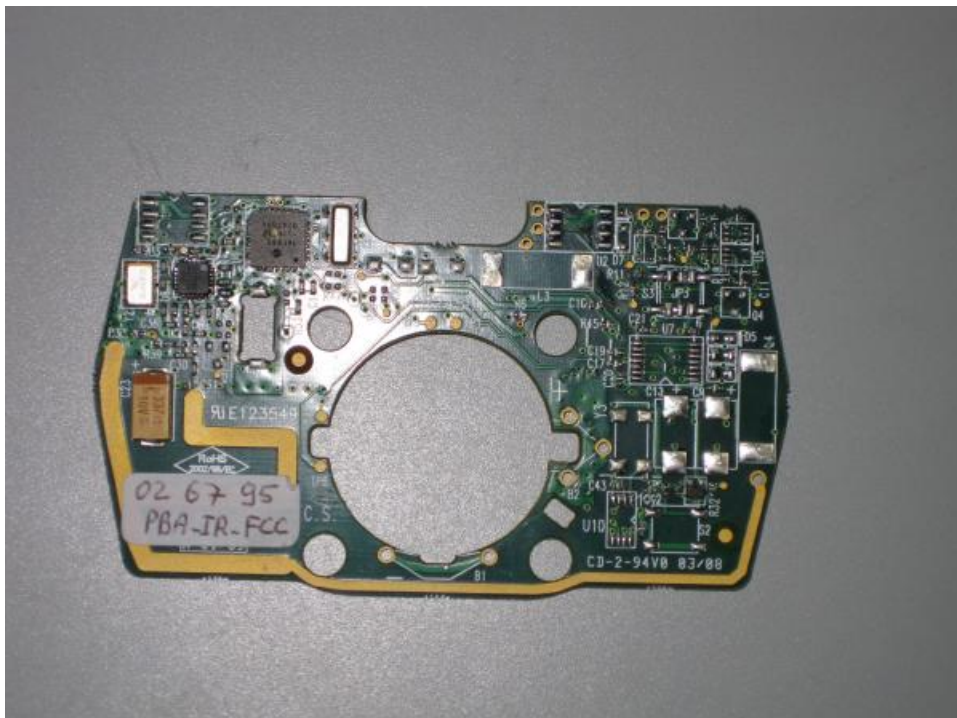
PCB with secondary PCB removed



PCB with secondary with component covers removed



Bottom of Secondary PCB



Top of Secondary PCB