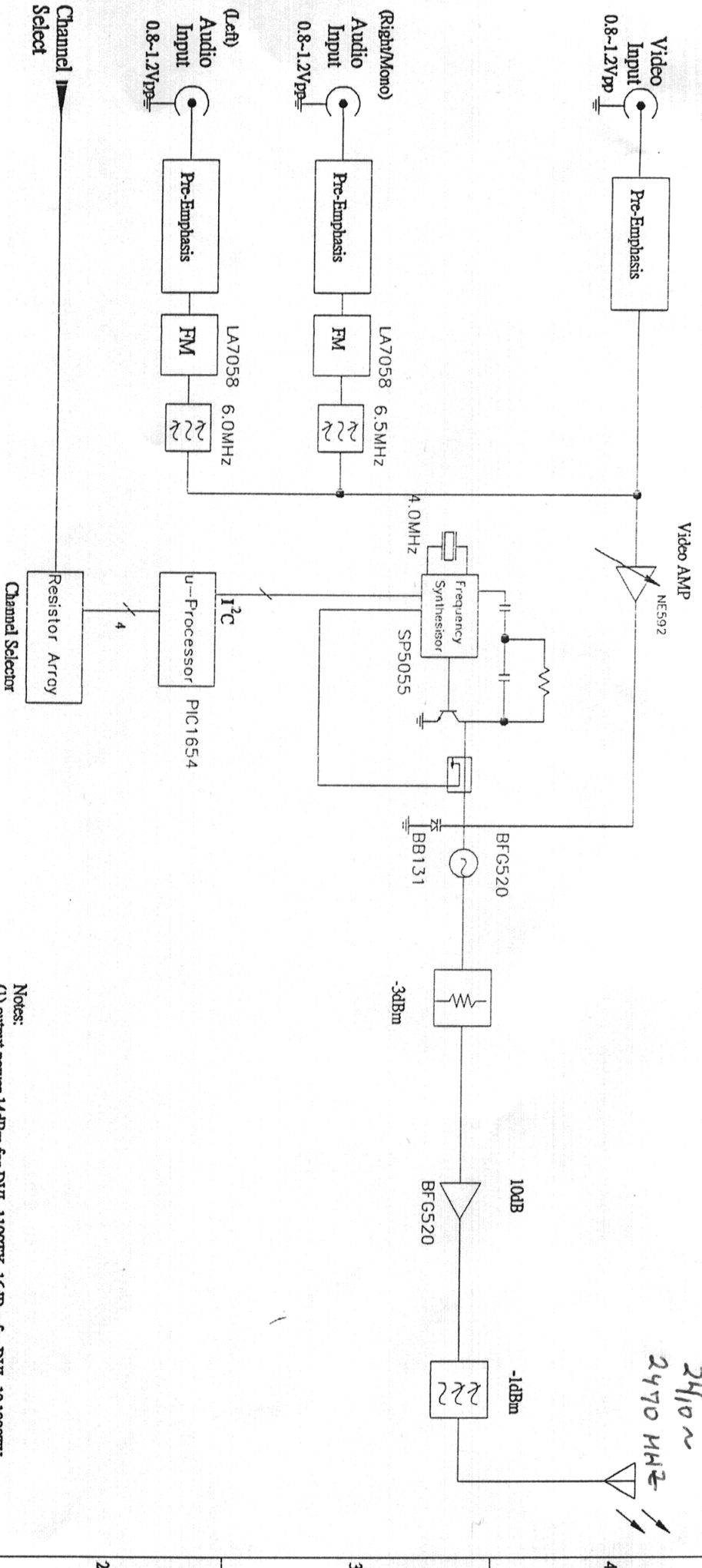




- Notes:
- (1) output power 14dBm for DVL-1100TX, 16dBm for DVL-121300TX
 - (2) Using 4 resistors connected to Vcc/GND to select different carrier frequency

FCC ID: 03G-DVL1066T

unless otherwise specified, all dimensions are in mm integral dimensions ± 0.2 one place decimal ± 0.1

and tolerances are angular dimensions $\pm 1^\circ$ two place decimals ± 0.05

Digicom Corporation

REV	ECO No.	DESCRIPTION	ENGINEER	DATE	PROCESS	CHECK BY	SCALE	DWG. TITLE	REV
C	890510	DWG TITLE REVISED	陳嘉祥	5/25/00	FINISH	陳嘉祥	5/25/00	DVL-1000TX Block Diagram	C
REV	ECO No.	DESCRIPTION	ENGINEER	DATE	PROCESS	CHECK BY	SCALE	DWG. TITLE	REV
DG-RR-001	A		B				A4	BR-000003-2	1 OF 1

12/5/00

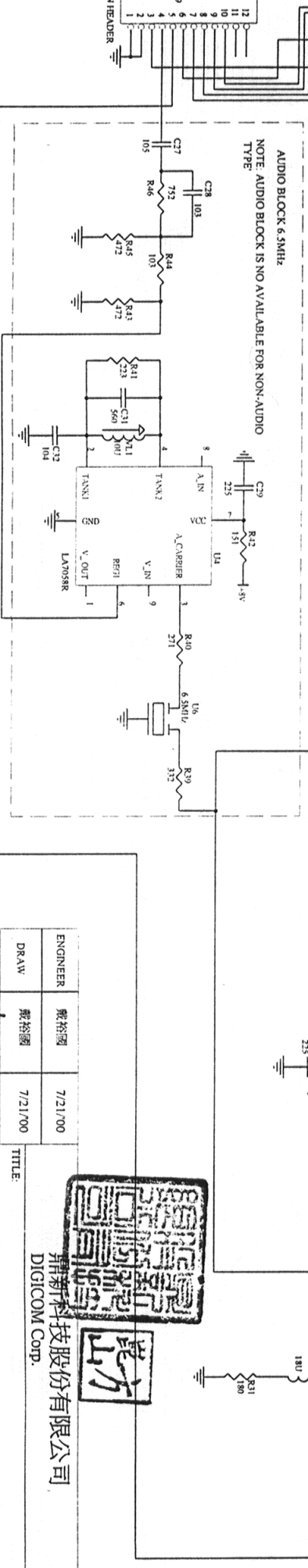
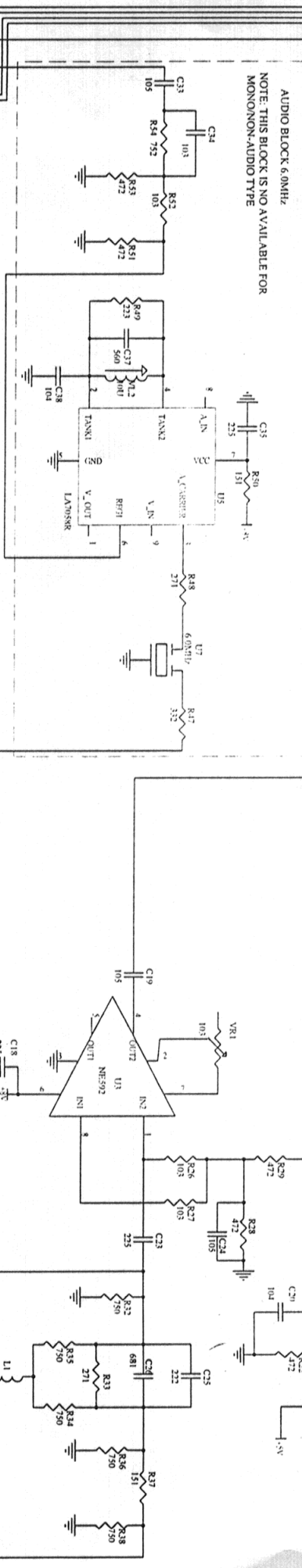
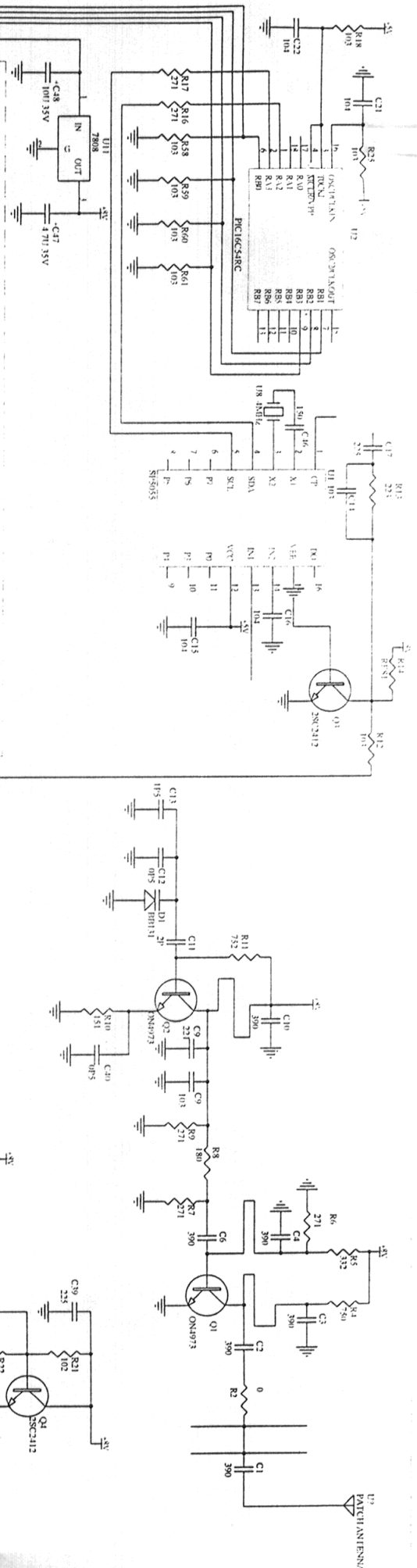
EA98693 / QTK2000-F038

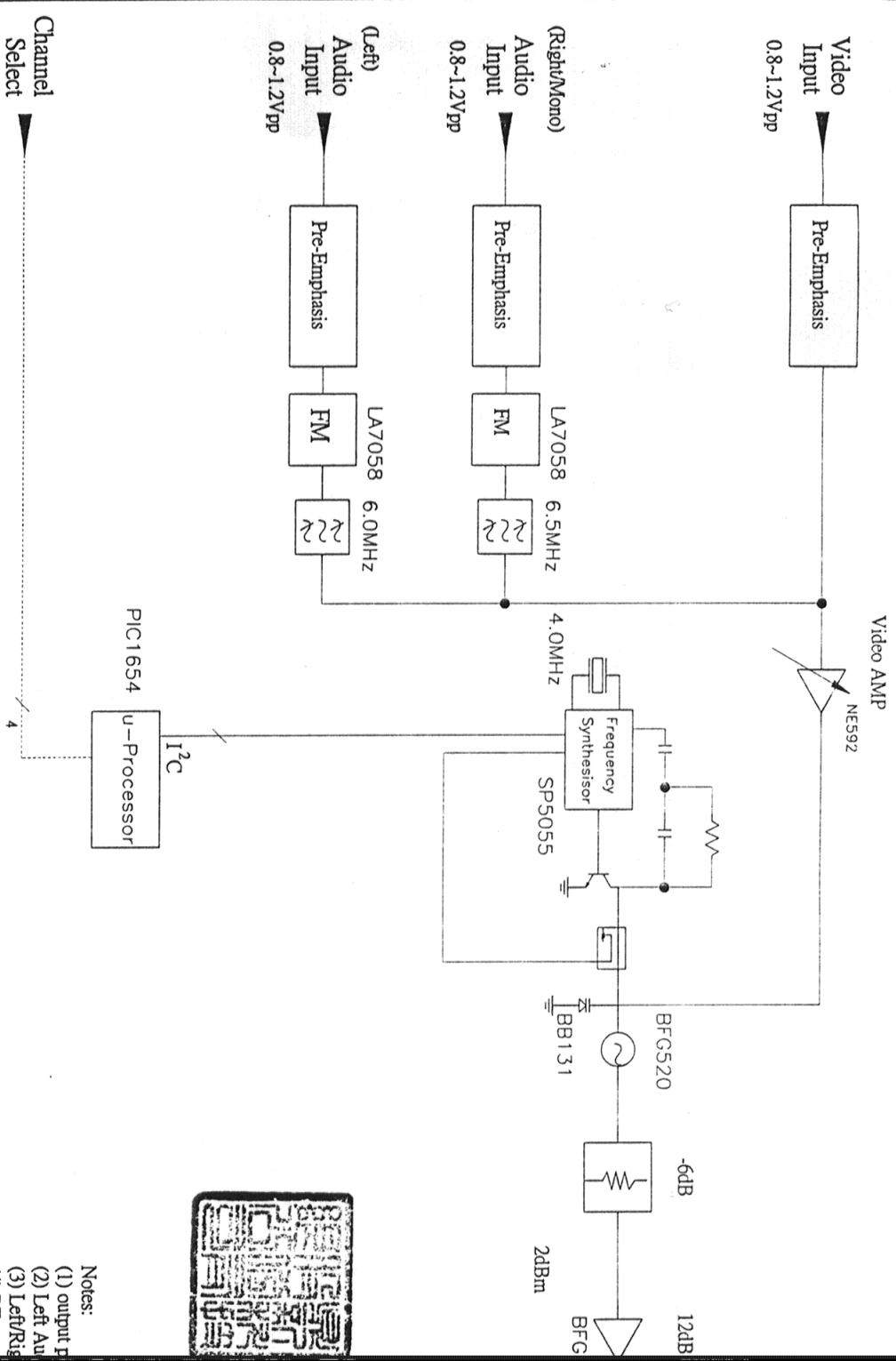
RH/Sec

1/4



Fee ID: 03G-DVL1000T

[illegible]



FCC ID: 03G DVL1D00T

unless otherwise specified, all dimensions are in mm and tolerances are integral dimensions ± 0.2 angular dimensions one place decimal ± 0.1 two place decimals

A	890221	1st Release	陳嘉祥	2/23/00	FINISH	MATERIAL	DRAW BY	陳嘉祥
REV	ECO No.	DESCRIPTION	ENGINEER	DATE	PROCESS		ENG. BY	陳嘉祥
DG-RR-001	A	B	C	D			CHECK BY	陳嘉祥



- Notes:
- (1) output P
 - (2) Left Ant
 - (3) Left/Rig
 - (4) RF ante