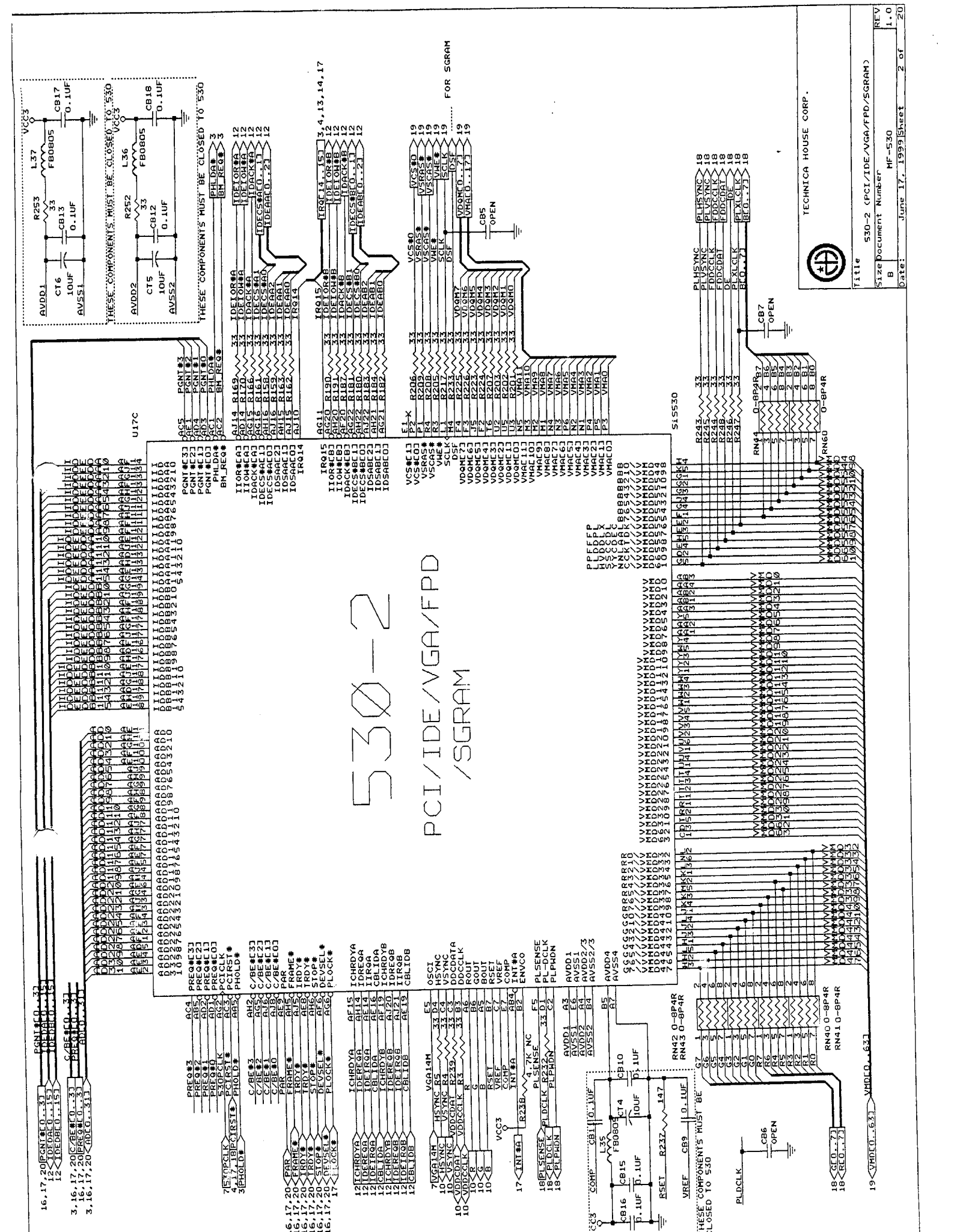


530-2

PCI/IDE/VGA/FPD /SGRAM



Title: 530-2 (PCI/IDE/VGA/FPD/SGRAM)

Size: Document Number MF-530

Date: June 17, 1999 Sheet 2 of 20

REV: 1.0

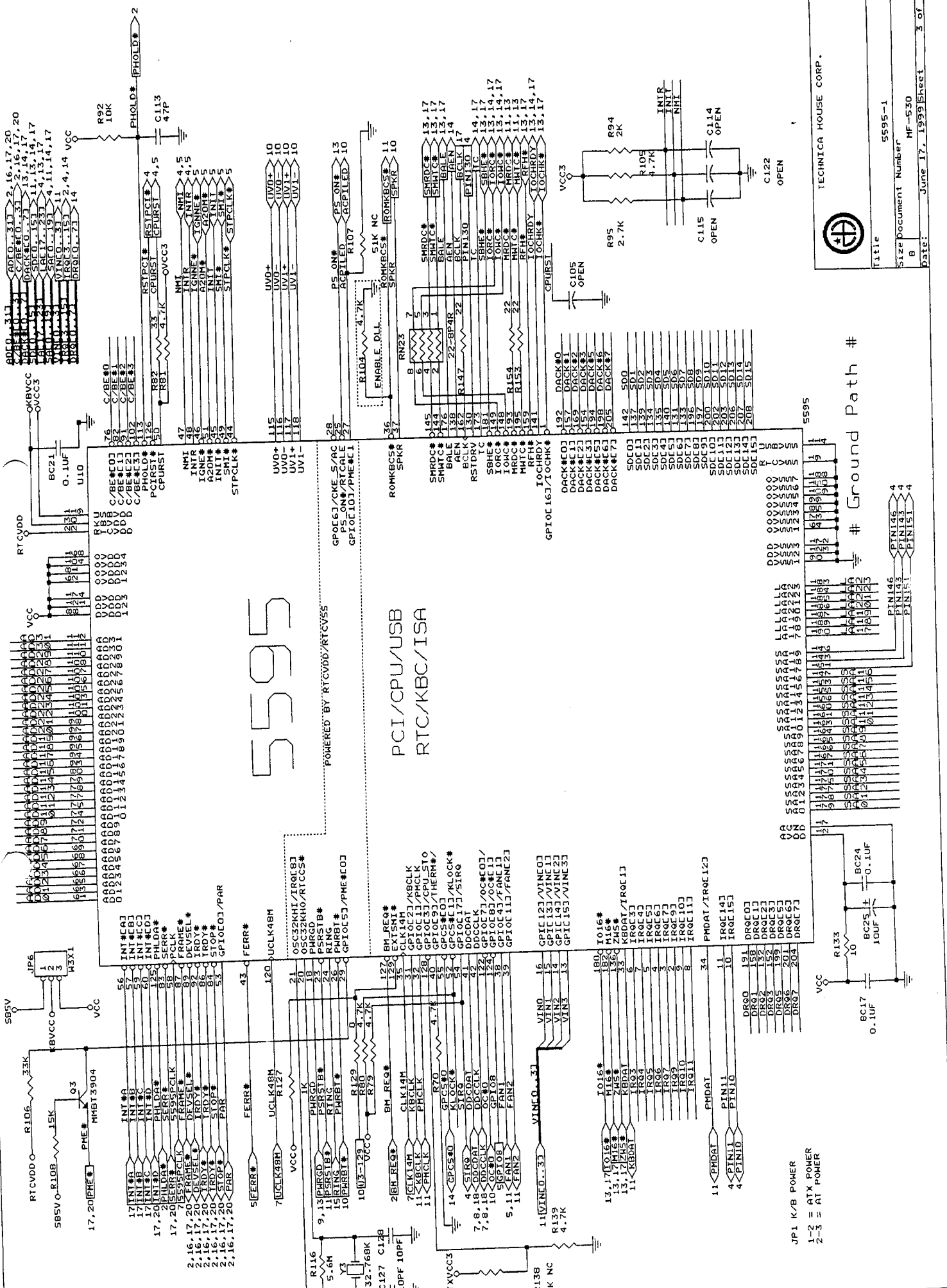
THESE COMPONENTS MUST BE CLOSED TO 530

THESE COMPONENTS MUST BE CLOSED TO 530

FOR SGRAM

VCC3

VCC3




5595

PCI/CPU/USB
RTC/KBC/ISA

POWERED BY RTCVDD/RTCVSS

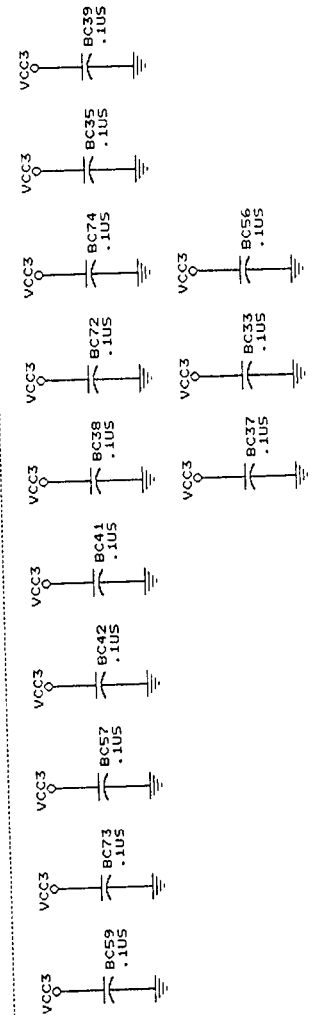
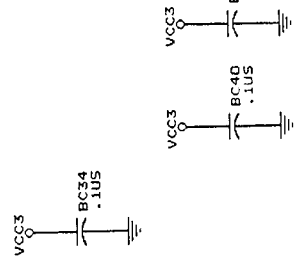
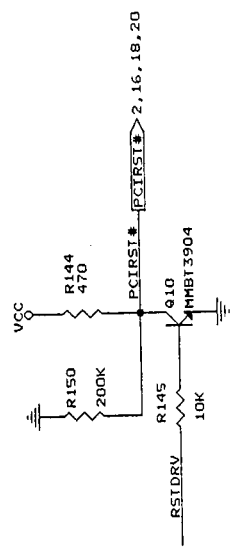
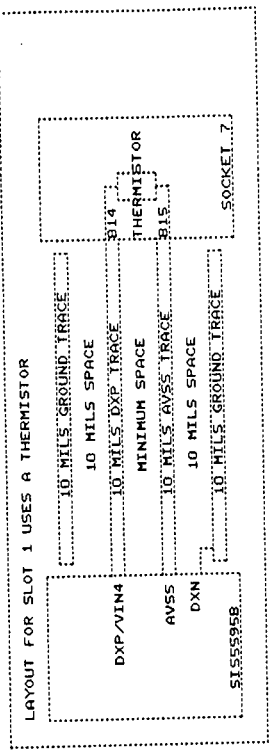
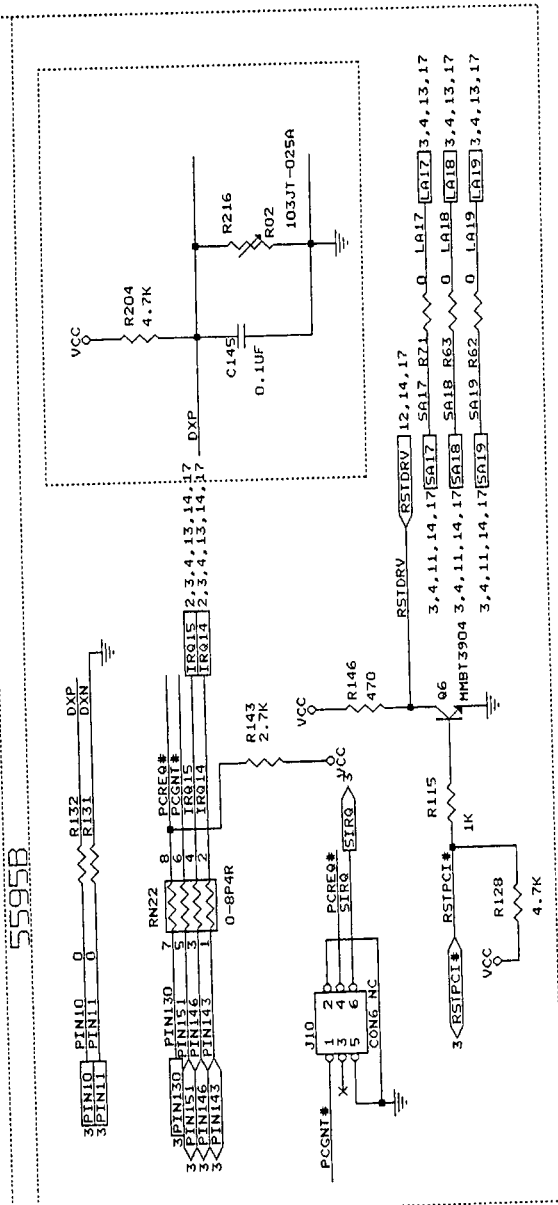
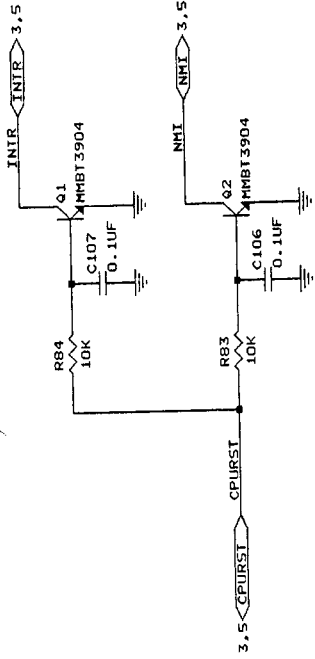
Ground Path

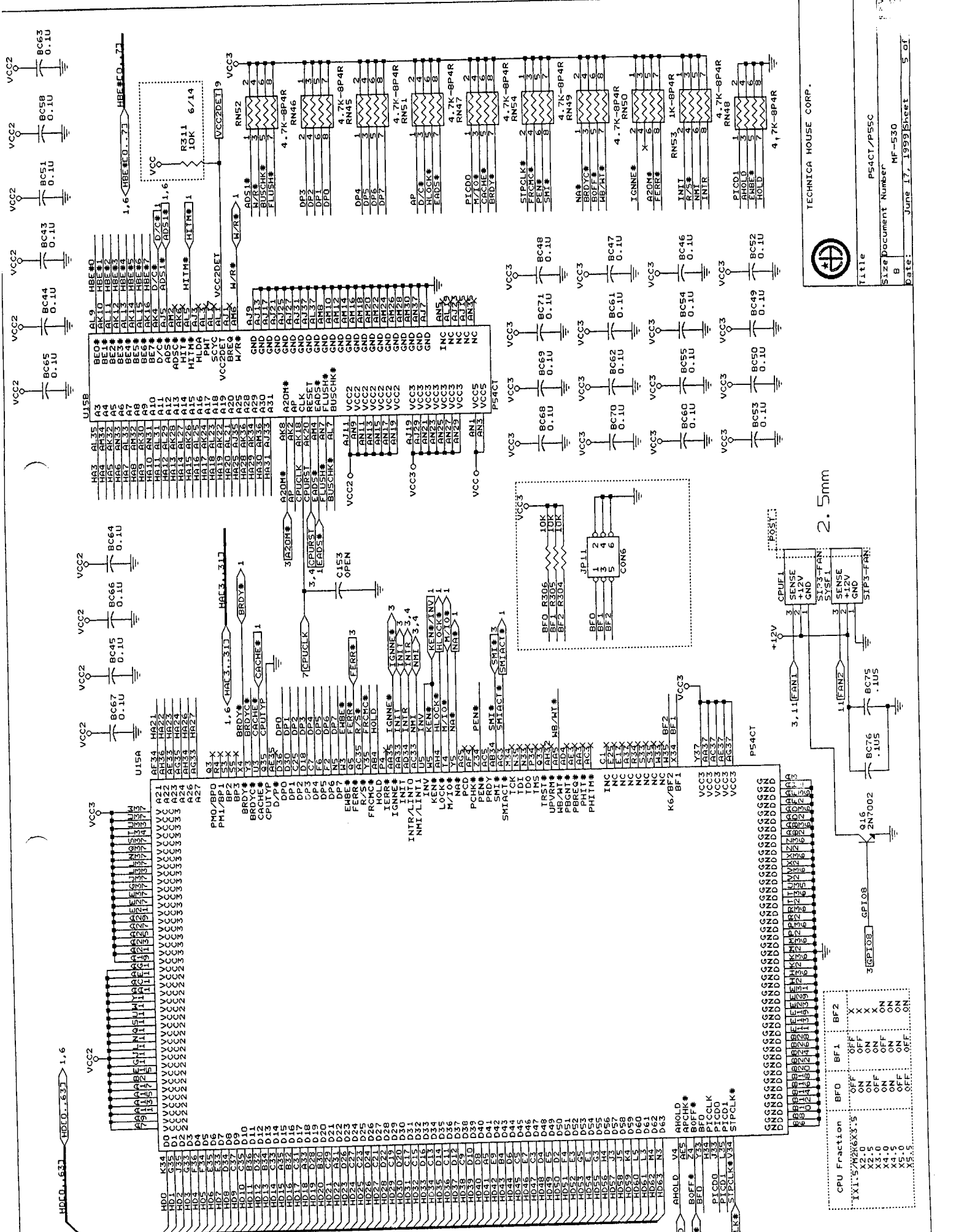
TECHNICA HOUSE CORP.




Title: 5595-1
Size Document Number: MF-530
REV: 1.0
Date: June 17, 1999 Sheet 3 of 20

JP1 K/B POWER
1-2 = ATX POWER
2-3 = AT POWER

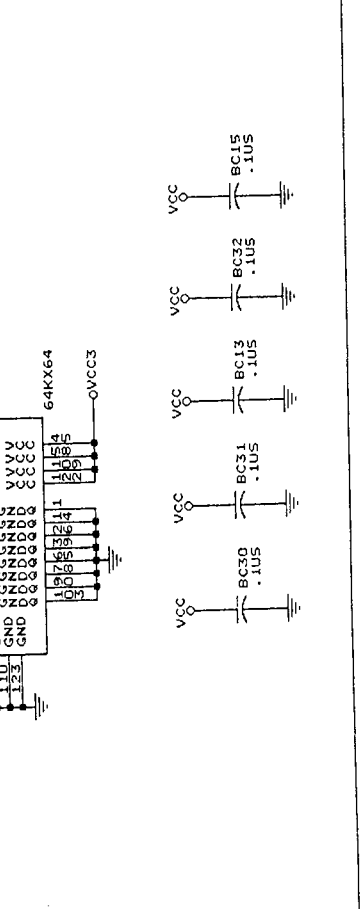
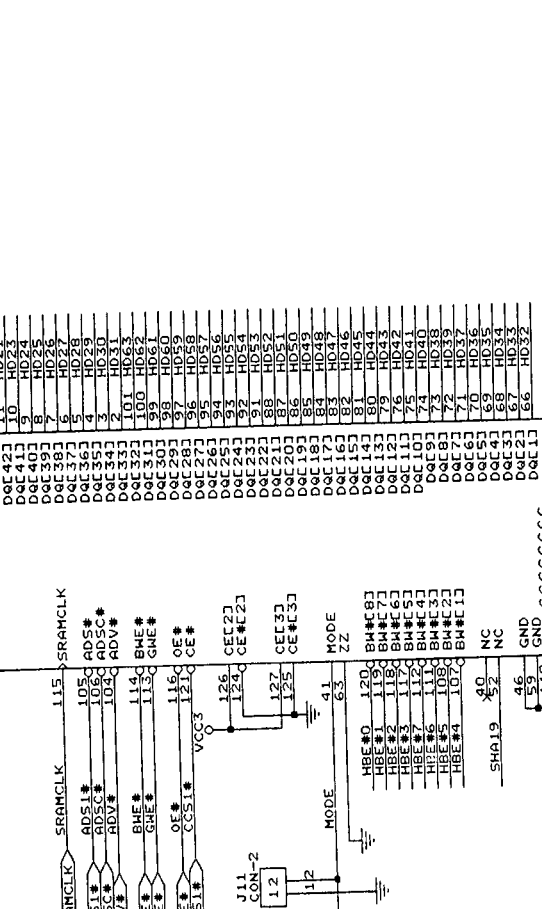
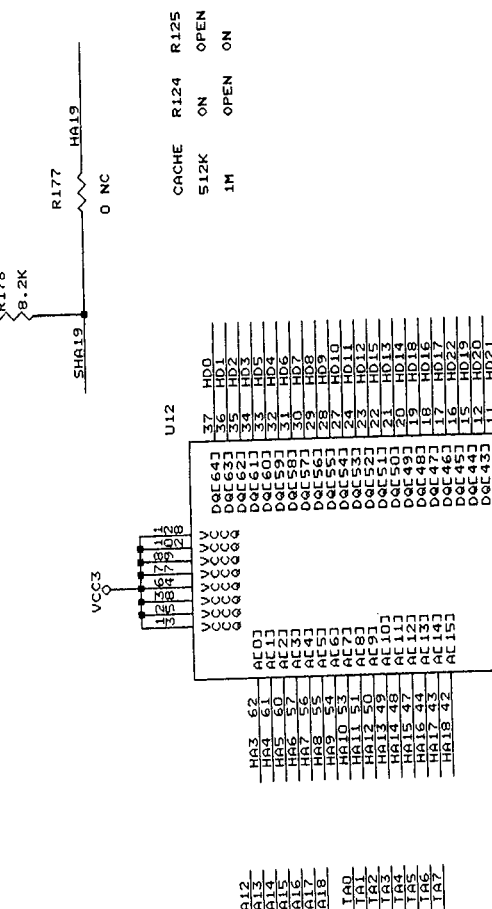
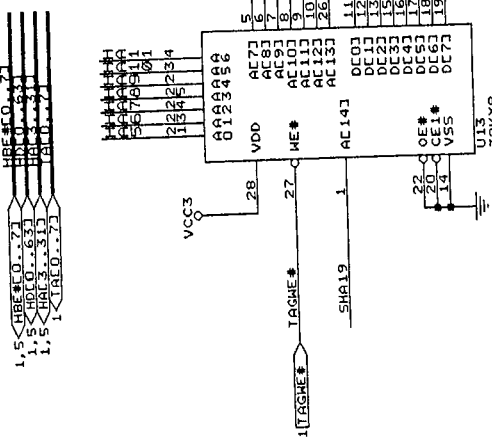




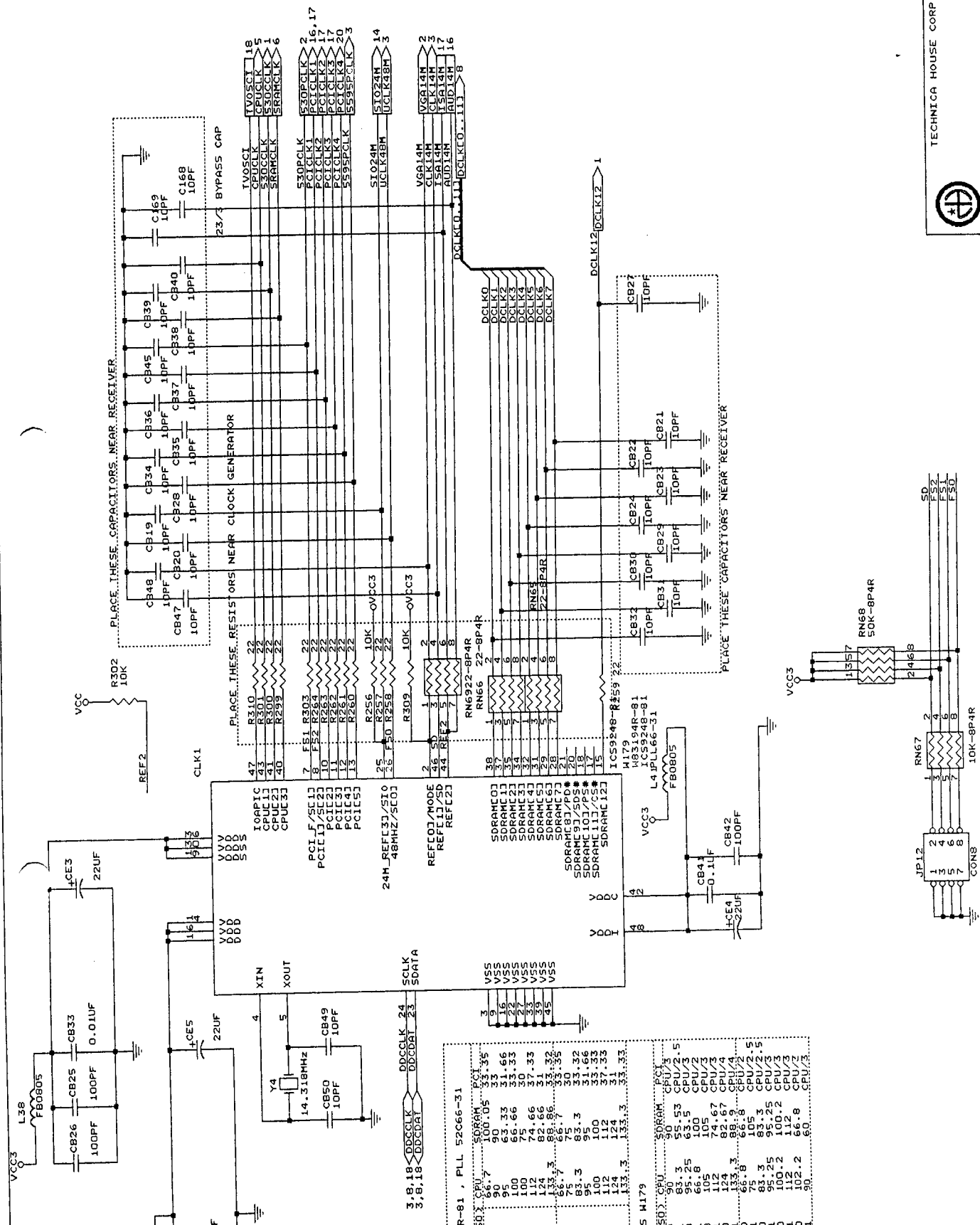
TECHNITCA HOUSE CORP.


 Title: P54CT/P55C
 Size: Document Number: MF-530
 Date: June 17, 1999 Sheet 5 of 5

CPU Fraction	BFO	BF1	BF2
X1.5*/HWX3.5	OFF	OFF	X
X2.0	ON	ON	X
X2.5	ON	ON	X
X3.0	OFF	OFF	ON
X4.0	ON	ON	ON
X5.0	OFF	OFF	ON
X5.5	OFF	OFF	ON



J3 ON = CYRIX CPU LINER MODE.



PLACE THESE CAPACITORS NEAR RECEIVER

PLACE THESE RESISTORS NEAR CLOCK GENERATOR

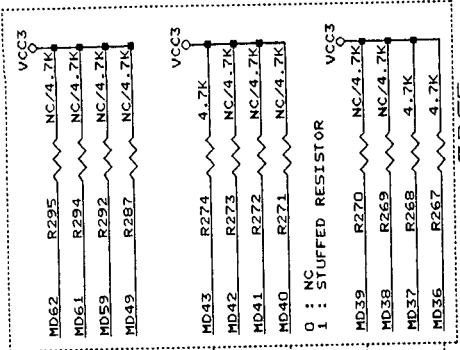
PLACE THESE CAPACITORS NEAR RECEIVER



TECHNICA HOUSE CORP.

Title: CLOCK GENERATOR (IMI C9745, IC59248-59, IWF513)
 Size: Document Number MF-530
 Date: June 14, 1999 Sheet 7 of 20

EXTERNAL POWER DOWN
 HOST SLOW DECODE
 INTERRUPT DISABLE
 VSEOFF ENABLE



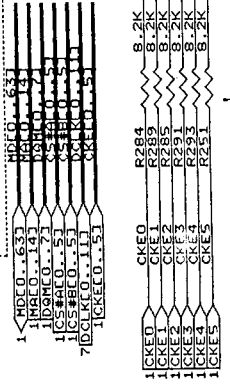
Close EDGE

MCLK LEADED SOCLK

MD39	MD38	MD37	MD36	TIME
0	0	0	0	+0.5ns
0	0	0	0	+1.0ns
0	0	0	0	+1.5ns
0	0	0	0	+2.0ns
0	0	0	0	+2.5ns
0	0	0	0	+3.0ns
0	0	0	0	+3.5ns
0	0	0	0	+4.0ns
0	0	0	0	+4.5ns
0	0	0	0	+5.0ns
0	0	0	0	+5.5ns
0	0	0	0	+6.0ns
0	0	0	0	+6.5ns
0	0	0	0	+7.0ns
0	0	0	0	+7.5ns
0	0	0	0	+8.0ns

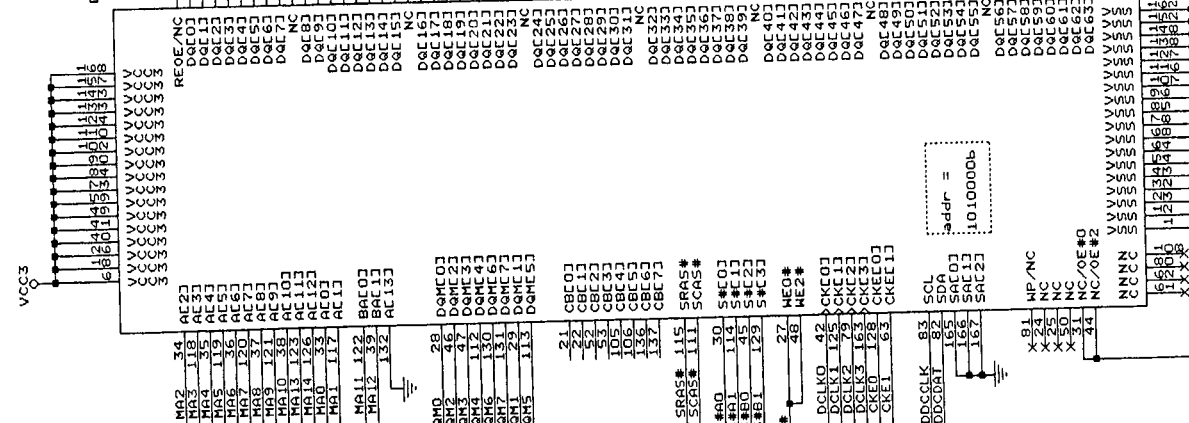
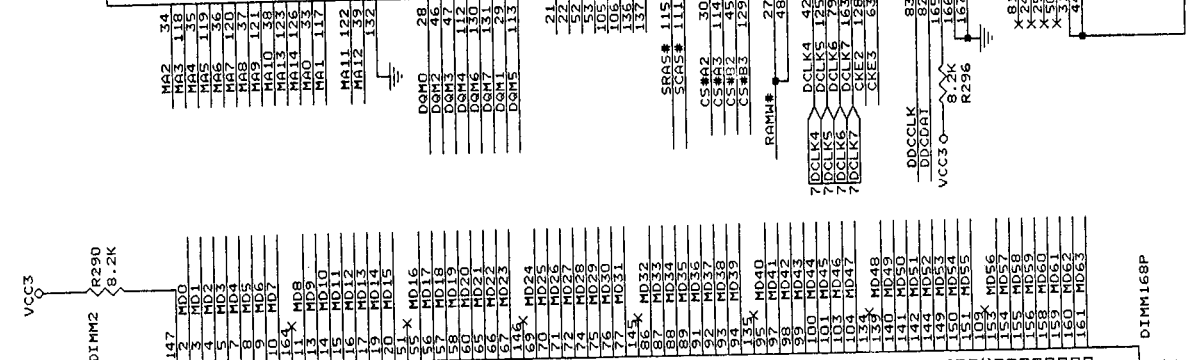
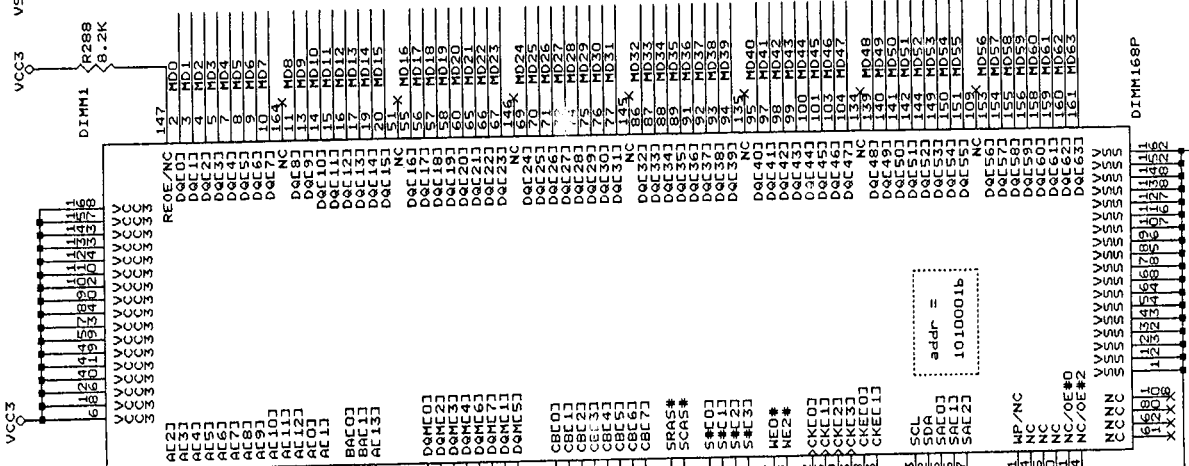
MDHCLK LEADED CPUCLK

MD43	MD42	MD41	MD40	TIME
0	0	0	0	-2.0ns
0	0	0	0	-1.0ns
0	0	0	0	0.0ns
0	0	0	0	+1.0ns
0	0	0	0	+1.5ns
0	0	0	0	+2.0ns
0	0	0	0	+2.5ns
0	0	0	0	+3.0ns
0	0	0	0	+3.5ns
0	0	0	0	+4.0ns
0	0	0	0	+4.5ns
0	0	0	0	+5.0ns
0	0	0	0	+5.5ns
0	0	0	0	+6.0ns
0	0	0	0	+6.5ns
0	0	0	0	+7.0ns
0	0	0	0	+7.5ns
0	0	0	0	+8.0ns

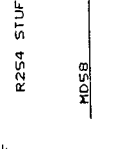


TECHNICA HOUSE CORP.

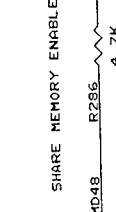
Title: DIMM1 & DIMM2
 Size Document Number: MF-530
 B
 Date: June 17, 1999 Sheet 8 of 20



R254 STUFFED = PANEL LINK LCD



SHARE MEMORY ENABLE



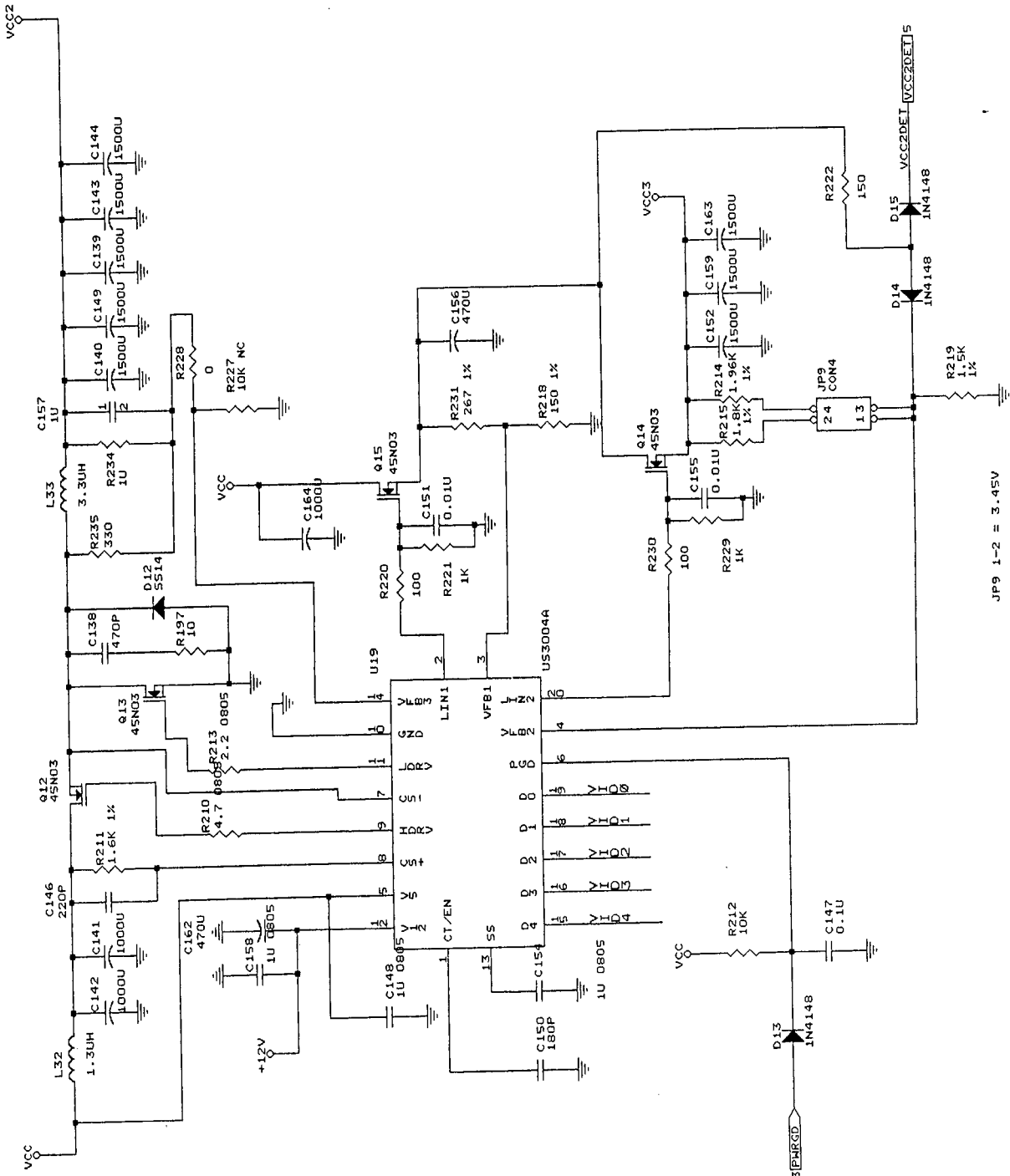
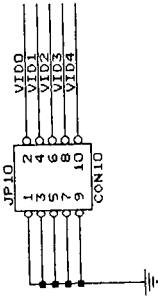
JP13 ON = SHARE MEMORY
 OFF = DISABLE INT VGA



R298 2.7K



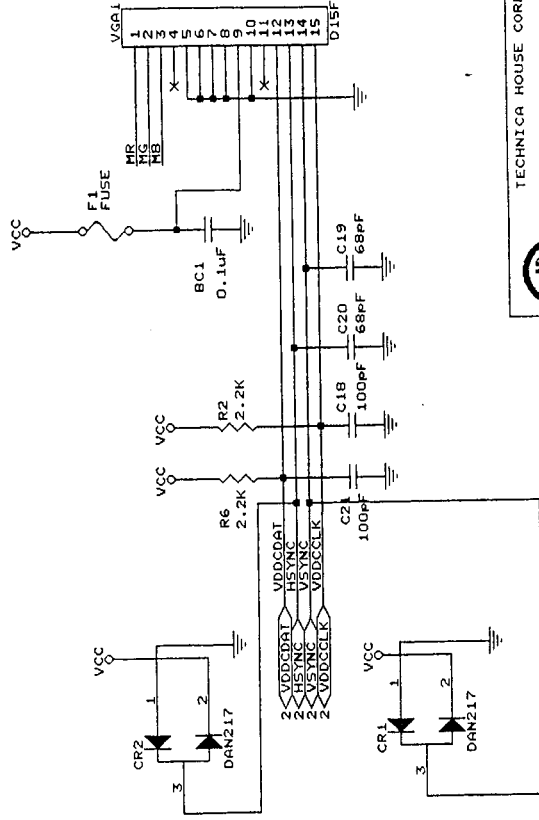
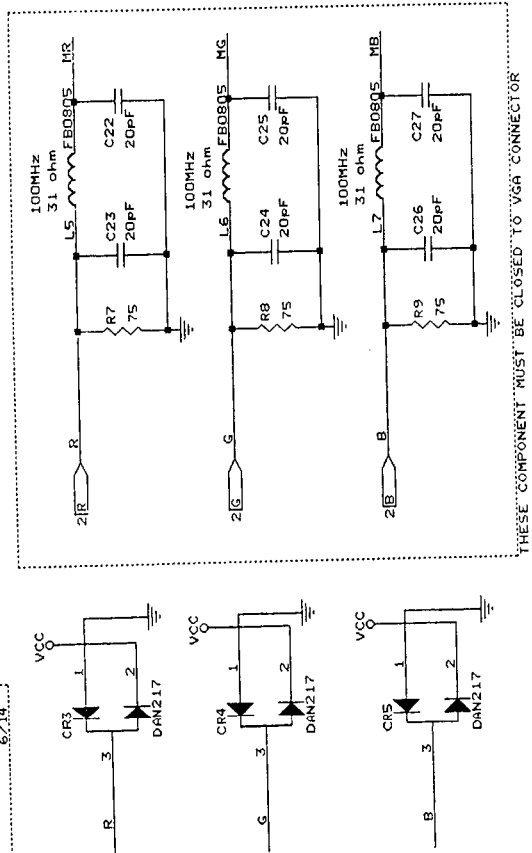
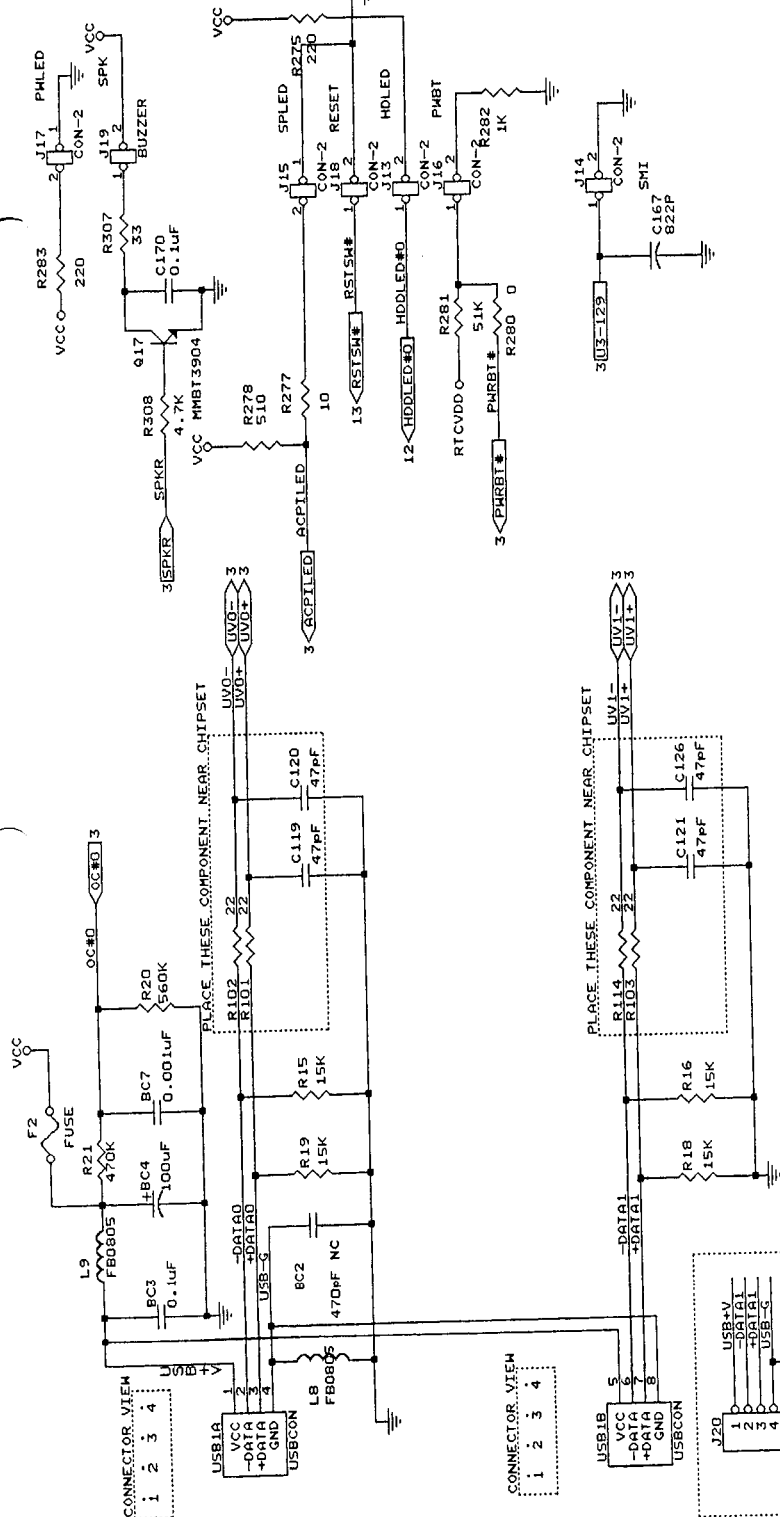
VCC2	1-2	3-4	5-6	7-8	9-10
1.8V	OFF	ON	OFF	ON	ON
2.0V	OFF	ON	ON	ON	ON
2.2V	OFF	ON	OFF	OFF	OFF
2.5V	ON	OFF	ON	OFF	OFF
2.8V	OFF	OFF	OFF	ON	OFF
2.9V	ON	OFF	ON	ON	OFF
3.1V	ON	ON	OFF	ON	OFF
3.2V	OFF	OFF	ON	ON	OFF
3.3V	ON	OFF	ON	ON	OFF
3.5V	ON	ON	ON	ON	OFF



JP9 1-2 = 3.45V
 2-3 = 3.52V



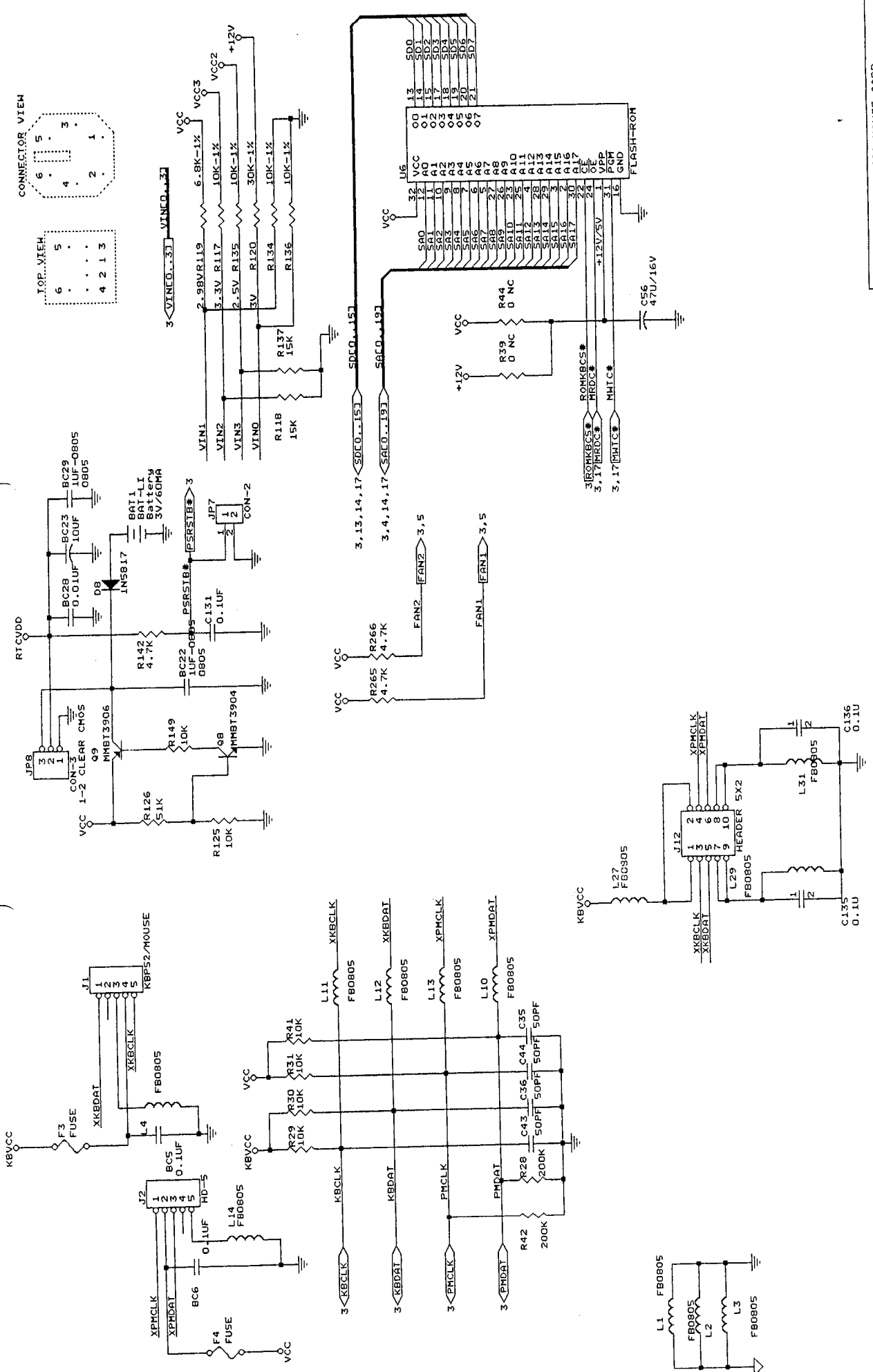
TECHNICA HOUSE CORP



NOTE: SIS IS NOT RESPONSIBLE FOR ANY ERRORS OR OMISSIONS IN THESE SCHEMATICS. THIS IS AN EXAMPLE ONLY.



TECHNICA HOUSE CORP.

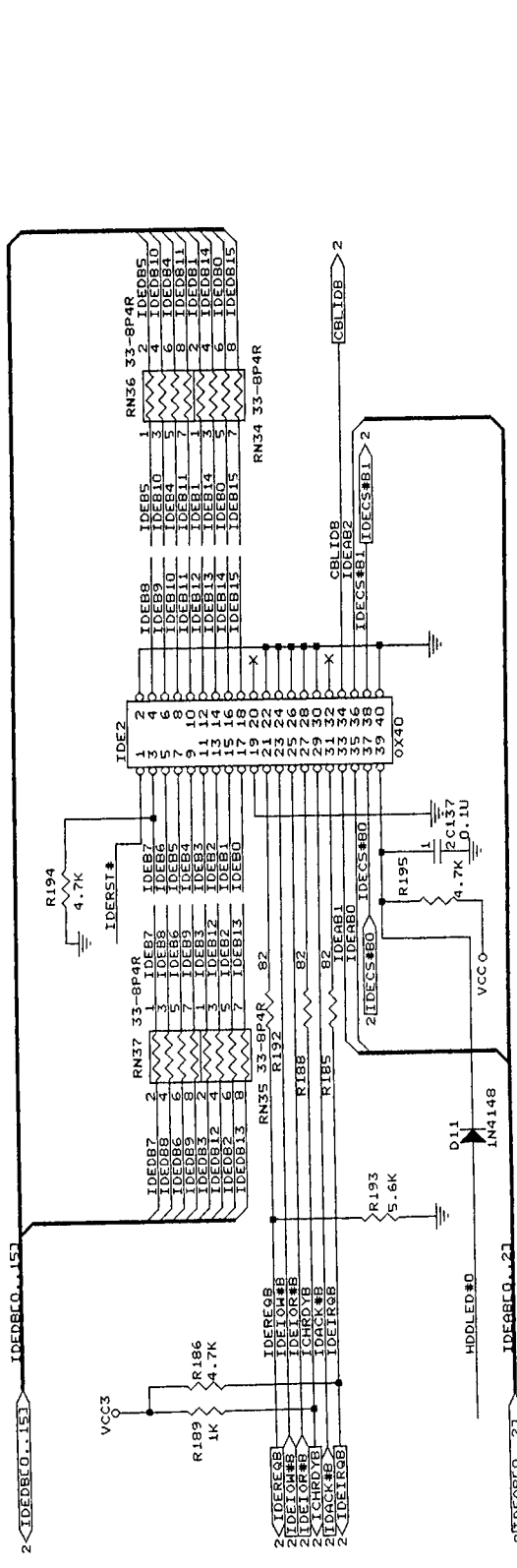
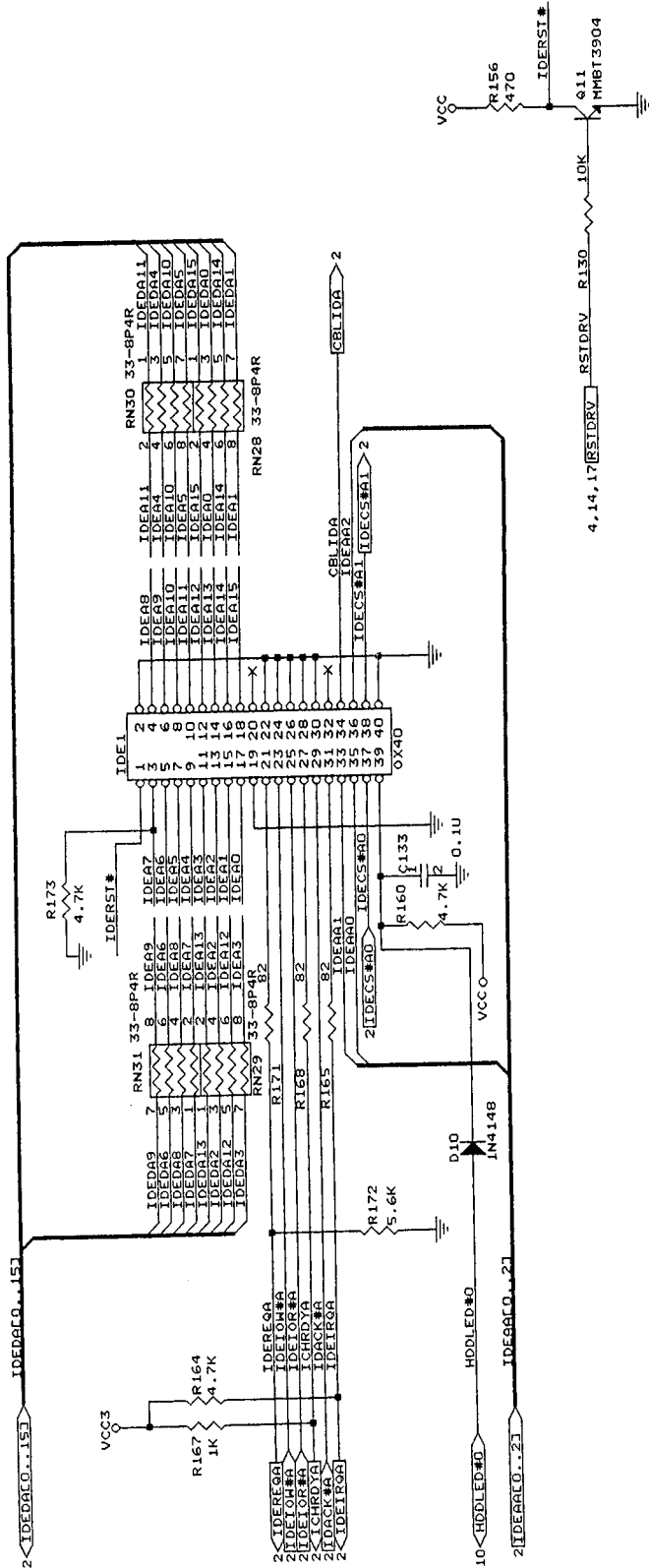


NOTE: IS NOT RESPONSIBLE FOR ANY ERRORS OR OMISSIONS IN THESE SCHEMATICS. THIS IS AN EXAMPLE ONLY.

TECHNICA HOUSE CORP.



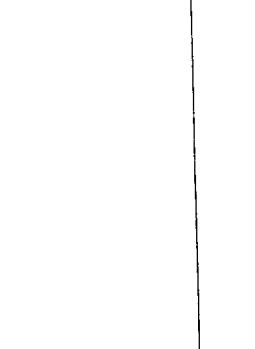
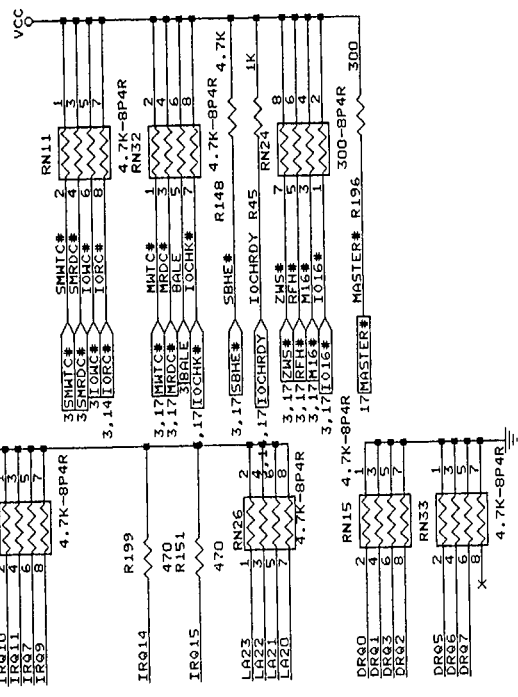
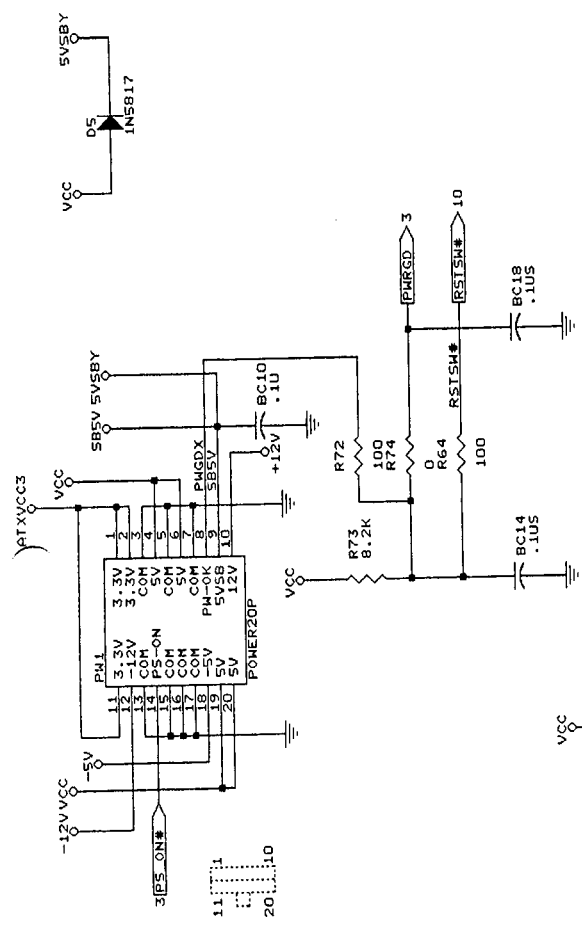
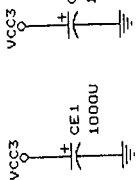
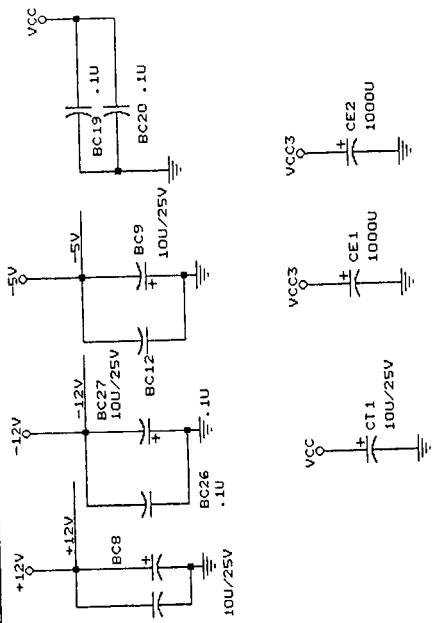
Title: KEYBOARD & MOUSE CONNECTORS
 Size: Document Number MF-530
 Date: June 17, 1999 Sheet 11 of 19



TECHNICA HOUSE CORP.

NOTE: IS NOT RESPONSIBLE FOR
 ANY ERRORS OR OMISSIONS IN
 THESE SCHEMATICS. THIS IS
 AN EXAMPLE ONLY.

Title	IDE CONNECTORS
Size	B
Document Number	MF-530
Date	May 27, 1999
Sheet	12



NOTE: THIS IS NOT RESPONSIBLE FOR
SIGNAL LOSS OR MISFUNCTIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.



TECHNICA HOUSE CORP.

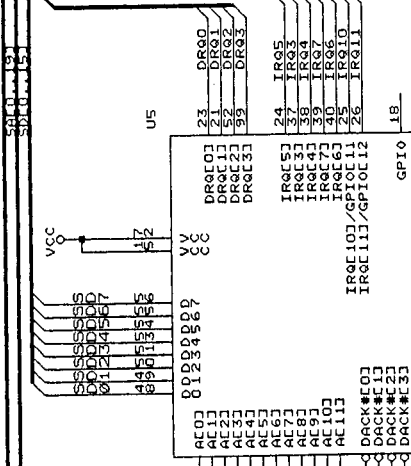
Title
Size Document Number
Date:

POWER CONN.
MF-530
June 17, 1999



TECHNICA HOUSE CORP.

SAO 28 DACK#0..Z1
 SA1 29 SA11..15
 SA2 30 DRQ#0..7,13
 SA3 31 DRQ#1..10,14,17
 SA4 32 DRQ#2..11,14,17
 SA5 33 DRQ#3..15
 SA6 34 IRQ#0..15
 SA7 41 IRQ#1..15
 SA8 42 IRQ#2..15
 SA9 43 IRQ#3..15
 SA10 27 IRQ#4..15
 SA11 28 IRQ#5..15



SAO 28 DACK#0
 SA1 29 DACK#1
 SA2 30 DACK#2
 SA3 31 DACK#3
 SA4 32 DACK#4
 SA5 33 DACK#5
 SA6 34 DACK#6
 SA7 41 DACK#7
 SA8 42 DACK#8
 SA9 43 DACK#9
 SA10 27 DACK#10
 SA11 28 DACK#11

DIR#A 15
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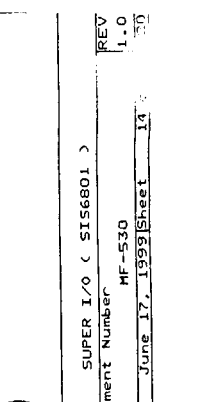
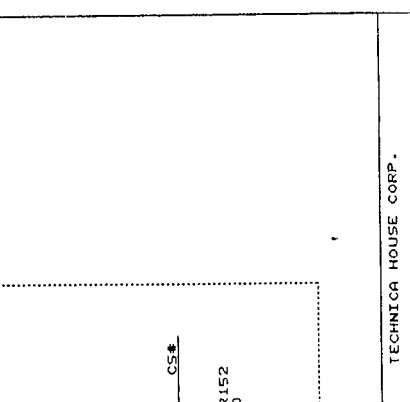
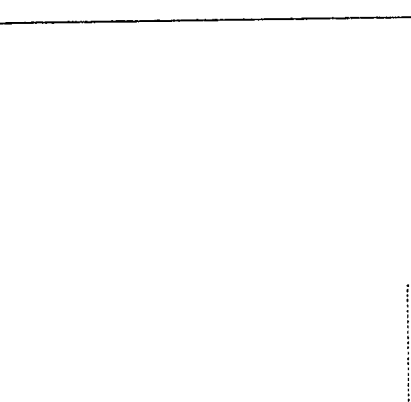
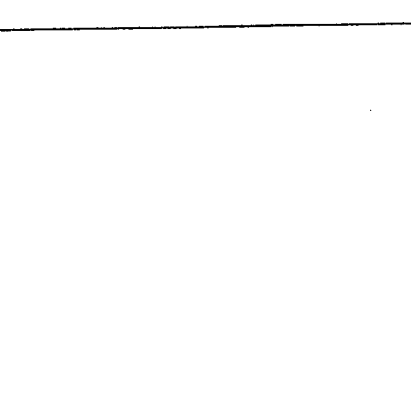
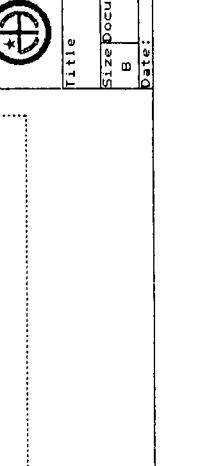
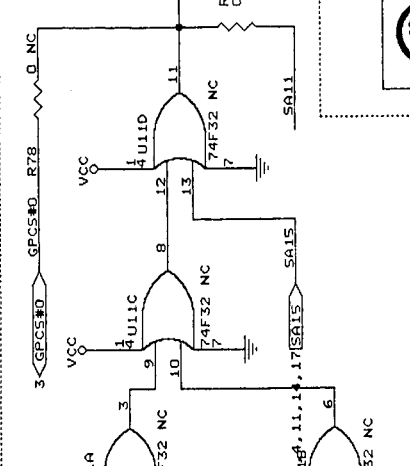
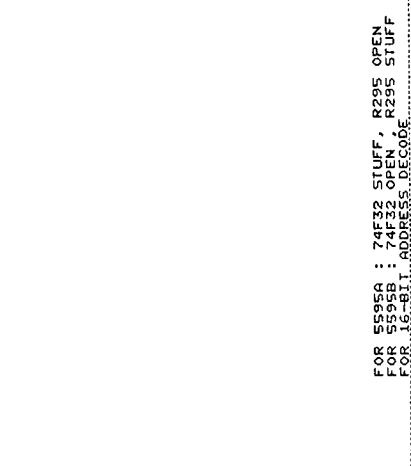
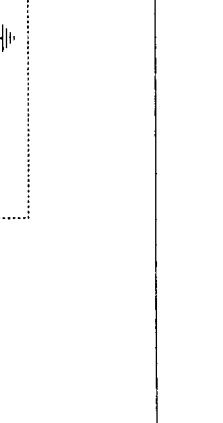
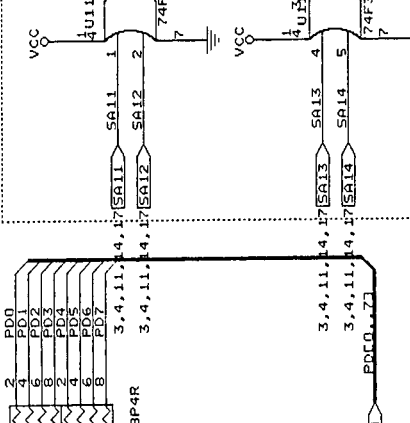
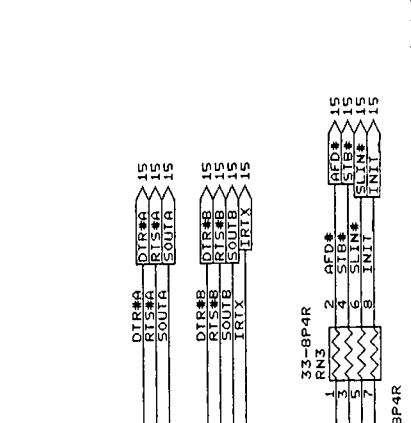
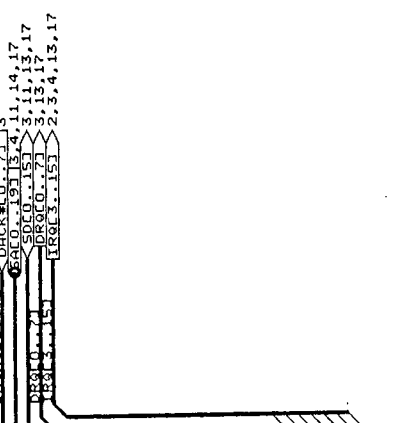
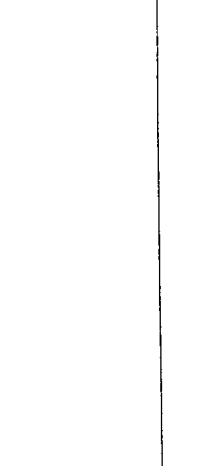
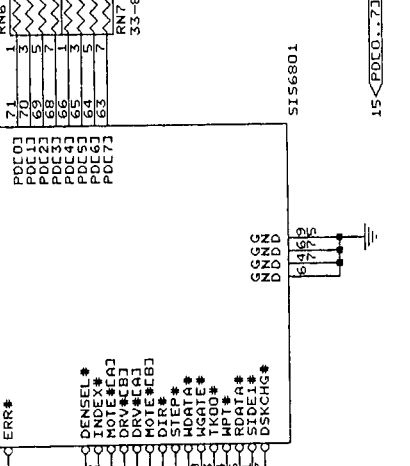
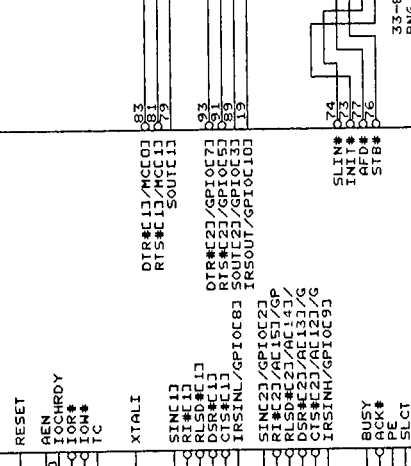
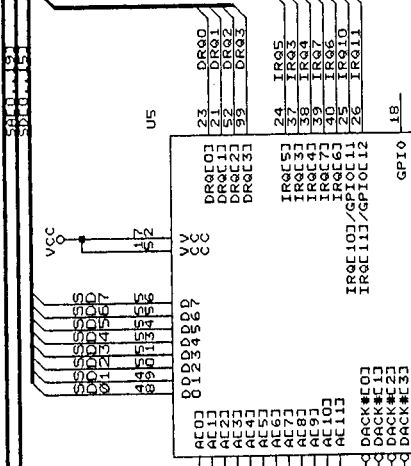
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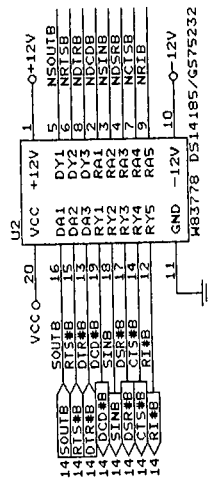
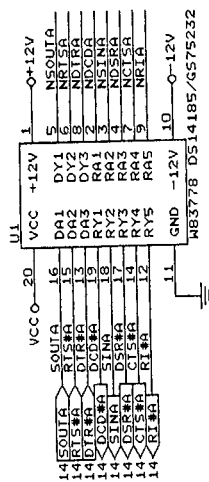
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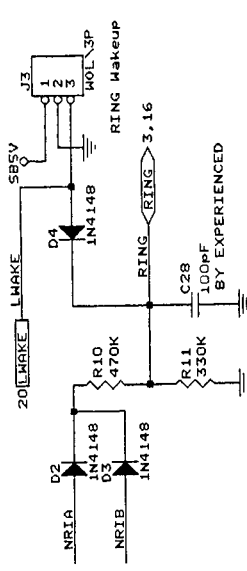
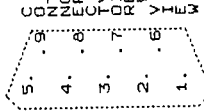
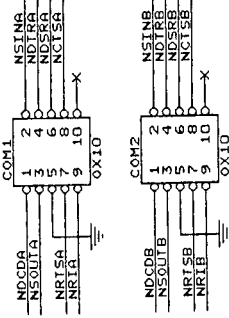
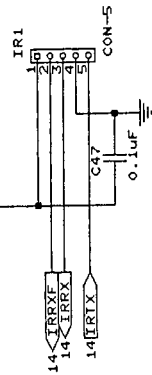


FOR 5585A : 74F32 STUFF, R295 OPEN
 FOR 5595B : 74F32 OPEN, R295 STUFF
 FOR .16..51.1..ADDRESS..DECODE

COM

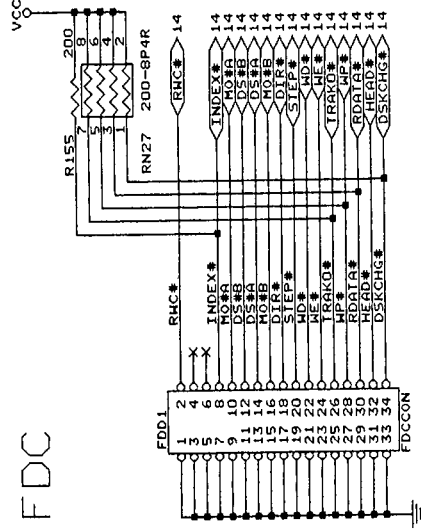


IR CONNECTOR

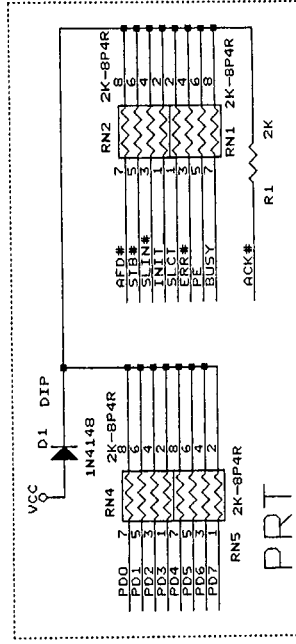
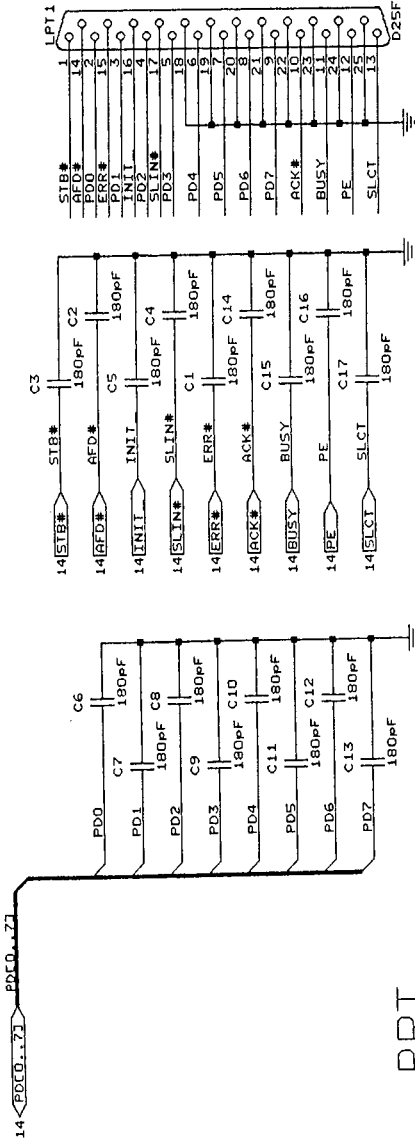


FOR MODEM MAKE-UP FUNCTION

FDC

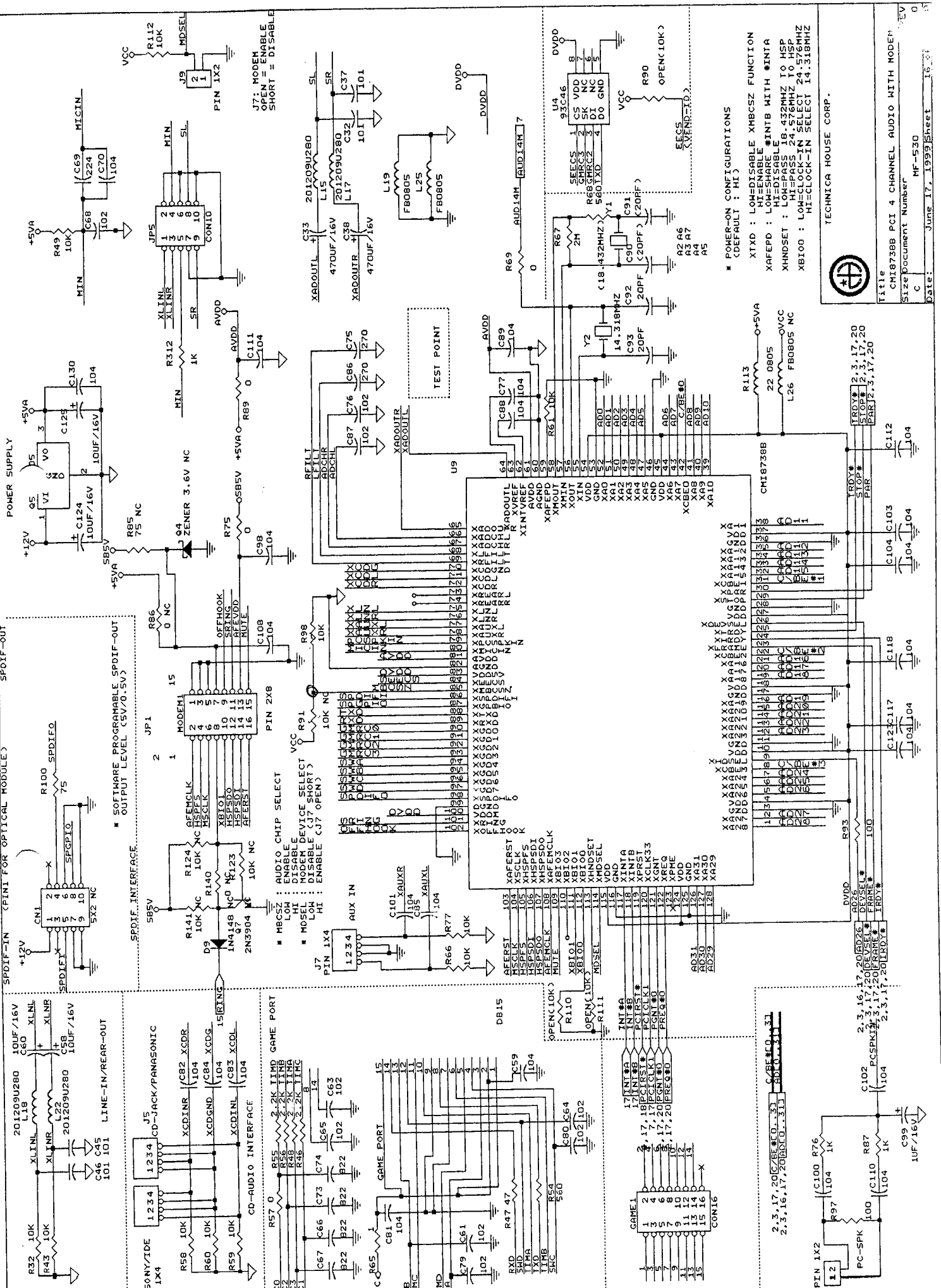


PRT



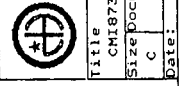
JET RAY INFORMATION





POWER-ON CONFIGURATIONS
(DEFAULT: HI)

XTXD : LOW=DISABLE XBBCSZ FUNCTION
XAFEPD : HIGH=ENABLE
XAFENP : LOW=SHARE #INTB WITH #INTA
XHNDSET : HIGH=DISABLE B.432MHZ TO HSP
#HIFPASS : HI=PASS 24.576MHZ TO HSP
XBLOCK-OUT : LOW=CLOCK-IN SELECT 24.576MHZ
#HICLOCK-IN : HI=CLOCK-IN SELECT 14.318MHZ



Title: CHI8738B PCI 4 CHANNEL AUDIO WITH MODEM
Size: Document Number: MF-530
C
Date: June 17, 1999
Sheet: 16 of 19

J7: MODEM
OPEN = ENABLE
SHORT = DISABLE

J9: MODEM
OPEN = ENABLE
SHORT = DISABLE

MODEM IN
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CON10

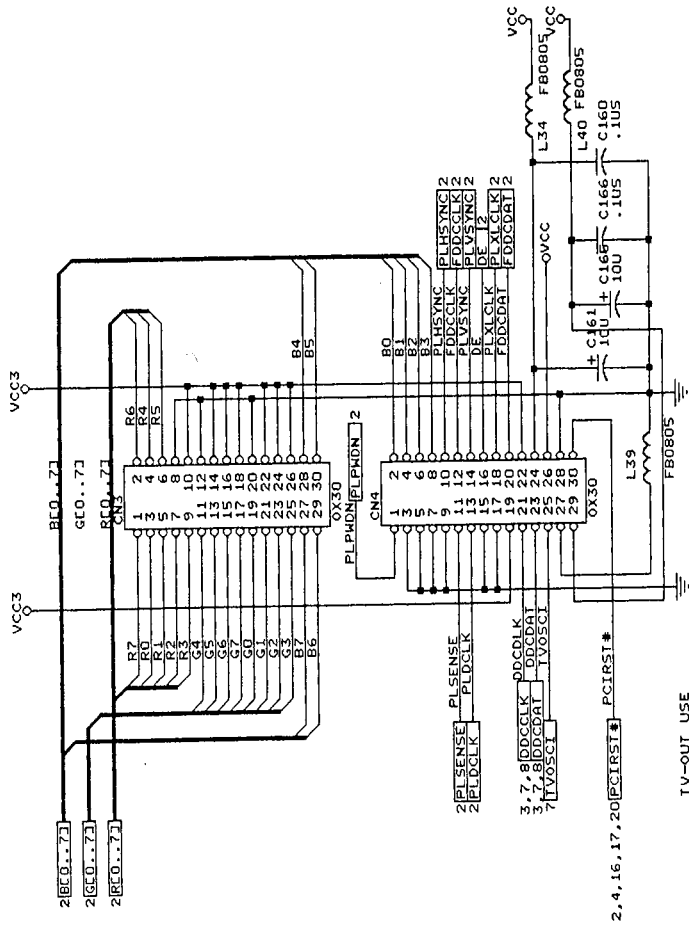
MODEM IN
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SL
SR
CON10

MODEM IN
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CON10

MODEM IN
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SR
CON10

MODEM IN
MIN
SL
SR
CON10

MODEM IN
MIN
SL
SR
CON10



THREE OPTIONS :

- 1 : You can use S1140(TMS) & DFP connector on board.
- 2 : You can use DS90CF561(LVDS) & 74F374 & DFP connector on board.
- 3 : You can use SIS-DFF connector on board.

FOR

- 1: TV-OUT FOUNDATION.
- 2: PANEL LINK FOUNDATION.
- 3: LVDS FOUNDATION.



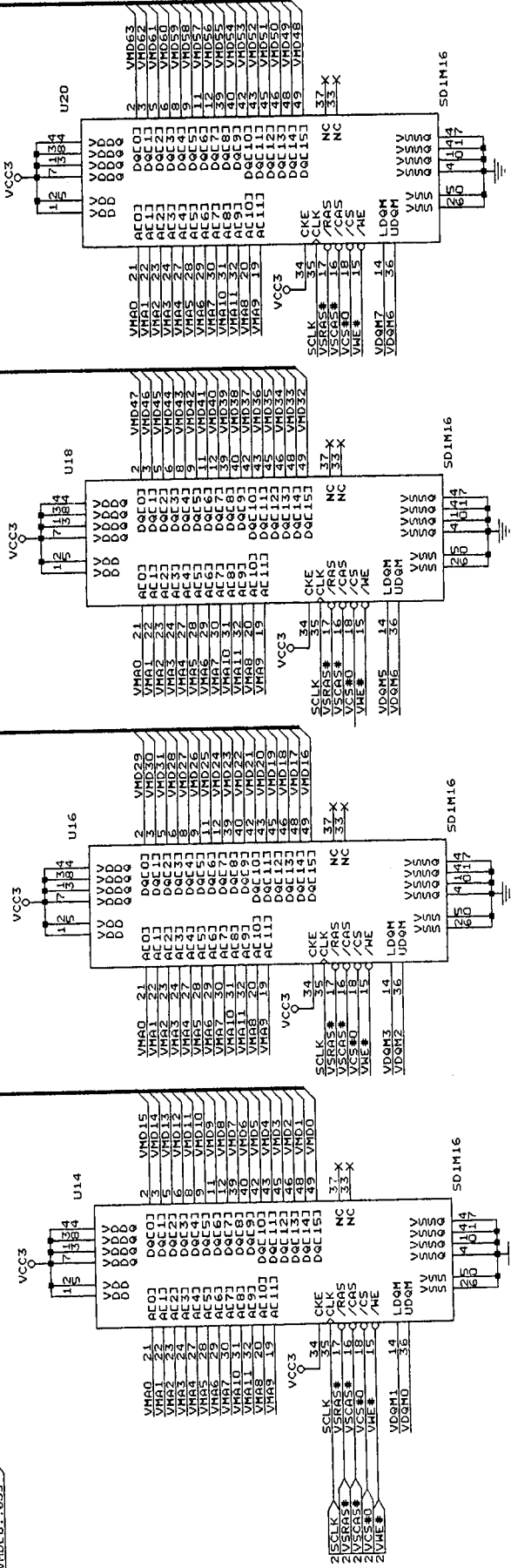
TECHNICA HOUSE CORP.

2[VDDME0..173] VDDME0..173
 2[VHRC0..117] VHRC0..117

4M

8M

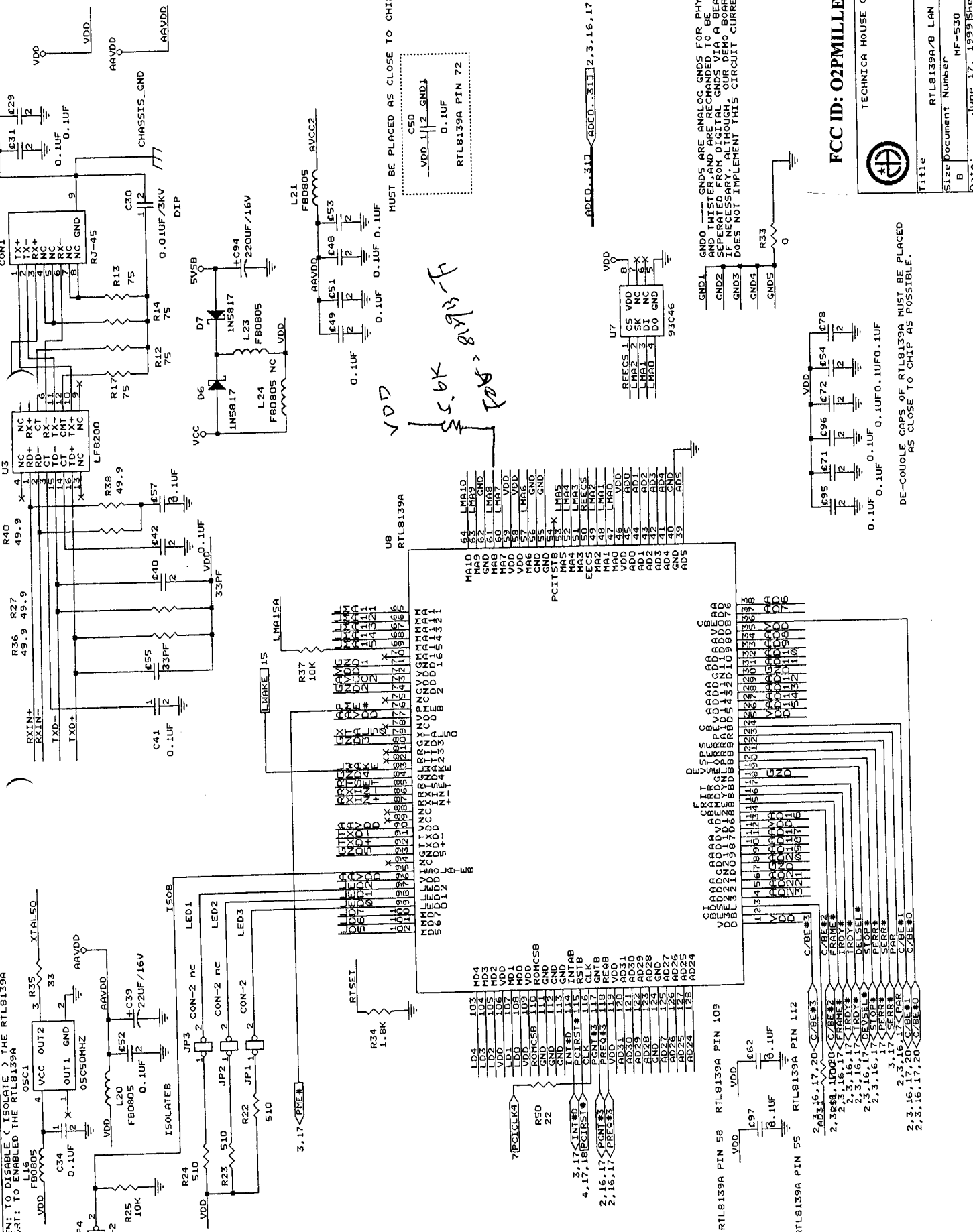
2[VMD0..633] VMD0..633



TECHNICA HOUSE CORP.

Title: SDRAM (IMX16)
 Size Document Number: MF-530
 B
 Date: June 14, 1999 Sheet 19 of 20

OPEN: TO DISABLE (ISOLATE) THE RTL8139A
 SHORT: TO ENABLE THE RTL8139A



FCC ID: 02PMILLENNIUM-100



TECHNICA HOUSE CORP.

Title

RTL8139A/8 LAN CHIP

Size Document Number MF-530

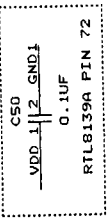
Date: June 17, 1999 Sheet 20

DE-COUPLE CAPS OF RTL8139A MUST BE PLACED AS CLOSE TO CHIP AS POSSIBLE.

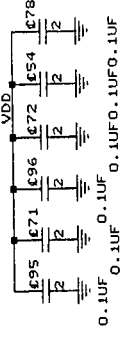
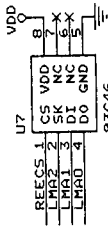
GND1 GND2 GND3 GND4 GND5
 GND1 GND2 GND3 GND4 GND5
 GND1 GND2 GND3 GND4 GND5
 GND1 GND2 GND3 GND4 GND5
 GND1 GND2 GND3 GND4 GND5

ADCC...311 ADCC...313 2,3,16,17

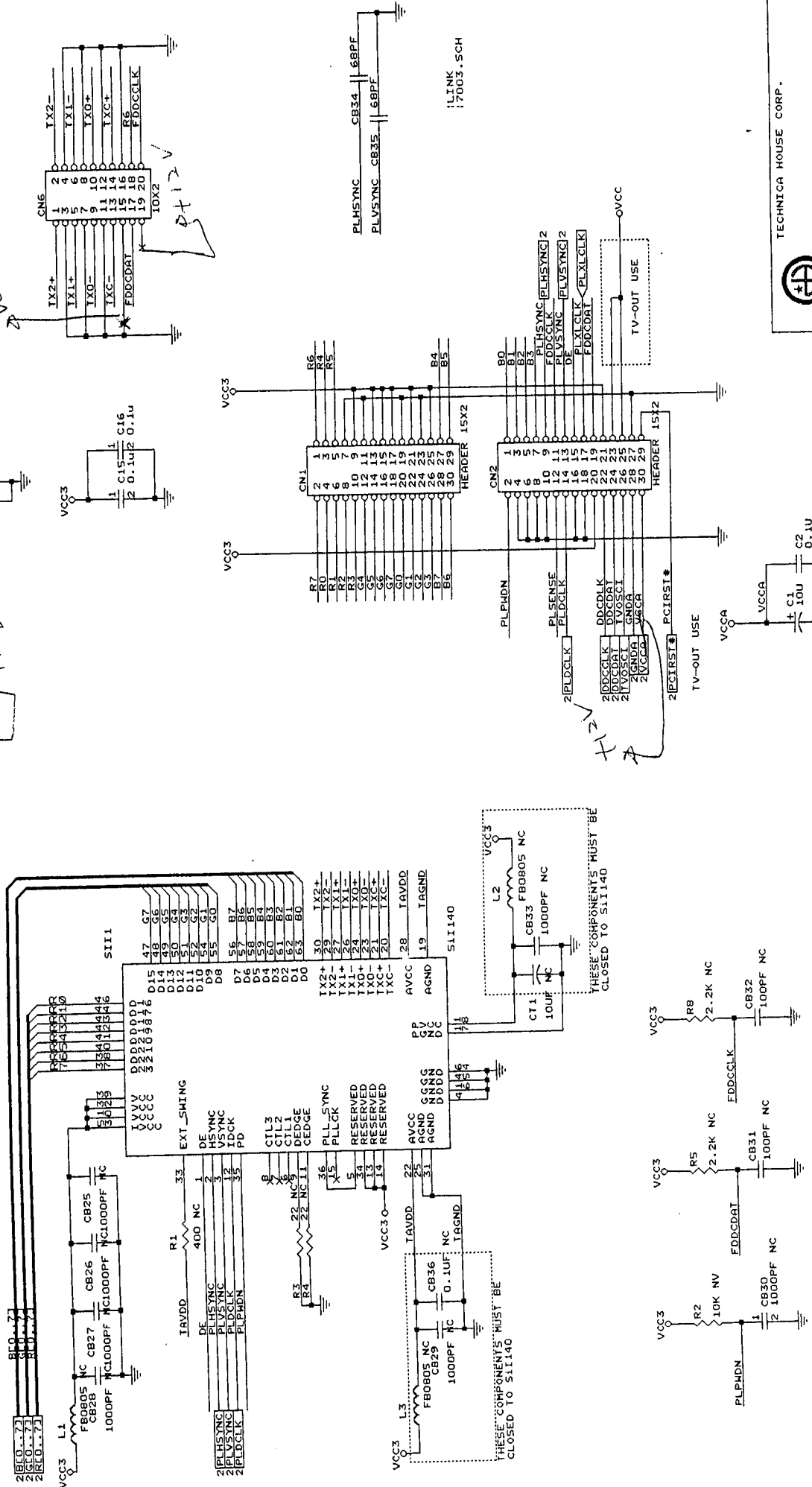
MUST BE PLACED AS CLOSE TO CHIP



VDD
 5.6K
 8.2K
 10K



THREE OPTIONS :
 1 : You can use Si1140(TMDS) & DFP connector on board.
 2 : You can use DS90C56(LVDS) & 74F374 & DFP connector on board.
 3 : You can use SIS-DFP connector on board.



FOR DOT BOARD