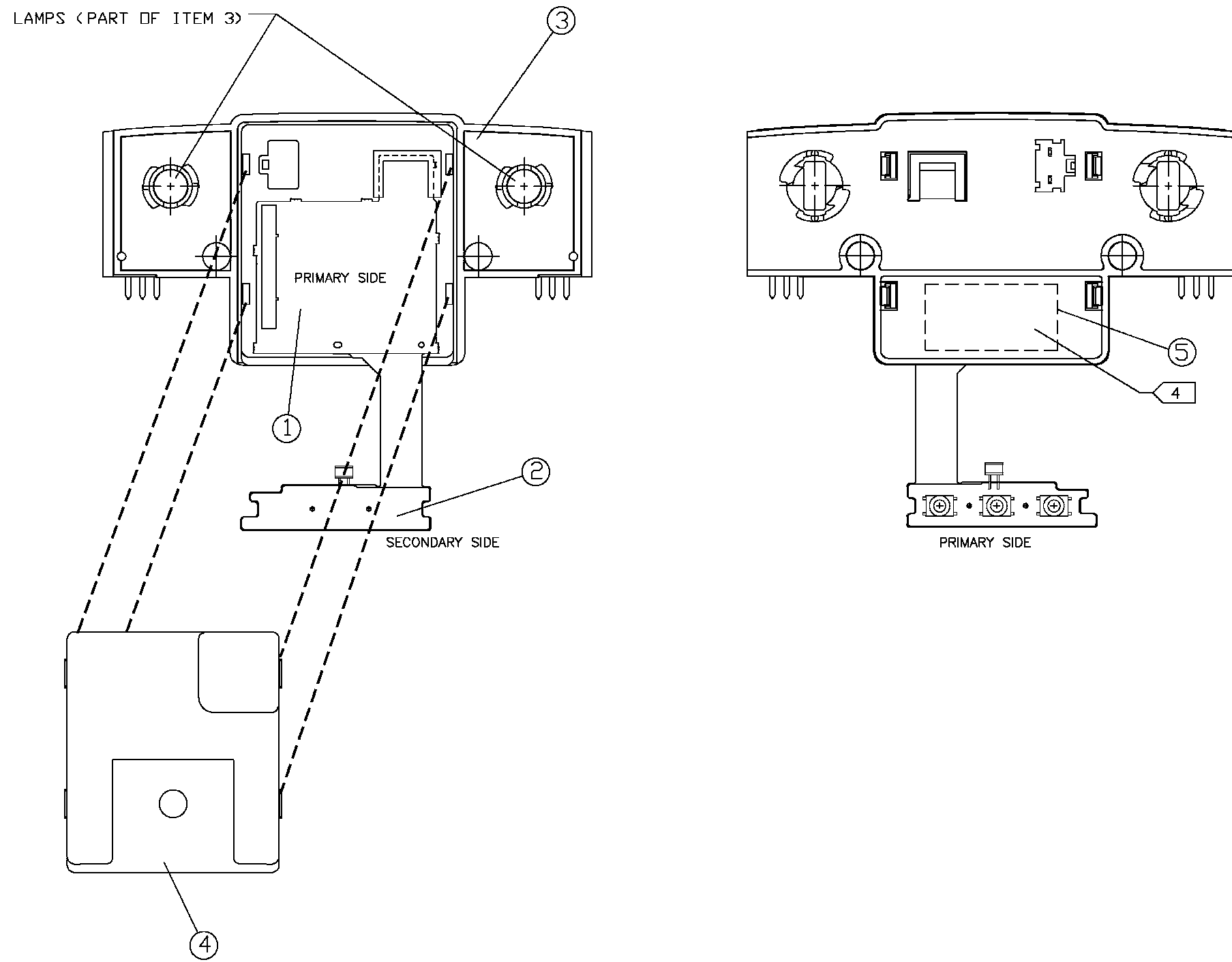


REVISION HISTORY				
REV	DESCRIPTION	BY/DATE	ECO NO.	APPROVED
AAA	RELEASE TO PRODUCTION FROM JCI DWG. 1228190 REV. 7 PART 1228190 REV. 7	CDP 1/14/14	-	-



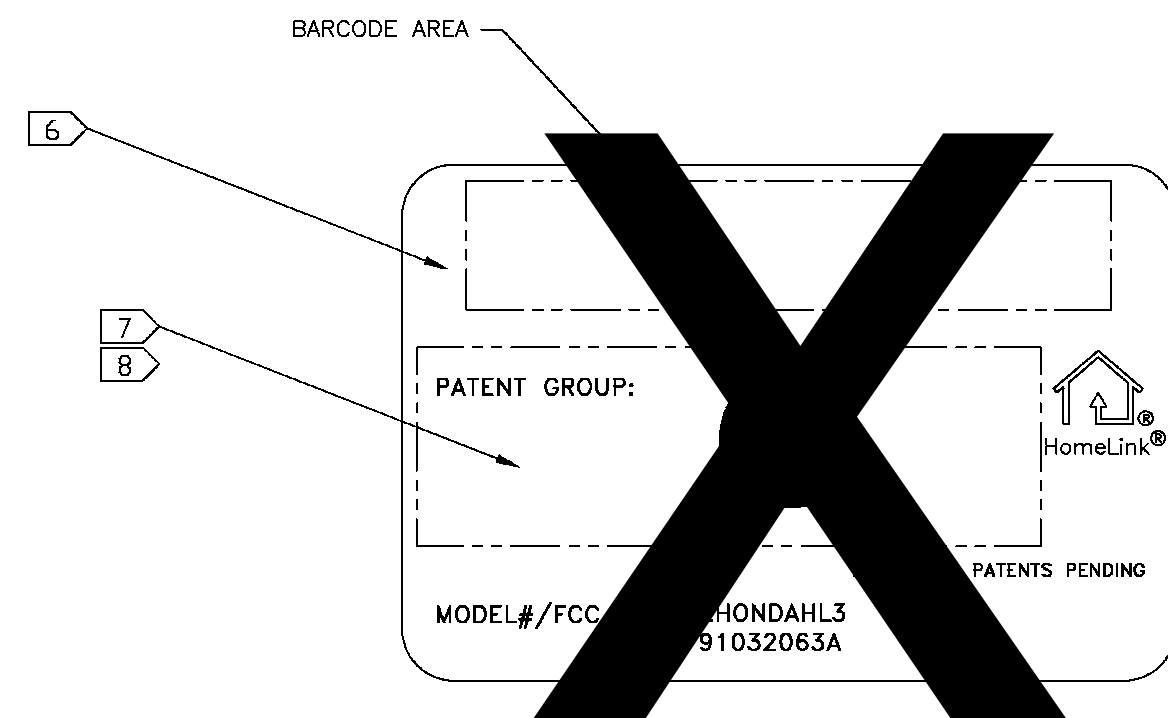
- NOTES:
1. REFERENCE IPC-A-610 STANDARDS FOR ASSEMBLY PRACTICES AND PROCEDURES, UNLESS OTHERWISE SPECIFIED.
 2. ATTACH ITEM 2 TO ITEM 1 USING LAP SOLDER PROCESS.
 3. AFTER LAP SOLDER, ASSEMBLY MUST BE FUNCTIONALLY TESTED.
 4. ATTACH BAR CODE/PATENT NUMBER LABEL (ITEM 5) TO OUTSIDE OF HOUSING IN AREA INDICATED. DO NOT COVER MARKINGS ON HOUSING.
 5. TO ASSEMBLE INTO PLASTICS, INSERT MAIN BOARD (ITEM 1) INTO EDGE CONNECTOR INTERLOCK ON ITEM 3, THEN SNAP ON RETAINER (ITEM 4) AS SHOWN.
 6. ASSEMBLER IS TO PRINT LABEL INFORMATION AS SHOWN, USING ADHESIVE LABEL (ITEM 5).
 7. REFER TO LATEST REVISION OF DRAWING VJ0245 FOR ACTUAL PATENT NUMBERS FOR THE PATENT GROUP IDENTIFIED.
 8. LAY OUT PATENT NUMBERS TO FILL SPACE AVAILABLE, BUT NATION / ORGANIZATION OF ORIGIN HEADINGS MUST BE MAINTAINED FOR CORRESPONDING PATENT NUMBERS AS SHOWN ON LATEST REVISION OF DRAWING VJ0245.
 9. THE NVM ON EACH PART WILL BE PROGRAMMED WITH THE FOLLOWING POWER LEVEL SETTINGS FOR FCC COMPLIANCE.

REGION	FREQ. RANGE (MHz)	ROM POWER LEVEL FOR U101 (PART OF ITEM 1)	NVM ADDRESS	FCC CERTIFIED POWER LEVEL (NVM DATA)	NEEDS TO BE PROGRAMMED IN NVM
0	288-303	0x0AF3	0x0D	0x0CF1	YES
1	303-321	0x0CF1	0x0E	0x0AF3	YES
2	336-360	0x13EA	0x0F	0x0BF2	YES
3	360-380	0x13EA	0x10	0x0FEE	YES
4	380-399	0x13EA	0x11	0x19E5	YES
5	411-420	0x15E8	0x12	0x11EC	YES
DEFAULT CODE POWER		0x32CB	0x0A	0x2AD3	YES
VEHICLE ID		0x00FD	0x13	-	NO
MAX TX POWER		0x0EFF	0x14	-	NO

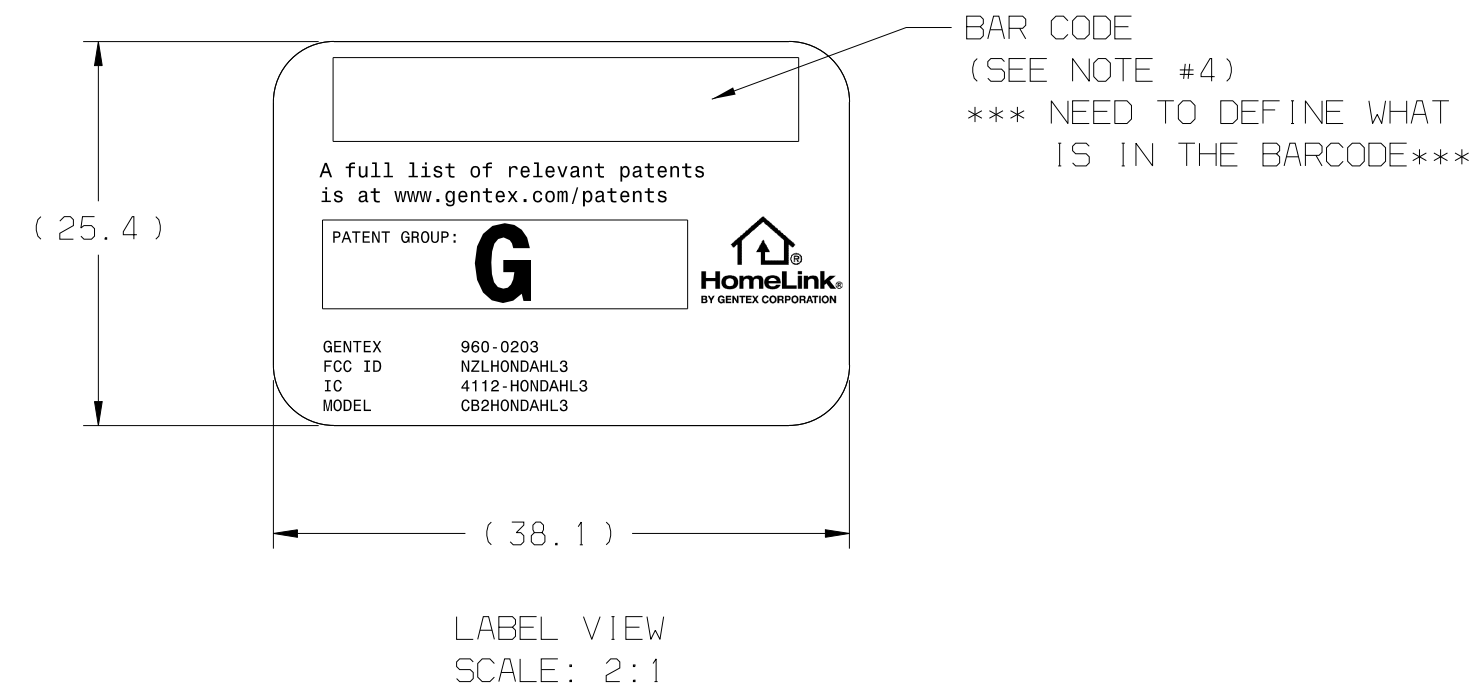
- NOTE: THE NVM VALUE INCLUDES A SELF CHECK. HIGH BYTE IS THE ACTUAL VALUE. LOW BYTE IS COMPLEMENT OF HIGH BYTE + 2.
10. TRAINING SENSITIVITY:
- A. THE RECEIVER THRESHOLD IS PROGRAMMED IN NVM ADDRESS 0x04.
 $RECEIVER\ THRESHOLD = [0.5386 * ((AVERAGE\ 250_ON\ AND\ 450_ON) - (AVERAGE\ 250_OFF\ AND\ 450_OFF))] * 0.45 + (AVERAGE\ 250_OFF\ AND\ 450_OFF)] - 10.169$
 - B. THE RECEIVER THRESHOLD IS PUT IN THE UPPER BYTE, THE LOWER BYTE IS THE UPPER BYTE +2, THEN INVERTED.
11. MUST COMPLY WITH TEST SPECIFICATION TD-ELE-049.

PART NUMBER: 1228190 REV 7

LOCATION	DESCRIPTION	PART#	QTY	PACKAGE
1	BOARD PC HMLK3 ASM 3D REV B	VR9036-B	1	TH_FAKEPART
2	BOARD PC HONDA FLEX 3V80 REV B	VC2266-B	1	TH_FAKEPART
3	HSG ASM LWR CARRIER WITH BULBS	1129329	1	TH_FAKEPART
4	HSG ASM UPR RETAINER WITH FIDAM	VAB895-F	1	TH_FAKEPART
5	LABEL STOCK, ORANGE 1.0 X 1.5	VJ8986	1	TH_FAKEPART



ELECTRONICS STD. UNLESS OTHERWISE SPECIFIED		PROJECT: 01 HONDA OHC HMLNK III		DRW. BY: ARON HOEVENAAR	DATE: 29FEB00
THIRD ANGLE PROJECTION		COND. BY: BRET SUNNERVERILLE		DATE: 29FEB00	
DO NOT SCALE		CUSTOMER: HONDA		ENGR. APPROV: TROY REDDER	DATE: 29FEB00
DIMENSIONS ARE IN INCHES WITH EQUIVALENT MILLIMETERS SHOWN IN BRACKETS.		SCALE: NONE		SHEET: 1 OF 1	
D		DWG. NO. 1228190		X	



****PRELIMINARY****

QTY	ITEM	DESCRIPTION	JCI PART NO.	GENTEX PART NO.
1	5	LBL. STOCK, 1.0X1.5 ORANGE	VJ8986	375-0896-000
1	4	HSG ASM, UPPER RETAINER W/FOAM	VAB895	625-0955-000
1	3	HSG ASM, LWR CARRIER W/BULBS	1129329	625-0952-000
1	2	FLEX ASM, HONDA	VC2266	725-0048-000
1	1	PCB ASM.	VR9036	605-5495-001

QTY	ITEM	DESCRIPTION	JCI PART NO.	GENTEX PART NO.
1	5	LBL. STOCK, 1.0X1.5 ORANGE	VJ8986	375-0896-000
1	4	HSG ASM, UPPER RETAINER W/FOAM	VAB895	625-0955-000
1	3	HSG ASM, LWR CARRIER W/BULBS	1129329	625-0952-000
1	2	FLEX ASM, HONDA	VC2266	725-0048-000
1	1	PCB ASM.	VR9036	605-5495-001

GENTEX CORPORATION
 600 NORTH CENTENNIAL ST
 ZEELAND, MICHIGAN 49464
 (616) 772-1800

TITLE: FINAL ASM, HONDA OHC HMLK3, MY01		SIZE: D	DWG. NO.: 960-0203	REV: AAA
NVS. SPECIFIES FEATURES REQUIRING SPECIAL CONSIDERATION. SEE CONTROL PLAN. INTERPRET DIMENSIONS AND TOLERANCE PER ASME Y14.5M - 1994		MATERIAL: SEE DWG.	MASS (CALCULATED): na	DATA FORMAT: CATIA V5 SHEET
THIRD ANGLE PROJECTION		DRAWN BY: CPOLLACK	DATE: 2/21/2014	1 OF 1