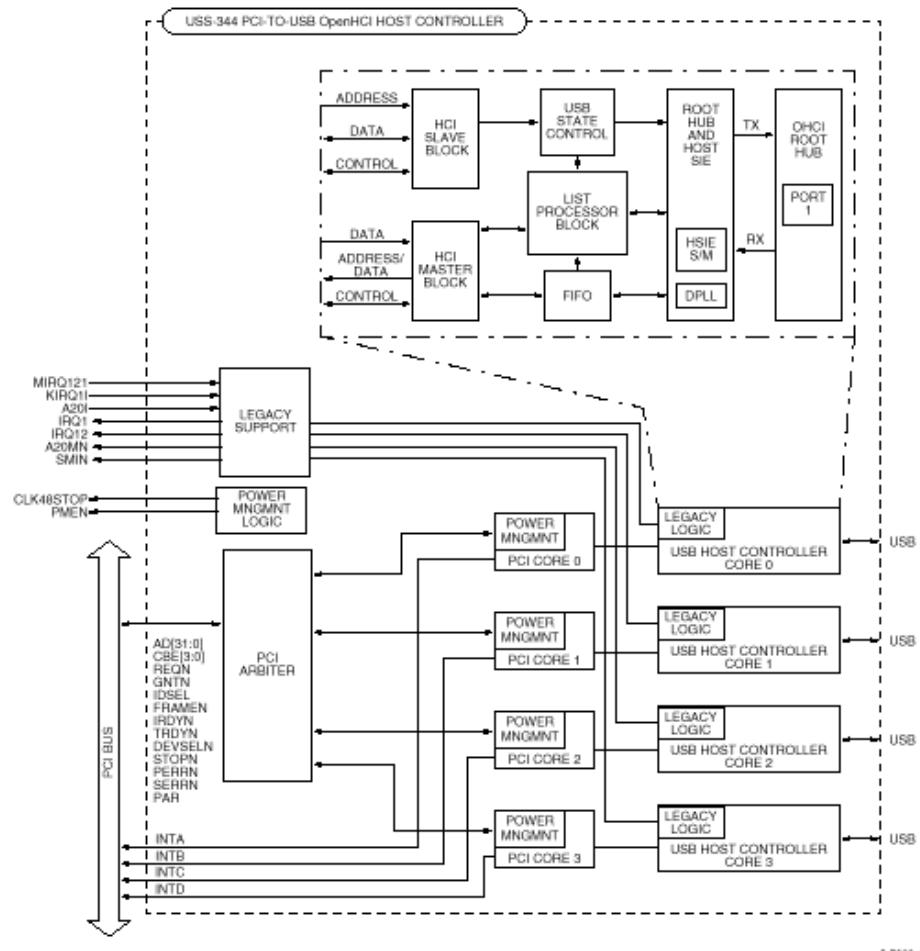


UT-L4P Total Block Diagram Description

Description (continued)



5-7828.r1

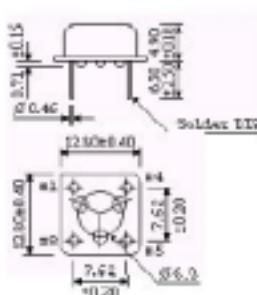
Figure 1. USS-344 Interconnection Diagram

Cycle: 48MHz

Oscillate Total Block Diagram Description

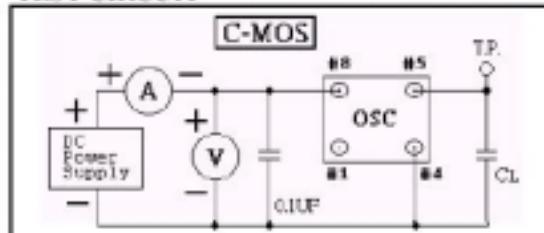
FREQUENCY RANGE:	1-100MHZ	
FREQUENCY STABILITY:	A:(+/-)100PPM B:(+/-)50PPM C:(+/-)25PPM	
(INCLUSIVE OF CALIBRATION TOLERANCE AT 25C, OPERATION TEMPERATURE RANGE, INPUT VOLTAGE CHANGE, LOAD CHANGE, AGING, SHOCK AND VIBRATION)		
TEMPERATURE RANGE:		
OPERATING TEMPERATURE	-10C TO 70C	
STORAGE TEMPERATURE	-55C TO 125C	
INPUT VOLTAGE:	4.5-5.5 VDC	
INPUT CURRENT:	1-20MHZ	20mA MAX
	21-35MHZ	30mA MAX
	36-50MHZ	40mA MAX
	51-100MHZ	55mA MAX
CMOS OUTPUT:		
SYMMETRY	60/40% TO 40/60% (AT 2.5VDD)	
RISE AND FALL TIME		
	1-50MHZ	10NS MAX
	51-100MHZ	5NS MAX
OUTPUT LOAD:	15PF	

DIMENSIONS (UNIT mm)



Pin connection	
PIN	CONNECTION
1	NC or Tri - State
4	CASE GND
5	OUT PUT
8	5.0VDC

TEST CIRCUIT



OUTPUT WAVEFORM

