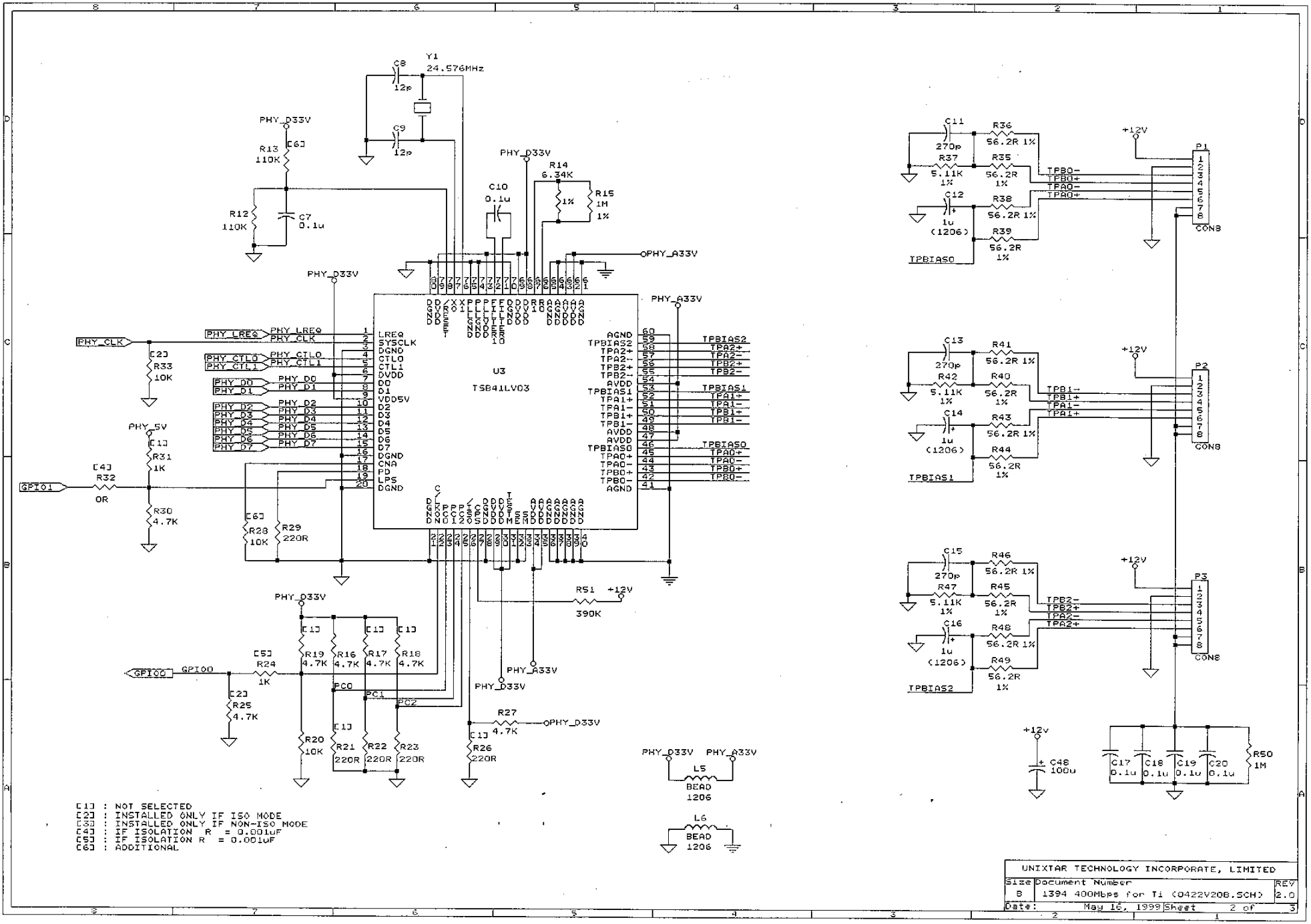
EXHIBIT D

Circuit Diagram


Y1
24.576MHz
C8
12p
C9
12p
PHY_D33V
R13
110K
C6]
R12
110K
C7
0.1u
R14
6.34K
1%
R15
1M
1%
C10
0.1u
OPHY_A33V
PHY_A33V
U3
TSB41LV03
PHY_CLK
PHY_LREQ
PHY_CTL0
PHY_CTL1
PHY_D0
PHY_D1
PHY_D2
PHY_D3
PHY_D4
PHY_D5
PHY_D6
PHY_D7
C2]
R33
10K
PHY_5V
C1]
R31
1K
C4]
R32
OR
R30
4.7K
GPIO1
C6]
R28
10K
R29
220R
R51
390K
+12V
C1]
R19
4.7K
R16
4.7K
R17
4.7K
R18
4.7K
C5]
R24
1K
GPIO0
C2]
R25
4.7K
R20
10K
PC0
PC1
PC2
C1]
R21
220R
R22
220R
R23
220R
R27
4.7K
C1]
R26
220R
L5
BEAD
1206
L6
BEAD
1206
C11
270p
R37
5.11K
1%
R36
56.2R 1%
R35
56.2R
1%
C12
1u
(1206)
R38
56.2R 1%
R39
56.2R
1%
TPBIAS0
TPB0-
TPB0+
TPA0-
TPA0+
P1
CON8
C13
270p
R42
5.11K
1%
R41
56.2R 1%
R40
56.2R
1%
C14
1u
(1206)
R43
56.2R 1%
R44
56.2R
1%
TPBIAS1
TPB1-
TPB1+
TPA1-
TPA1+
P2
CON8
C15
270p
R47
5.11K
1%
R46
56.2R 1%
R45
56.2R
1%
C16
1u
(1206)
R48
56.2R 1%
R49
56.2R
1%
TPBIAS2
TPB2-
TPB2+
TPA2-
TPA2+
P3
CON8
C48
100u
C17
0.1u
C18
0.1u
C19
0.1u
C20
0.1u
R50
1M
C1] : NOT SELECTED
C2] : INSTALLED ONLY IF ISO MODE
C3] : INSTALLED ONLY IF NON-ISO MODE
C4] : IF ISOLATION 'R' = 0.001uF
C5] : IF ISOLATION R = 0.001uF
C6] : ADDITIONAL
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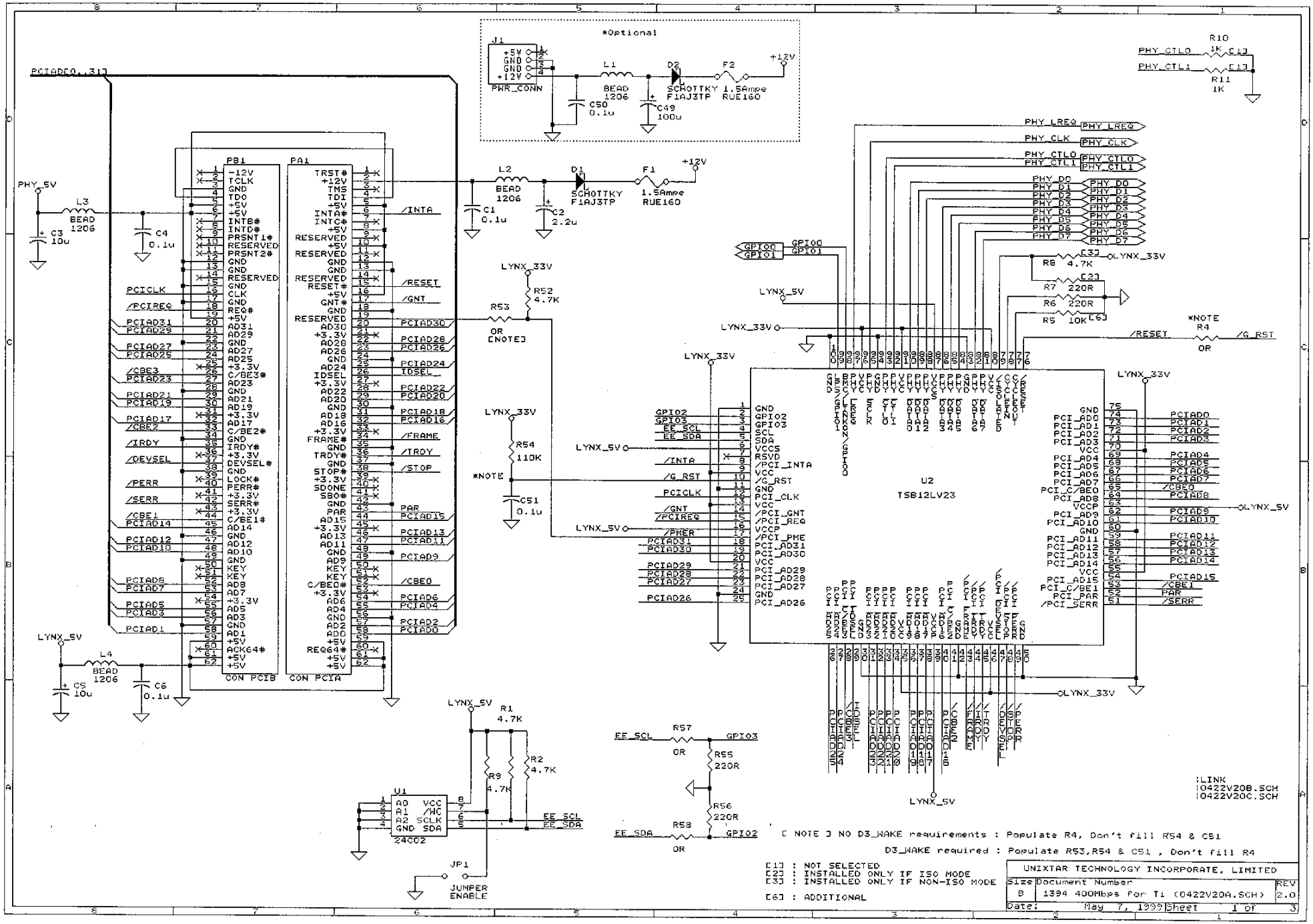
Size B
Document Number 1394 400Mbps for Ti (0422V208.SCH)
REV 2.0
Date: May 16, 1999
Sheet 2 of 3


*Optional
J1
PWR_CONN
L1
BEAD 1206
C50 0.1u
C49 100u
D2
SCHOTTKY 1.5Ampe F1AJ3TP
F2
RUE160
+12V
PCIAD[0..31]
PB1
PA1
CON PCIB
CON PCIA
PHY_5V
L3 BEAD 1206
C3 10u
C4 0.1u
L2 BEAD 1206
C1 0.1u
C2 2.2u
D1 SCHOTTKY F1AJ3TP
F1 1.5Ampe RUE160
LYNX_5V
L4 BEAD 1206
C5 10u
C6 0.1u
LYNX_33V
R52 4.7K
R53
OR [NOTE]
R54 110K
*NOTE
C51 0.1u
R1 4.7K
R2 4.7K
R9 4.7K
U1
24C02
JP1
JUMPER ENABLE
PHY_LREQ
PHY_CLK
PHY_CTL0
PHY_CTL1
PHY_D0
PHY_D1
PHY_D2
PHY_D3
PHY_D4
PHY_D5
PHY_D6
PHY_D7
R10 1K
R11 1K
R8 4.7K
R7 220R
R6 220R
R5 10K
*NOTE
R4
OR
/RESET
/G_RST
GPIO0
GPIO1
U2
TSB12LV23
R57
R55 220R
R56 220R
R58
EE_SCL
EE_SDA
GPIO3
GPIO2
LINK
0422V20B.SCH
0422V20C.SCH
[NOTE] NO D3_WAKE requirements : Populate R4, Don't fill R54 & C51
D3_WAKE required : Populate R53,R54 & C51 , Don't fill R4
[1] : NOT SELECTED
[2] : INSTALLED ONLY IF ISO MODE
[3] : INSTALLED ONLY IF NON-ISO MODE
[6] : ADDITIONAL
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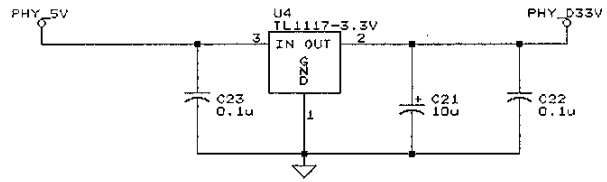
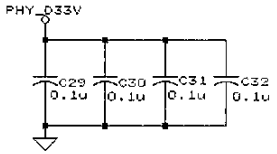
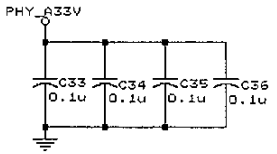
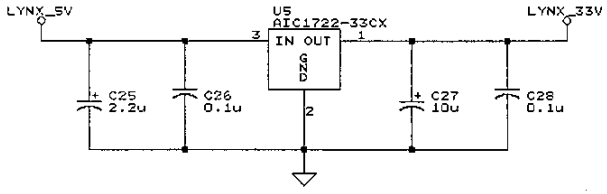
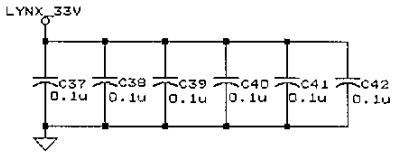
Date: May 7, 1999
Sheet 1 of 3

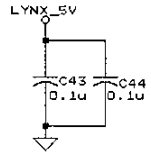












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Date: May 16, 1999	Sheet 3 of 3	