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**SUBJECT: Declaration of scheduled changes to the Brady IP Printer Series**

To all concerned parties:

Please accept this letter as a declaration identifying planned modifications to improve the Brady IP Series printer. The following information should be considered a comprehensive list which details the planned improvements:

	FUNCTION	OLD PART	NEW PART	REASON FOR CHANGE	TRANSMITTER EFFECTED
I	FPGA	Altera EP1C3	Acetel ProASIC 3	Improve Integration and improved solderability	No
II	Motor Driver	Trinamic TMC249/A	Allegro A3985	Reduce acoustic noise of motor with better driver	No
III	Power Choke	None	Wurth Elektronik 7447785006	Suppress noise at 100 kHz on the internal USB connectors to improve printer EMC	No
IV	Oscillator	Epson SG-8002JC Series	SiTime SiT1238	Reliability, improved manufacturing performance, better immunity performance	No
V	LCD Controller	Cypress CY7C63923-PVXC	Cypress CY8C24894-24LFX1	Improved performance, old part becoming obsolete	No
VI	Ferrite Ring	None	Kaschke K2004 R16/9,6/6,3	Reduce parasitic noise on the LAN Cable	No
VII	Clock IC	SiT1238AC-43-33S-T	SiT9001AC-43-33S3-33.33330	Reduces harmonics of the 33.333 MHz Clock	No

As part of the changes, the IP Printer will modify the geometry of the main board (as shown to the right) for ease of manufacturability. The board will have minor changes in the layout to accommodate changes listed above, as well as to improve solderability and versatility for second source connectors. Lastly, the start-up sequence will change via the firmware to improve reliability during the power up procedure.

*The IP Printer will not undergo any changes in external appearance. All changes will be made within the enclosure of the printer, and will not be made visible to the end user.*




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Date May 18, 2010