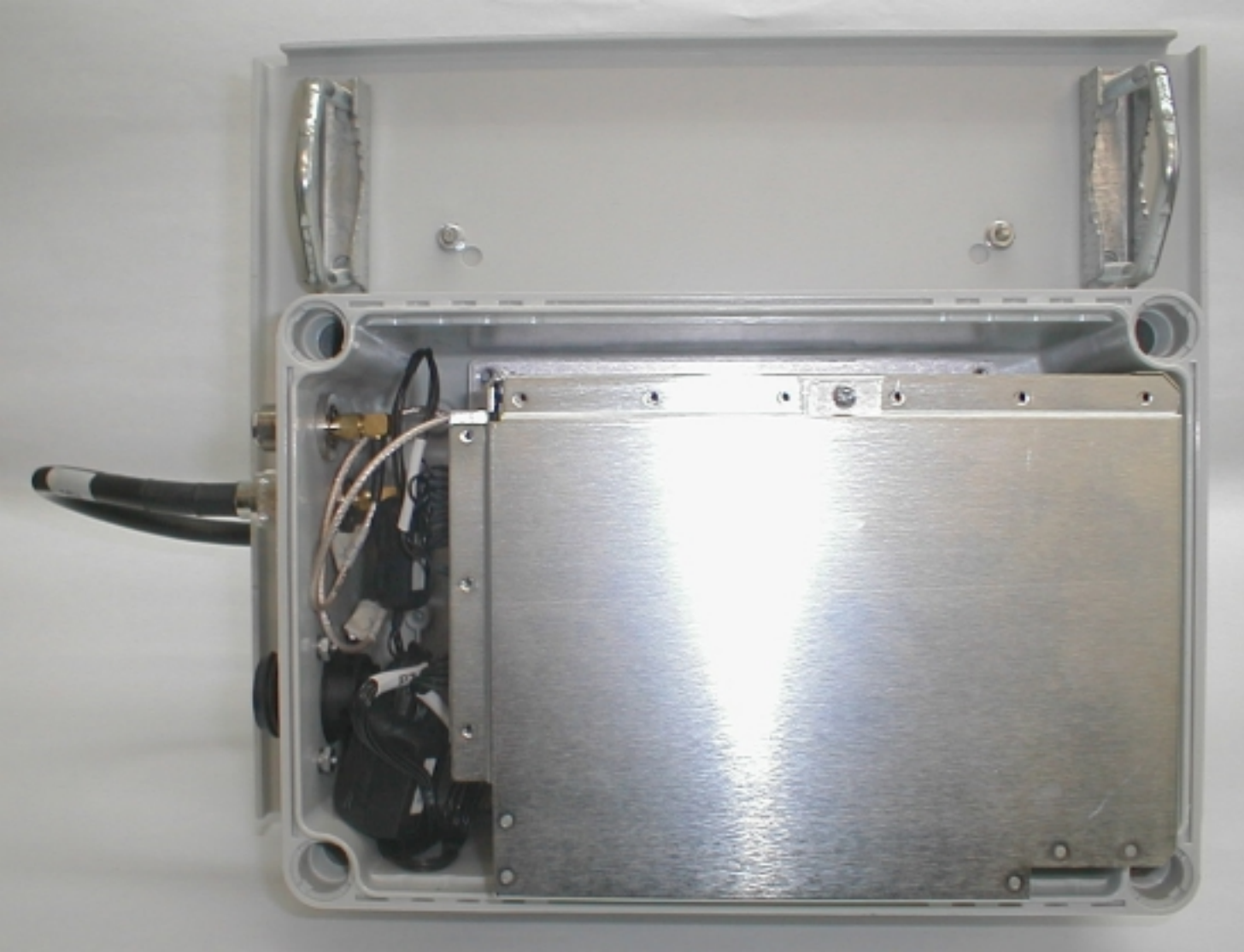


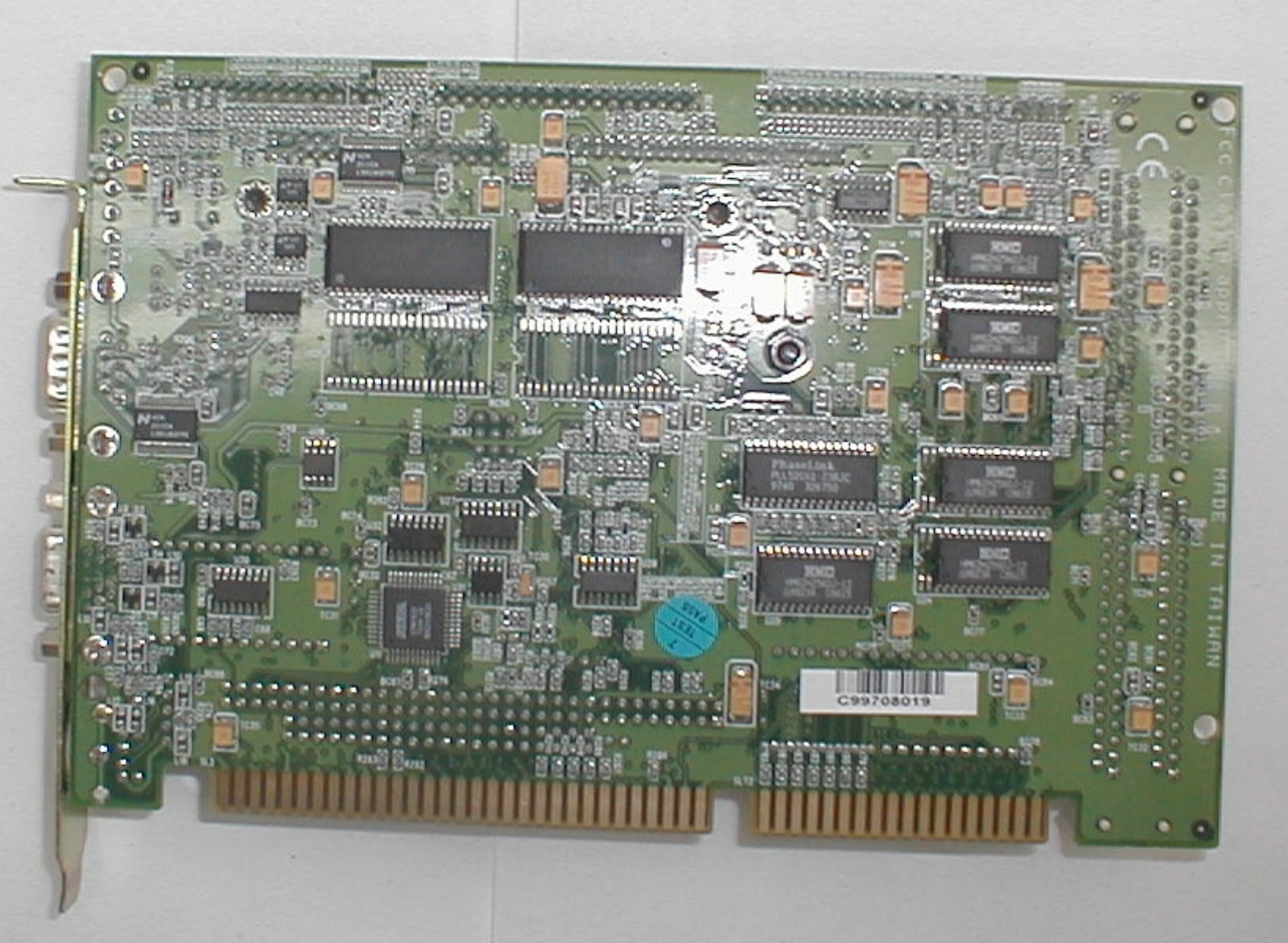
**RSC unit open**



CPU component side



CPU print side



**DMU component side**



DMU print side



RFU component side with RF shield



RFU component side without RF shield



RFU print side

