

CIRCUIT DIAGRAMS

PARA 2.983 (d)(7)

Circuit Diagrams

PWB Drawing No.	PCB Drawing No.	Description	Sheet No.
TVK 117 3014 I1	ROA 117 3391	Logic/Radio Interface	1 of 18
TVK 117 3014 I2	ROA 117 3391	Level Shift & Inverter Circuits	2 of 18
TVK 117 3014 L1	ROA 117 3391	System Connector & Interface Circuitry	3 of 18
TVK 117 3014 L2	ROA 117 3391	Gessica2 Charger/Voltage Regulators	4 of 18
TVK 117 3014 L3	ROA 117 3391	Susan ASIC	5 of 18
TVK 117 3014 L4	ROA 117 3391	System Memory	6 of 18
TVK 117 3014 L5	ROA 117 3391	DSP	7 of 18
TVK 117 3014 L6	ROA 117 3391	Audio I	8 of 18
TVK 117 3014 L7	ROA 117 3391	Audio II	9 of 18
TVK 117 3014 L8	ROA 117 3391	LCD Character Display	10 of 18
TVK 117 3014 L9	ROA 117 3391	Keypad and Backlight	11 of 18
TVK 117 3014 R1	ROA 117 3391	RX Front End	12 of 18
TVK 117 3014 R2	ROA 117 3391	RX Interface	13 of 18
TVK 117 3014 R3	ROA 117 339	Synthesizer/ TX Modulator	14 of 18
TVK 117 3014 R4	ROA 117 3391	TX Filters	15 of 18
TVK 117 3014 R5	ROA 117 3391	TX Power Amplifier	16 of 18
TVK 117 3014 R6	ROA 117 3391	Antenna Interface	17 of 18
TVK 117 3014 R7	ROA 117 3391	Voltage Regulators	18 of 18

NOTE: See enclosed envelope marked CONFIDENTIAL for all referenced schematics/drawings.