

## HANDSET BLOCK DIAGRAM DESCRIPTION

The handset consist of CPU (ASJ001H), single chip cordless telephone IC U3600BM. Keyboard, LCD. Charging battery (3.6V/200mA).

### RX ILLUSTRATION

At standby mode, the charging battery supply DC 3.6V to the whole unit, there is a RF signal 46MHz from base reach to the antenna and duplex filter will be amplified by consist of (Q1, R1, R3, L2, C6, C7), from pin40, pin41 input U3600BM internal mixer1, pin39 mixer1 output frequency 10.7 MHz IF, after 10.7 MHz IF filter, from pin33 input mixer2 of U3600BM, pin36 mixer2 output frequency 455KHz IF, after 455KHz IF filter, from pin31 input IF amplified and demodulator. Pin15 output RXDATA to CPU decode; pin27, pin28 output audio signal driver receiver.

### TX ILLUSTRATION

CPU TXDATA from C21, R21 input pin10 of U3600BM internal modulator; MIC audio signal from C19, R11 input U3600BM internal amplified, pin23 output. From C20 input pin22 of U3600BM internal compressor, pin20 output, from R13, C23 input pin19 of U3600BM internal limiter, pin18 output, after R15, R14 input pin17 of U3600BM internal amplified, pin16 output, after R18 input pin10 of U3600BM internal modulator. Pin3 output signal 49MHz carrier of modulated. After RF amplified (consist of Q4, R26, C37, L4, R24, C35, C36), from C38 radiated through the duplex filter and antenna.

## BASE BLOCK DESCRIPTION

The base consist of CPU (ASJ001B), single chip cordless telephone IC U3600BM, ringer detection circuit (consist of C1, R1, R2, D5, U5, R18), power supply circuit (consist of D14~D17, Q15, Z3, D16, C83), speech circuit (consist of Q1, Q2, Q7, Q8, Q9), charging circuit (consist of T1, D10~D13, Z1, U6)

### RX ILLUSTRATION

At standby mode, the voltage Regulator (Z3, Q15) after stable DC voltage to the whole unit, there is a RF signal 49MHz from base reach to the antenna and duplex filter will be amplified by consist of (Q1, R1, R3, L2, C6, C7), from pin40, pin41 input U3600BM internal mixer1, pin39 mixer1 output frequency 10.7 MHz IF, after 10.7 MHz IF filter, from pin33 input mixer2 of U3600BM, pin36 mixer2 output frequency 455KHz IF, after 455KHz IF filter, from pin31 input IF amplified and demodulator, pin15 output RXDATA to CPU decode; pin27, pin28 output audio signal to speech circuit.

### TX ILLUSTRATION

CPU TXDATA from C21, R21 input pin10 of U3600BM internal modulator; speech circuit audio signal from C19, R11 input U3600BM internal amplified, pin23 output. From C20 input pin22 of U3600BM internal compressor, pin20 output, from R13, C23 input pin19 of U3600BM internal limiter, pin18 output, after R15, R14 input pin17 of U3600BM internal amplified, pin16 output, after R18 input pin10 of U3600BM internal modulator. Pin3 output signal 49MHz carrier of modulated. After RF amplified (consist of Q4, R26, C37, L4, R24, C35, C36), from C38 radiated through the duplex filter and antenna.

Frequency Table

	Tx (Base)	Tx (Handset)
Ch 1	46.610	49.670
Ch 2	46.630	49.845
Ch 3	46.670	49.860
Ch 4	46.710	49.770
Ch 5	46.730	49.875
Ch 6	46.770	49.830
Ch 7	46.830	49.890
Ch 8	46.870	49.930
Ch 9	46.930	49.990
Ch 10	46.970	49.970