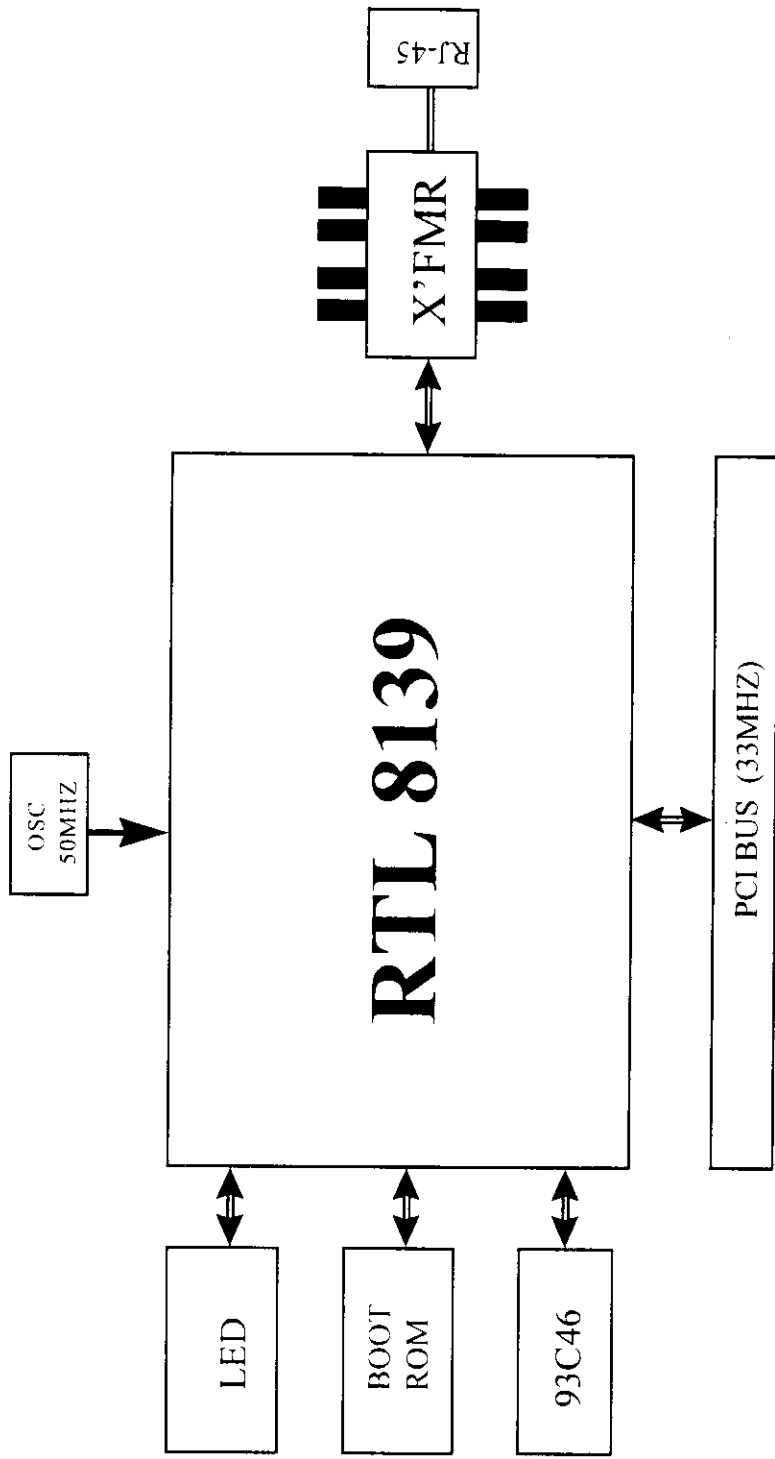


4. Block Diagram(s)

Figure 4.1 Block diagram of system, Page 13.A



EA1210R Block Diagram

NETRONIX, INC.
 PCB, EA1210R, 2/L



NETRONIX	JOB NAME	EAI210L2	ENGINEER: Armin
振興	LAYER	SILKSCREEN - TOP LAYOUT:	Julie
振興	DATE	87.06.10	2 LAYER REV: 1.0

