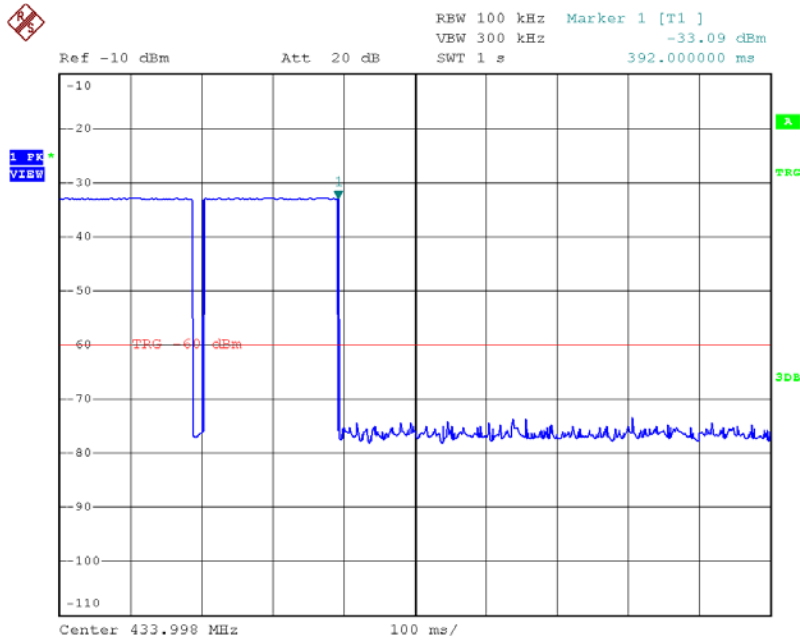


INTERTEK TESTING SERVICES

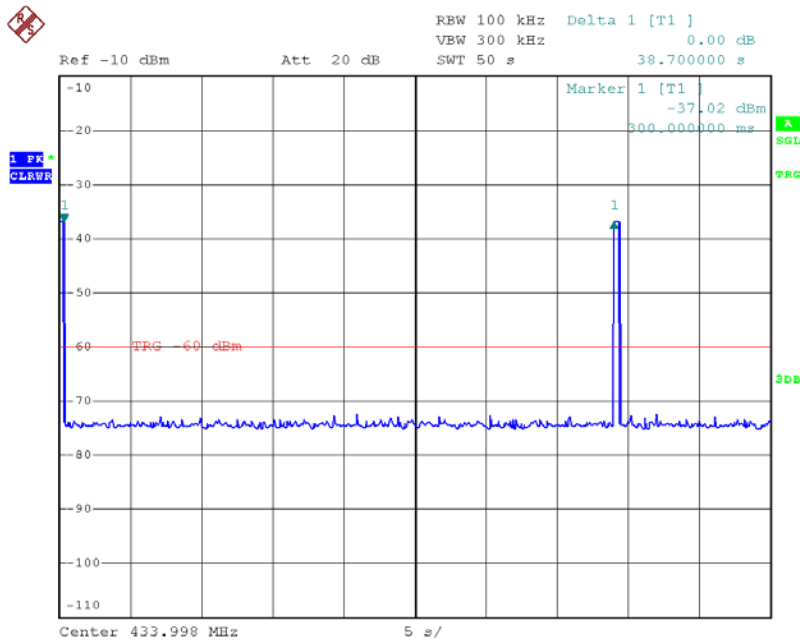
Report No.: HK10110746-1
Sample No.: 4/4

Timing



Date: 23.NOV.2010 20:05:29

Silent period

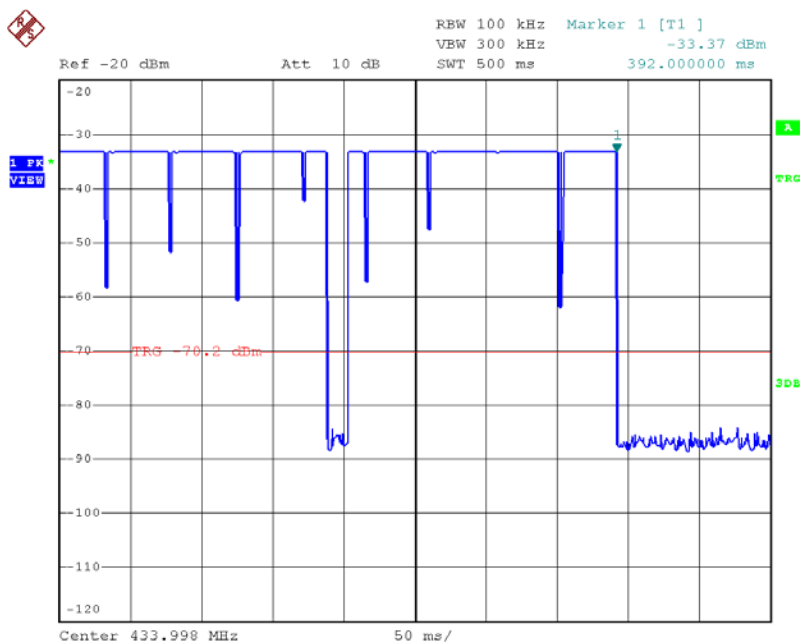


Date: 23.NOV.2010 20:08:10

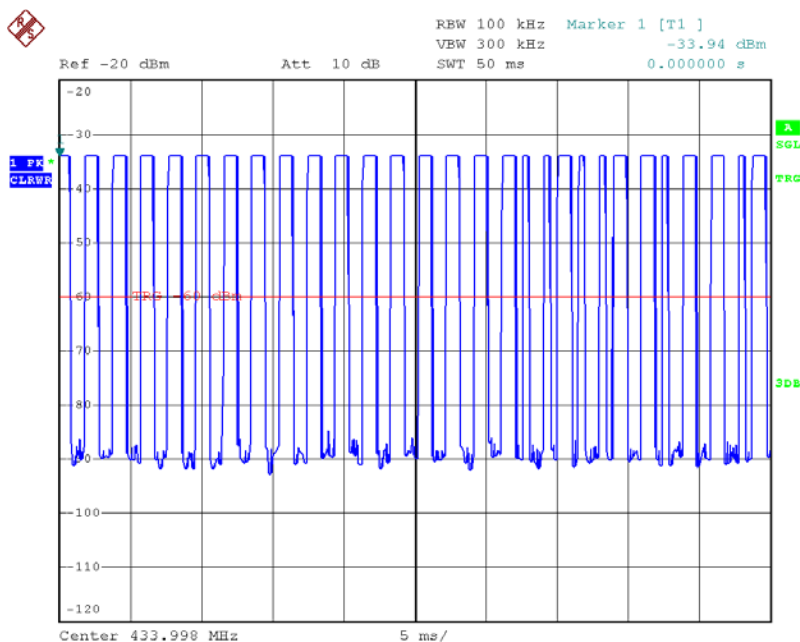
Timing & Silent Plot - Pursuant to FCC Part 15 Section 15.231 Requirement (Pass)

INTERTEK TESTING SERVICES

Report No.: HK10110746-1
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Date: 23.NOV.2010 20:12:55

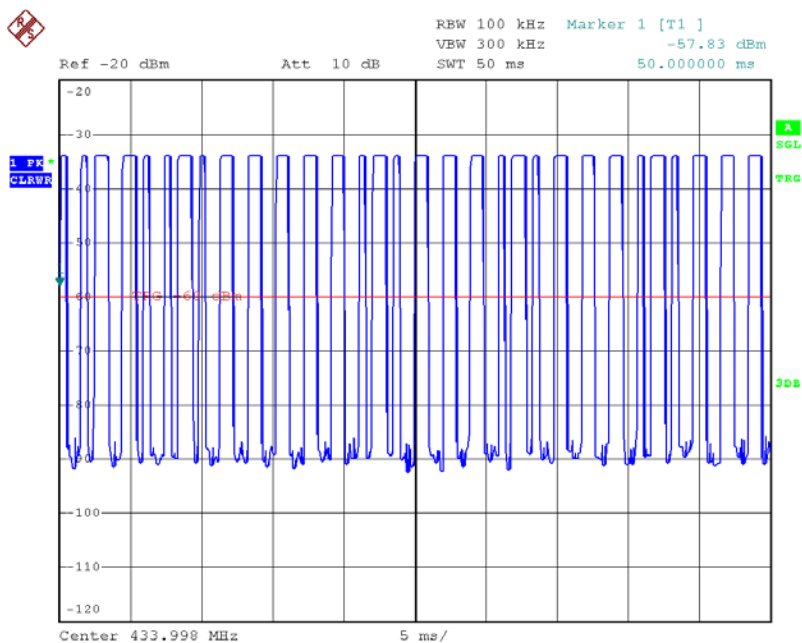


Date: 23.NOV.2010 20:19:49

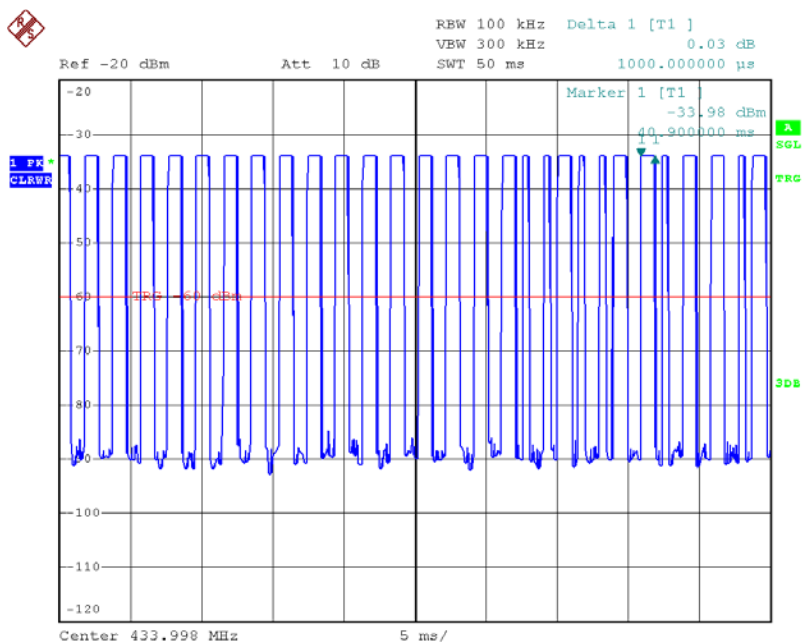
Average Factor Plot - Pursuant to FCC Part 15 Section 15.231 Requirement (Pass)

INTERTEK TESTING SERVICES

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Sample No.: 4/4



Date: 23.NOV.2010 20:18:43

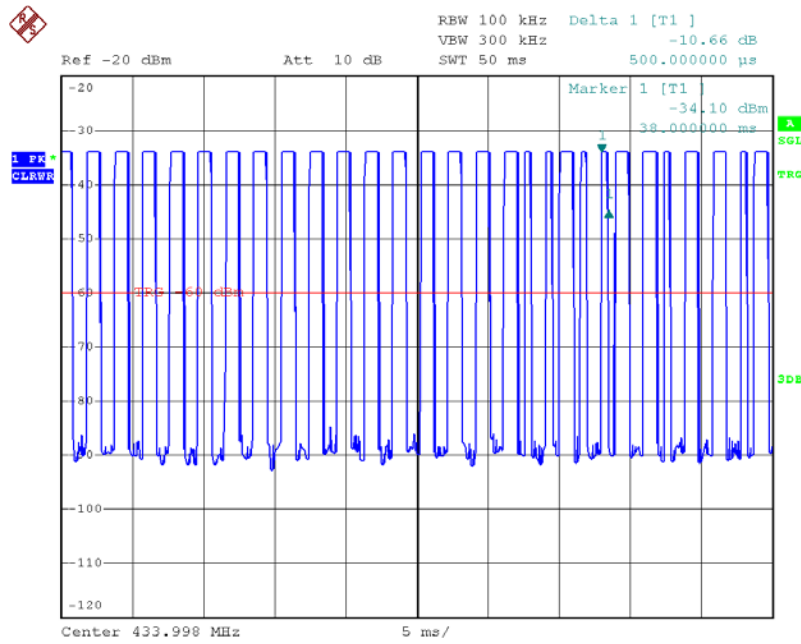


Date: 23.NOV.2010 20:20:12

Average Factor Plot - Pursuant to FCC Part 15 Section 15.231 Requirement (Pass)

INTERTEK TESTING SERVICES

Report No.: HK10110746-1
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Date: 23.NOV.2010 20:20:29

Duty cycle:

$$= \frac{1\text{ms} \times 43 + 500\text{us} \times 17}{100\text{ms}}$$

$$= 0.515$$

Average Factor:

$$20\log(0.515) = -5.7\text{dB}$$

Average Factor Plot - Pursuant to FCC Part 15 Section 15.231 Requirement (Pass)