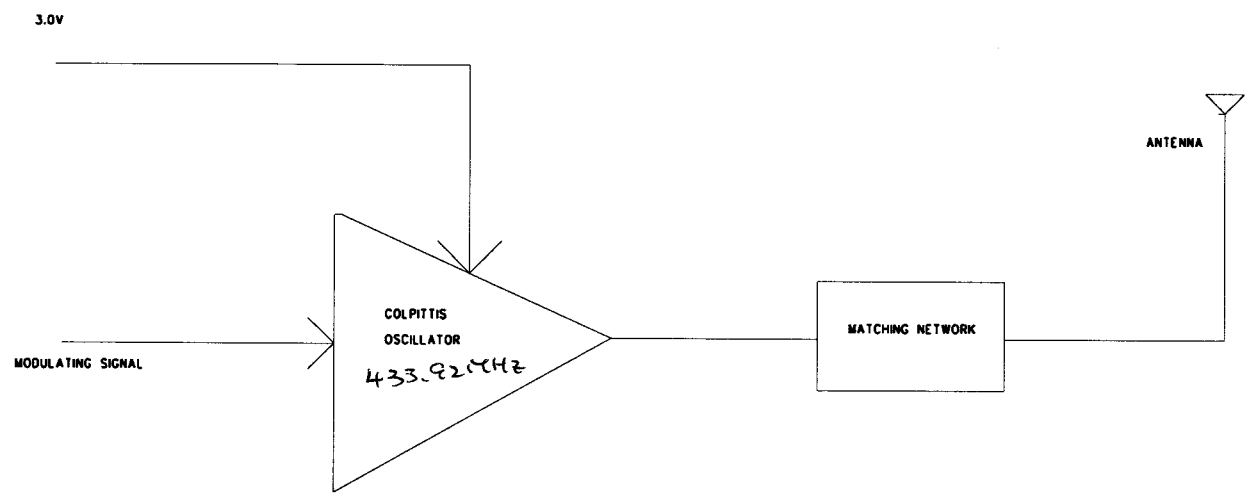


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| REVISION RECORD | | | |
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| LT# | ECR NO. | APPROVED | DATE |
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433.92 MHz Tx MODULE BLOCK DIAGRAM OF GA128TX

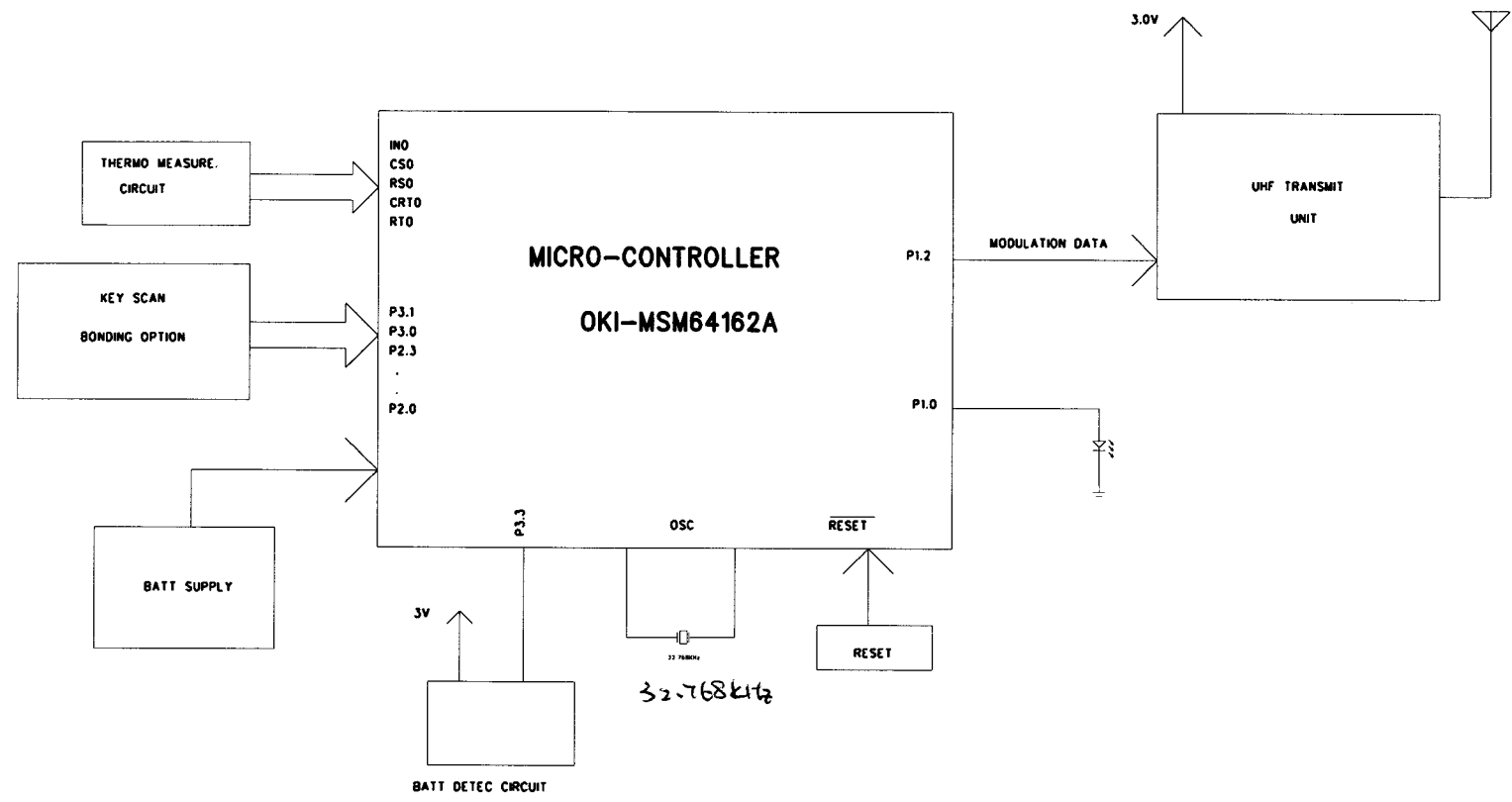
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| DRAWN: | | DATED: | | COMPANY: | | | |
| CHECKED: | | DATED: | | TITLE: | | | |
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SYSTEM BLOCK DIAGRAM OF GA128TX

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| DRAWN | | DATE: | | COMPANY | |
| CHECKED | | DATE: | | TITLE | |
| QUALITY CONTROL | | DATE: | | CODE | DATE |
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D
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