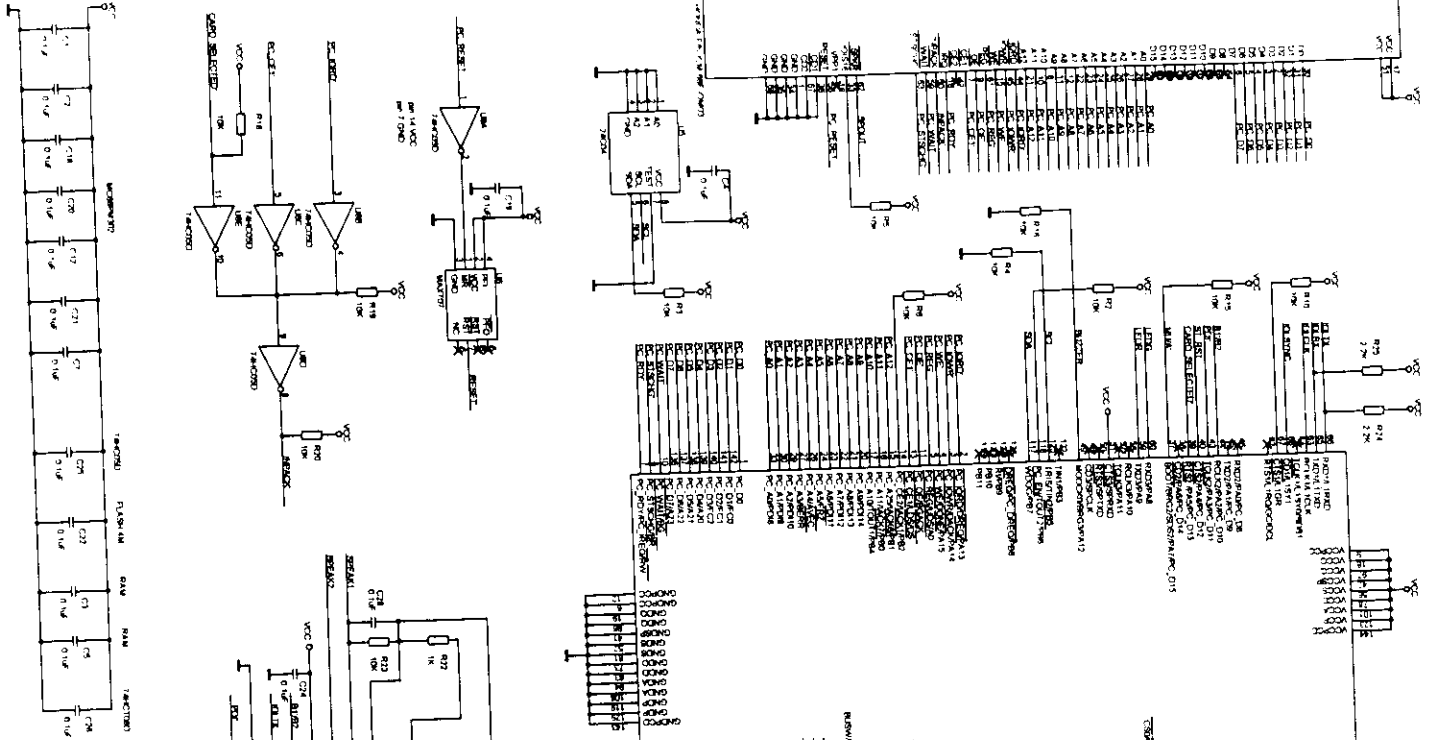
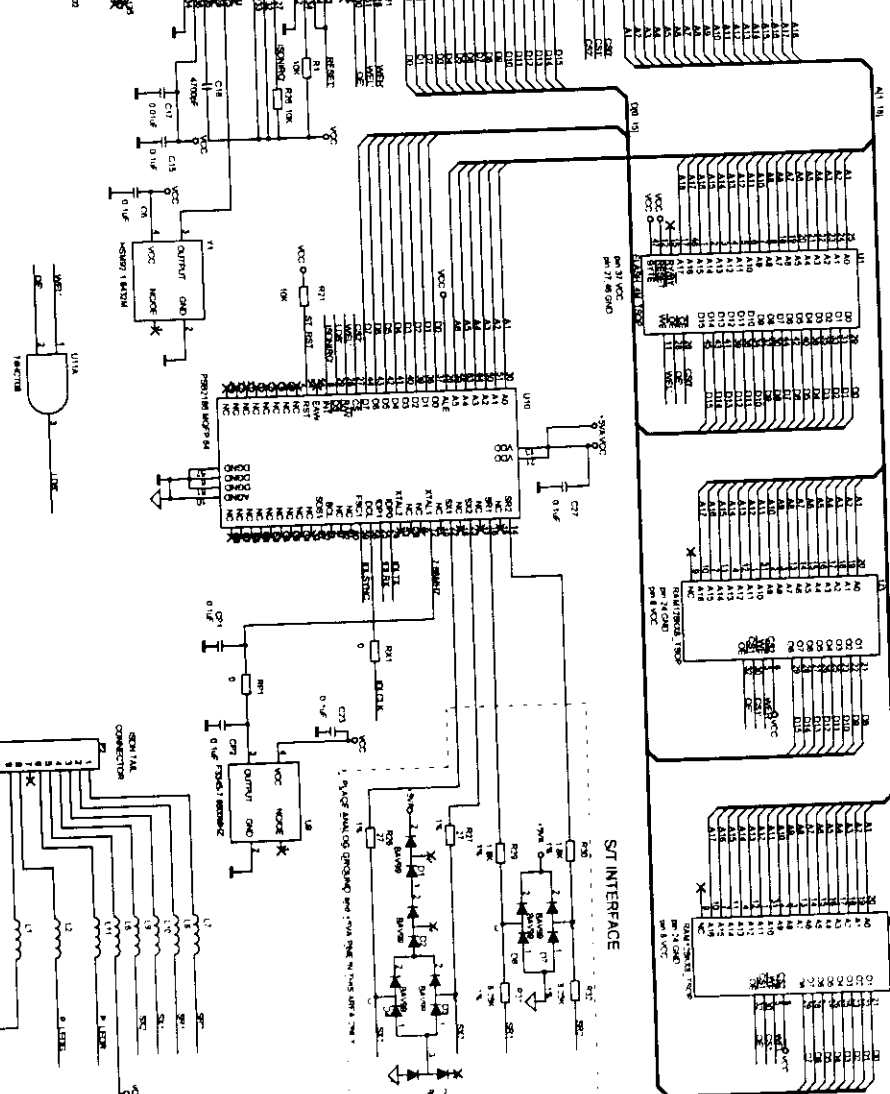


***EXHIBIT D***

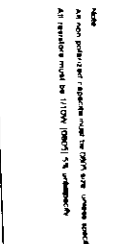
***Circuit Diagram***



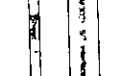
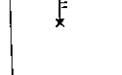
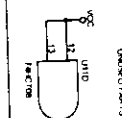
### ISON VOICE INTERFACE



### ST INTERFACE



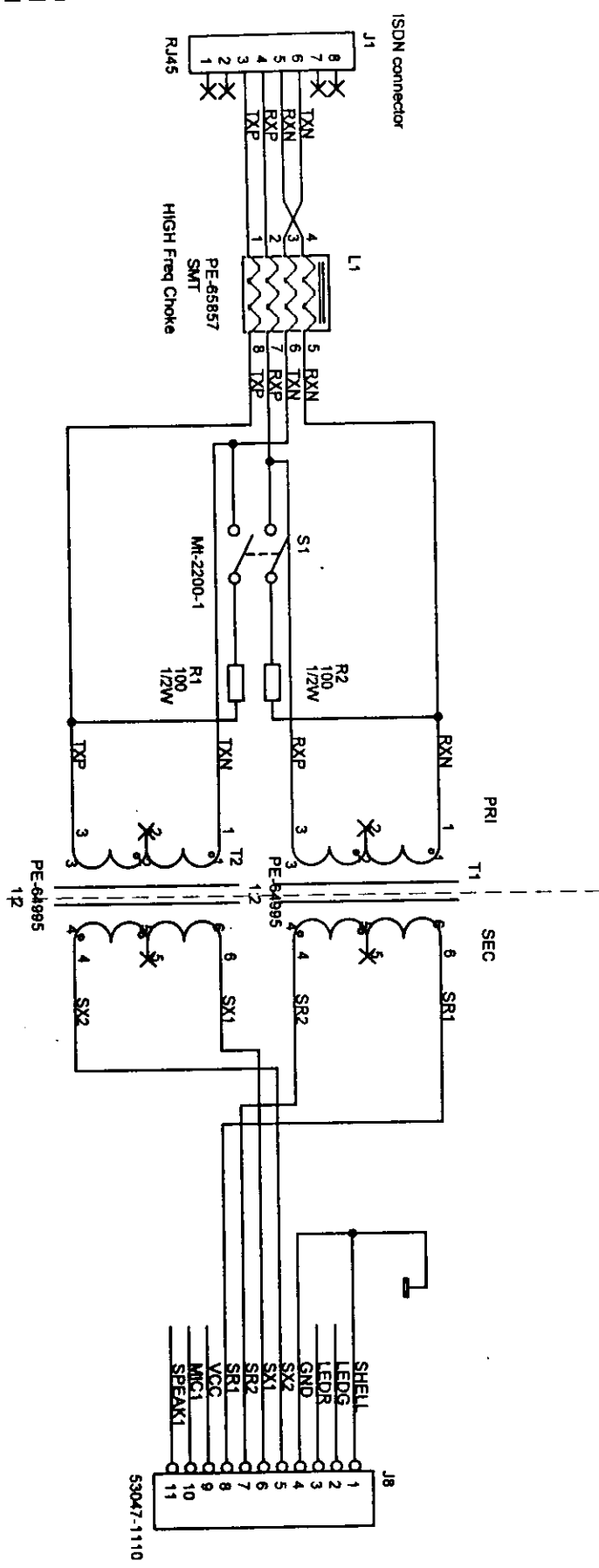
Note: As each point on a driver has its own time constant, the value of each capacitor should be adjusted to match the driver's time constant.



Revision August 27, 1988  
 ORIGINAL COMPONENTS BY:  
 TOLLSTONE, SAYS, TO  
 CONTROL, QLT, CHEN, QP, 84  
 C 2  
 121 TERMINAL CONTROL SYSTEM, ST, 120000  
 120000

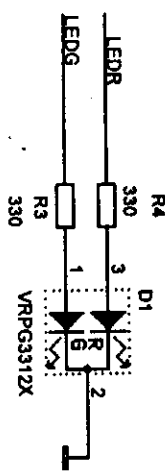
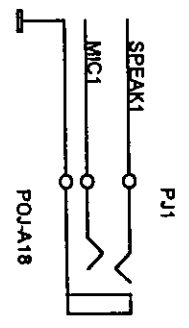
ISOLATION BARRIER

S/T INTERFACE



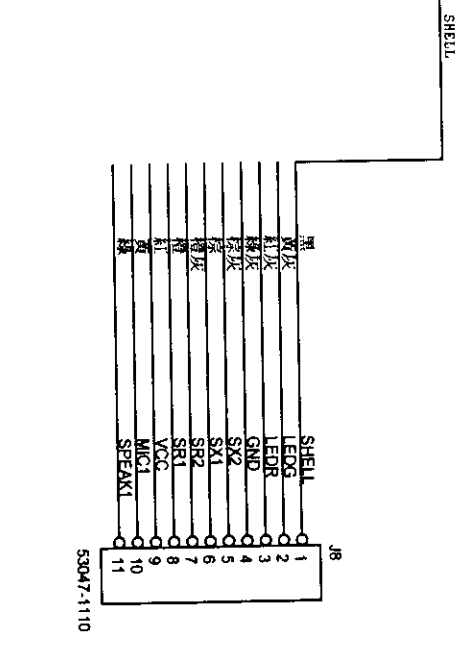
DO NOT PLACE ANY GROUND  
VCC PLANES OR COMPONENTS  
UNDER THIS SECTION

HEADSET CONNECTIONS



53047-1110

1	SR2	橙灰
2	SR1	橙
3	SX1	棕灰
4	SX2	棕灰
5	VCC	紅
6	LEDR	紅灰
7	LEDG	黃灰
8	GND	綠灰
9		
10	X MIC1	黃
11	X	
12	X	
13	⊖	
14	SPEAK1	綠
15		



APPLE PIE NEW CABLE

Title	
重新Layout 橙灰線 Cable	
Size	Document Number
A4	(Doc)
Date	Monday, Mar 11, 1996
Sheet	1 of 1