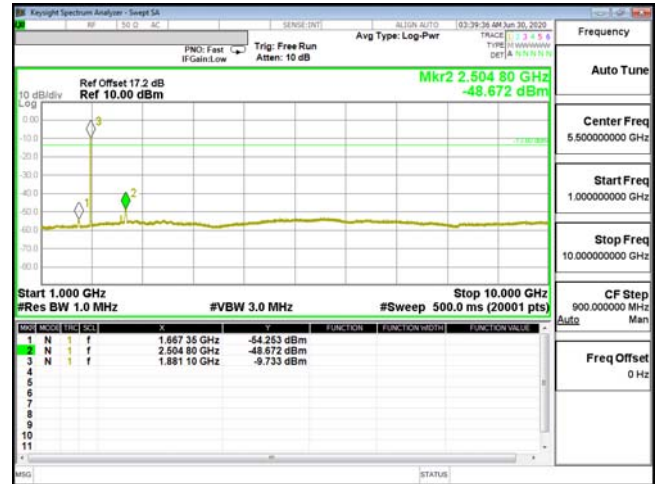
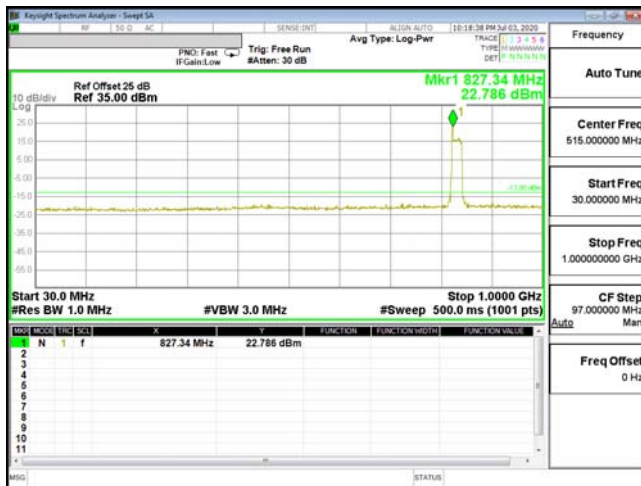


CSE-ENDC\_2A\_n5-64QAM\_20M\_CH166800\_834(100,6)



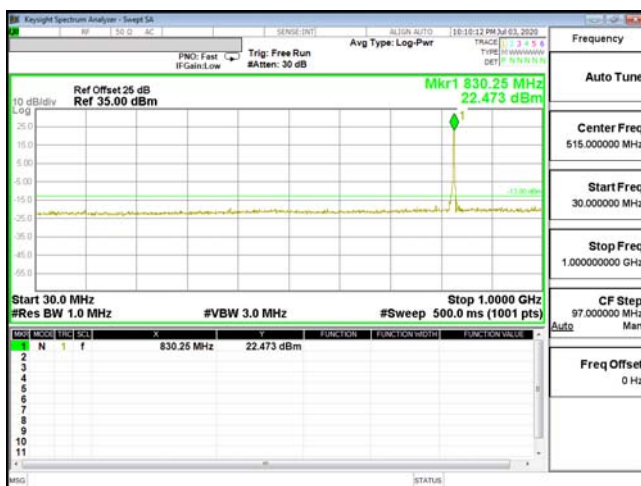
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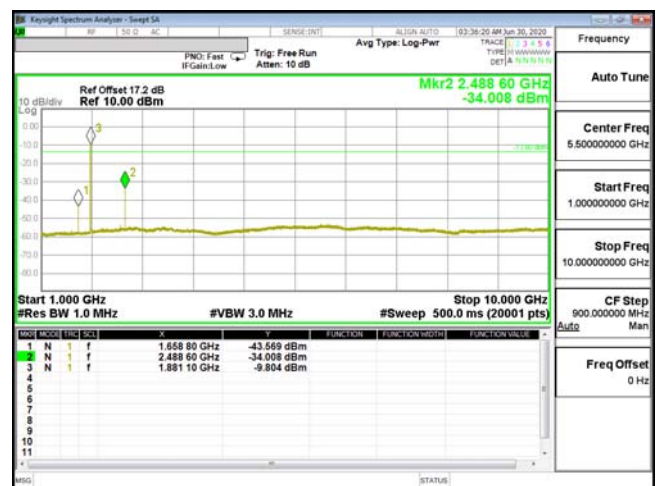
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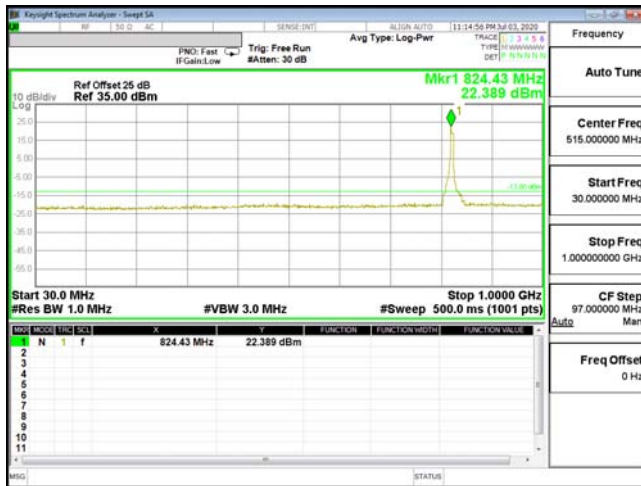
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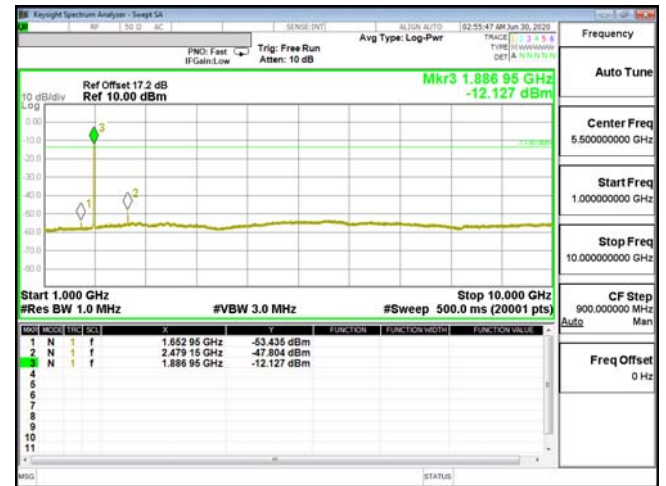
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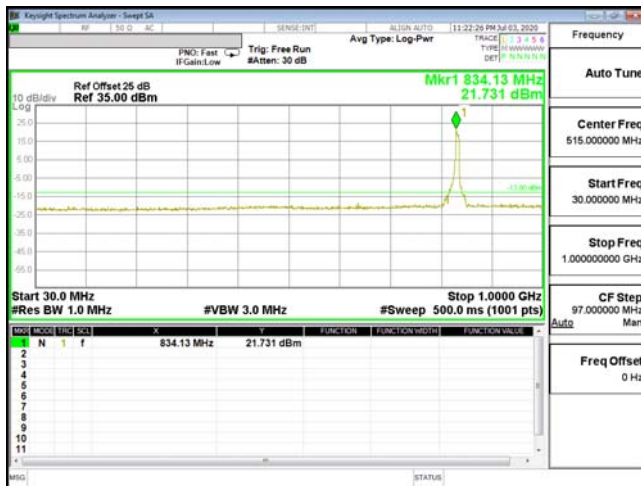
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CSE-ENDC\_2A\_n5-256QAM\_5M\_CH165300\_826.5(25,0)



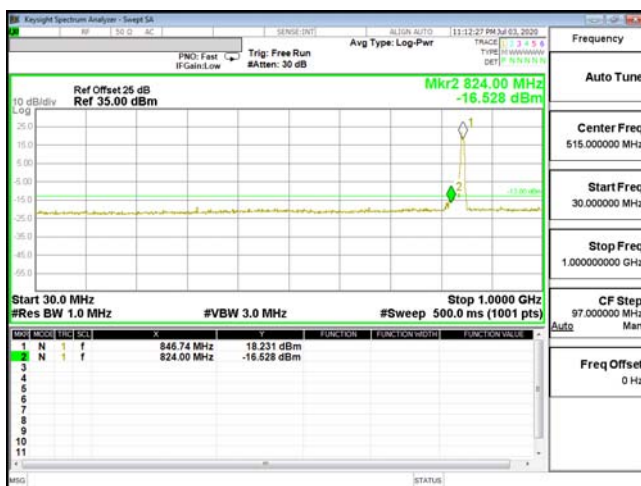
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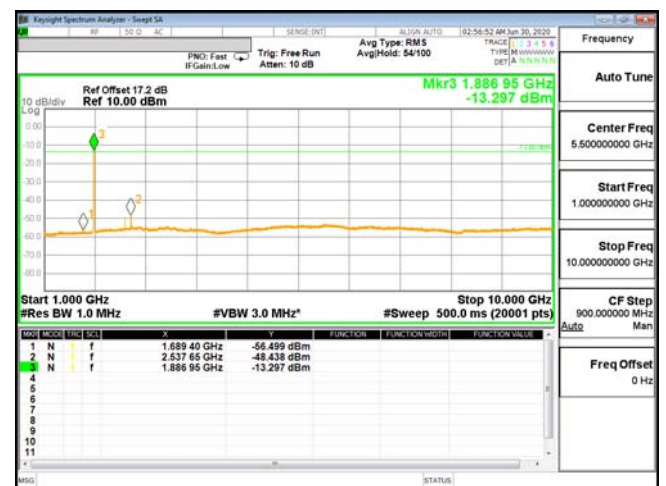
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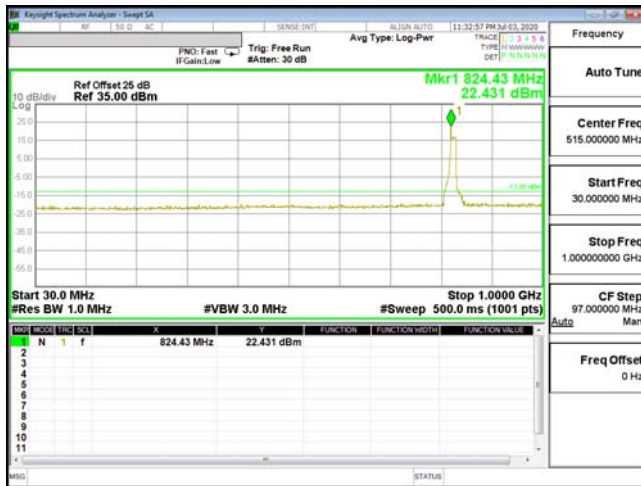
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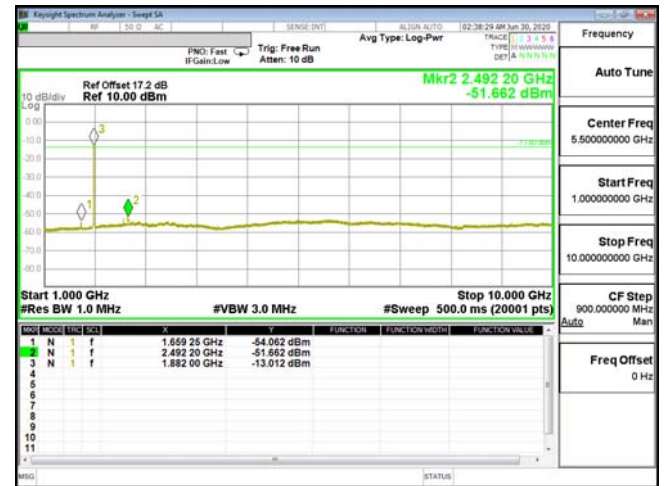
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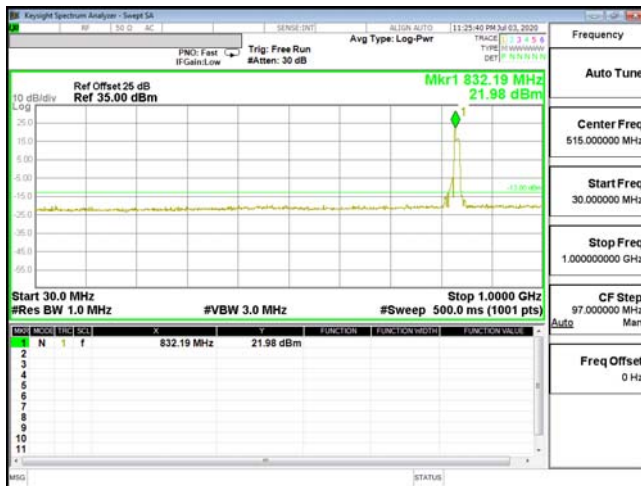
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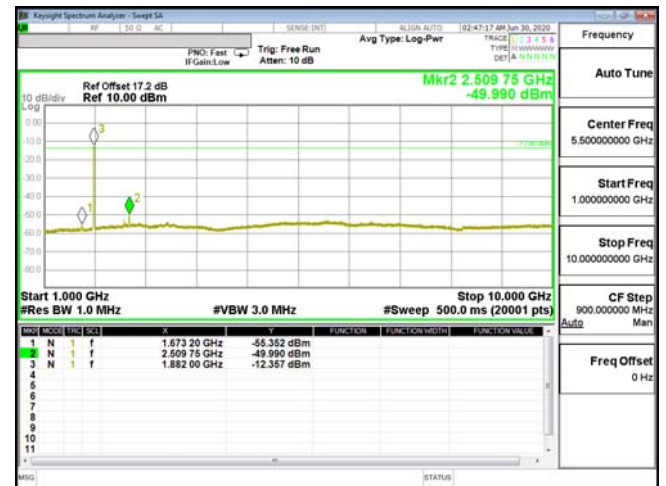
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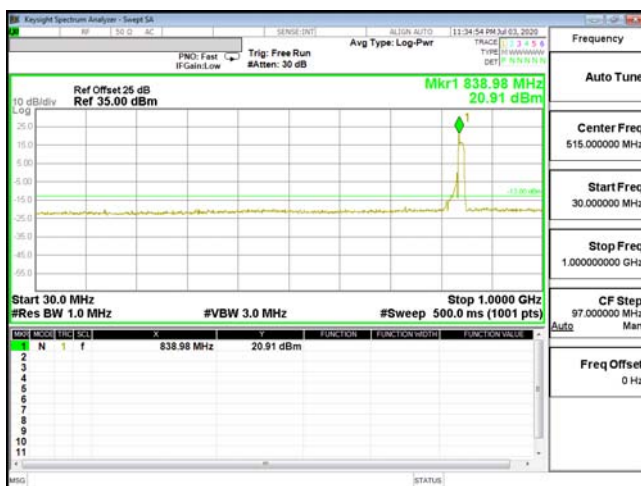
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CSE-ENDC\_2A\_n5-256QAM\_10M\_CH167300\_836.5(50,0)



CSE-ENDC\_2A\_n5-256QAM\_10M\_CH167300\_836.5(50,0)

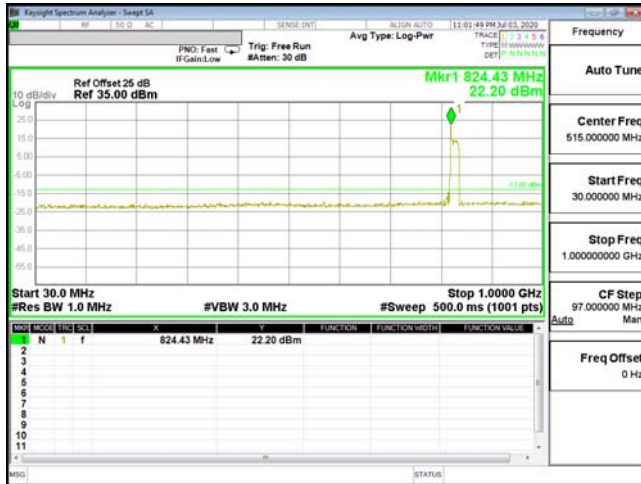


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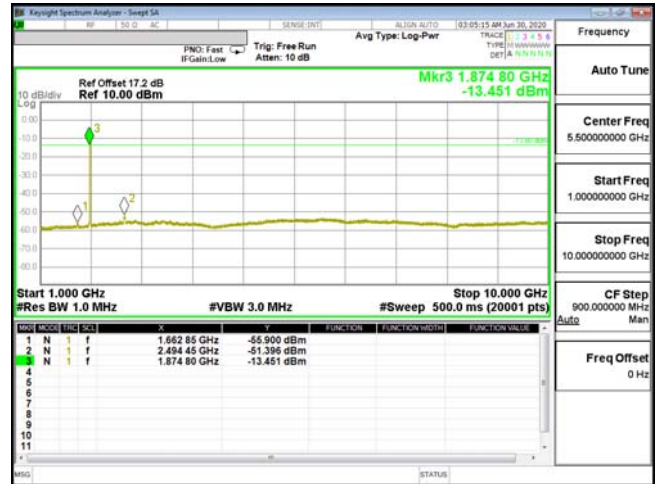


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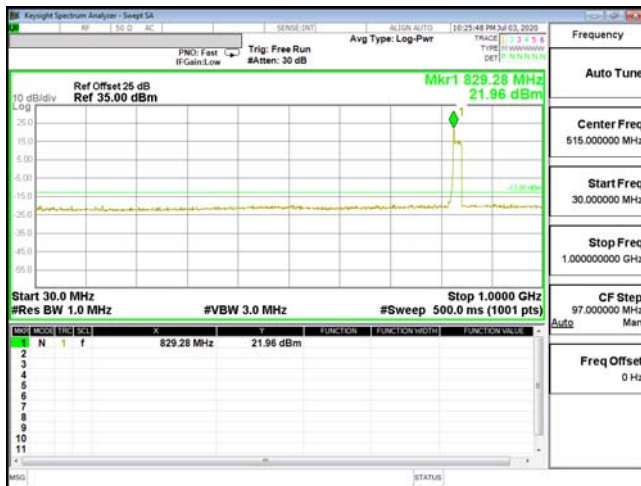




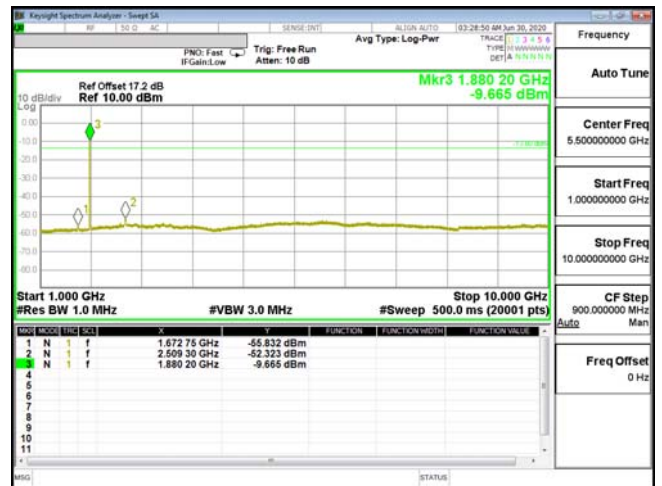
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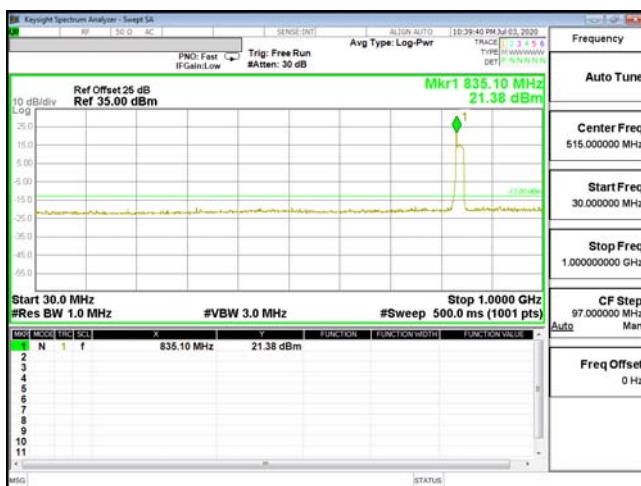
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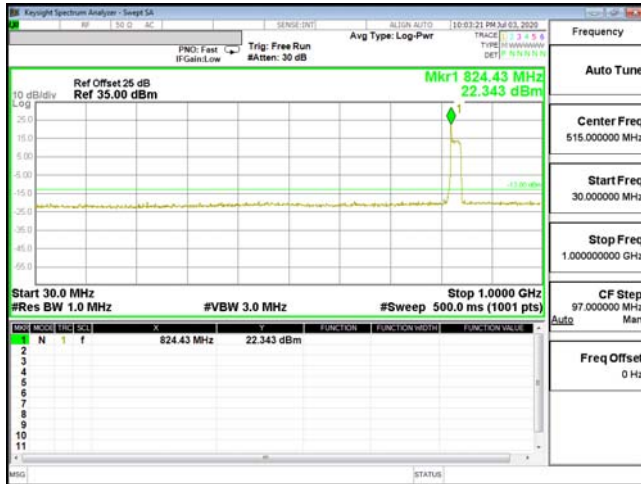
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CSE-ENDC\_2A\_n5-256QAM\_15M\_CH168300\_841.5(75,0)



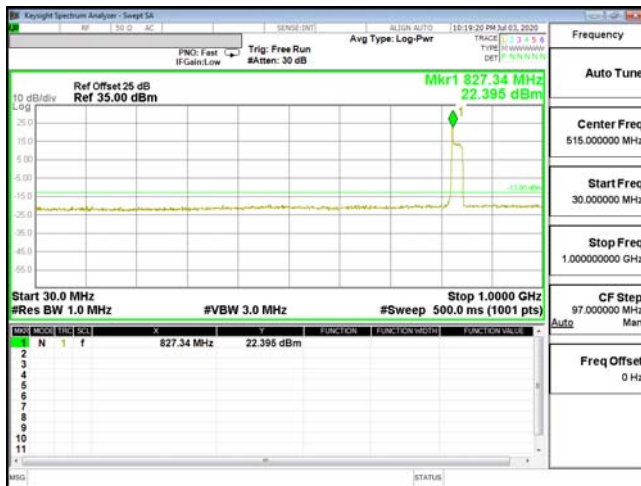
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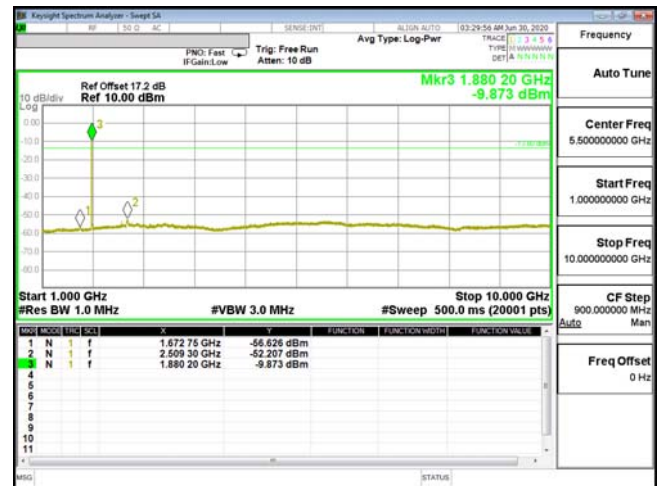
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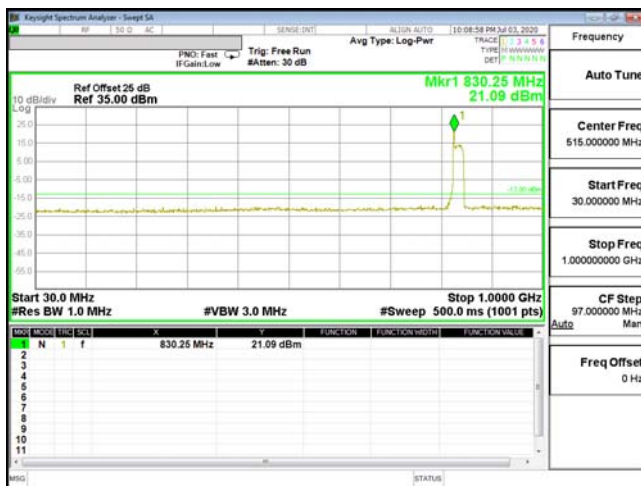
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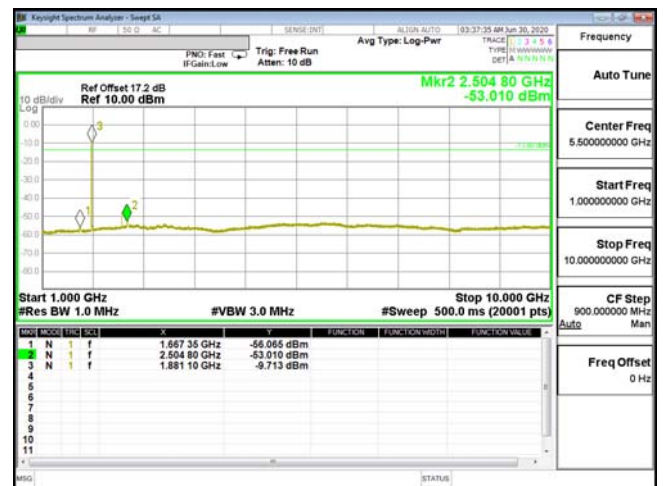
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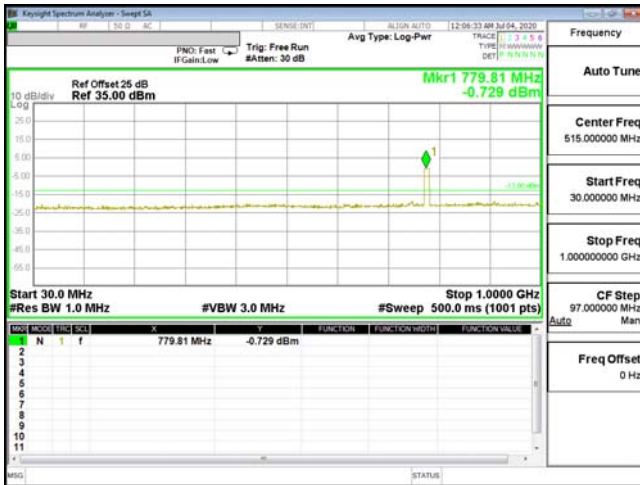


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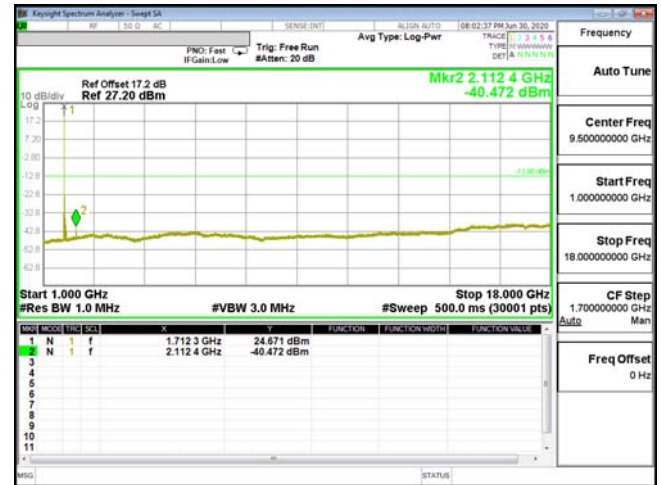


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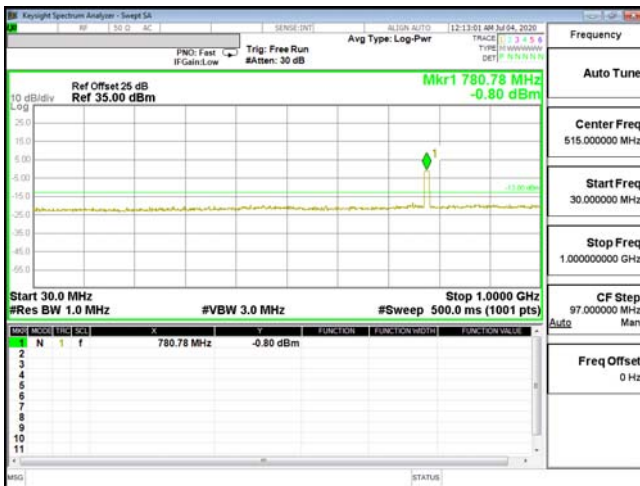
Product	LV55		
Test Item	Spurious Emissions (Conducted)		
Date of Test	2020/07/02	Test Site	SR12-H
Test Condition	ENDC n66	Test Range	30MHz~18GHz



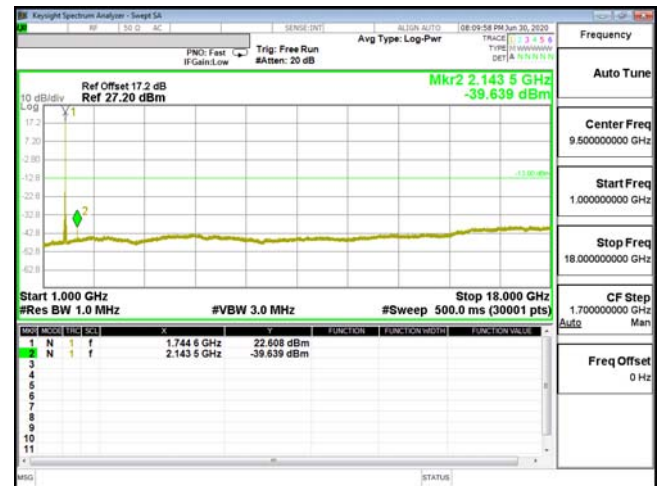
CSE-ENDC\_13A\_n66-PI2-BPSK\_5M\_CH342500\_1712.5(1,12)



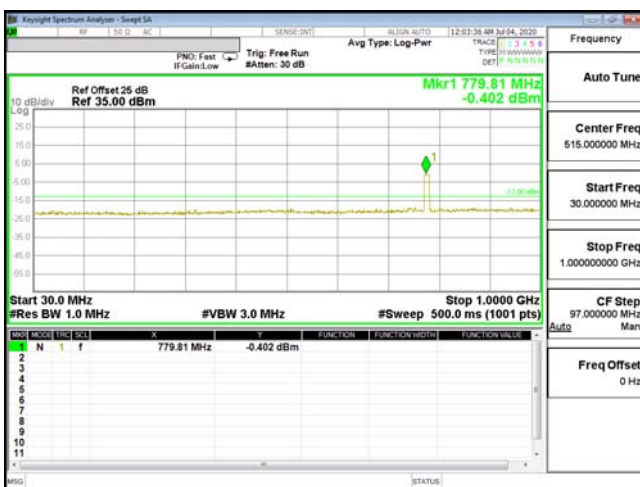
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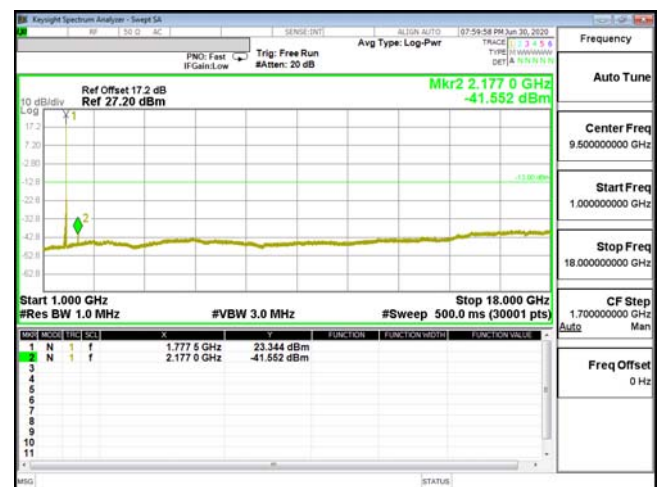
CSE-ENDC\_13A\_n66-PI2-BPSK\_5M\_CH349000\_1745(1,12)



CSE-ENDC\_13A\_n66-PI2-BPSK\_5M\_CH349000\_1745(1,12)

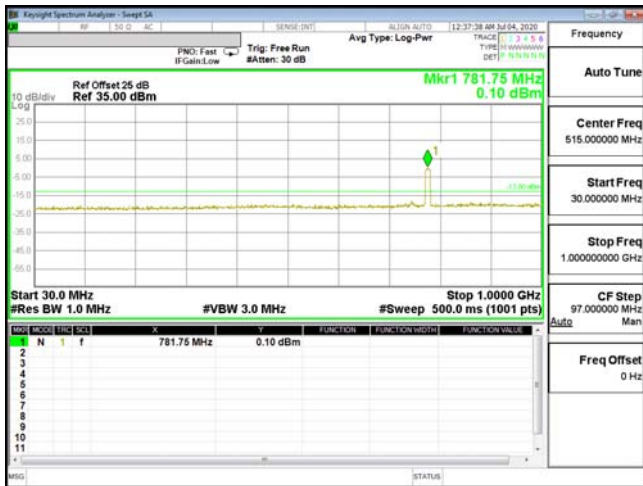


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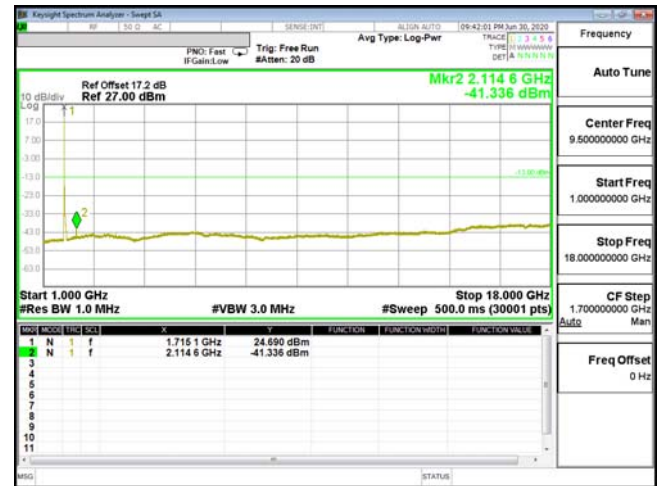


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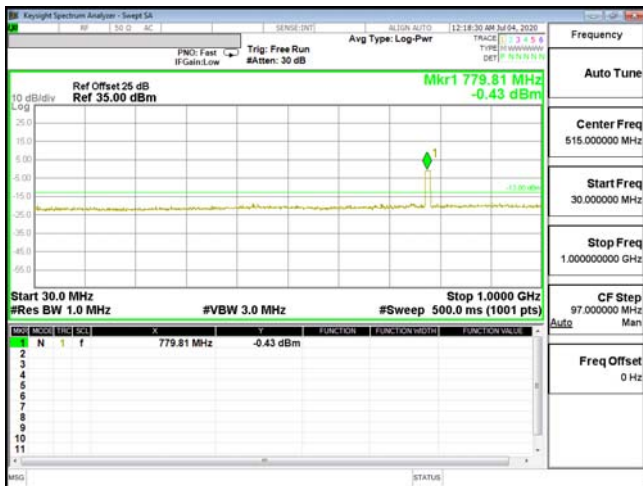




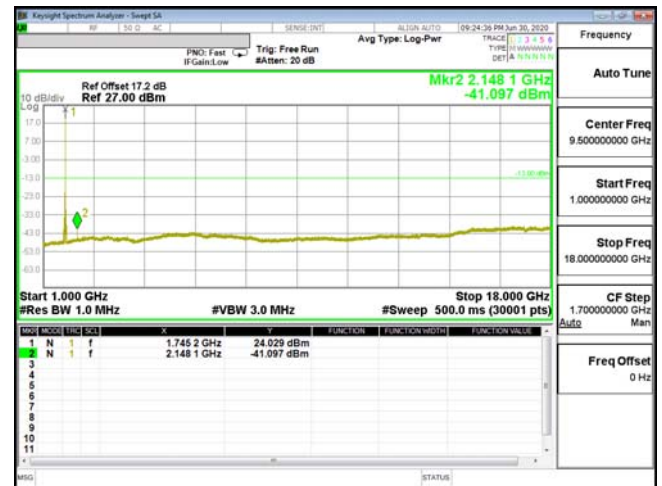
CSE-ENDC\_13A\_n66-PI2-BPSK\_10M\_CH343000\_1715(1,26)



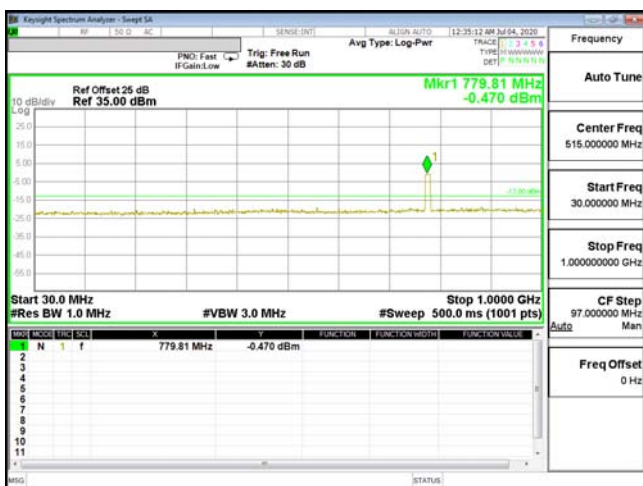
CSE-ENDC\_13A\_n66-PI2-BPSK\_10M\_CH343000\_1715(1,26)



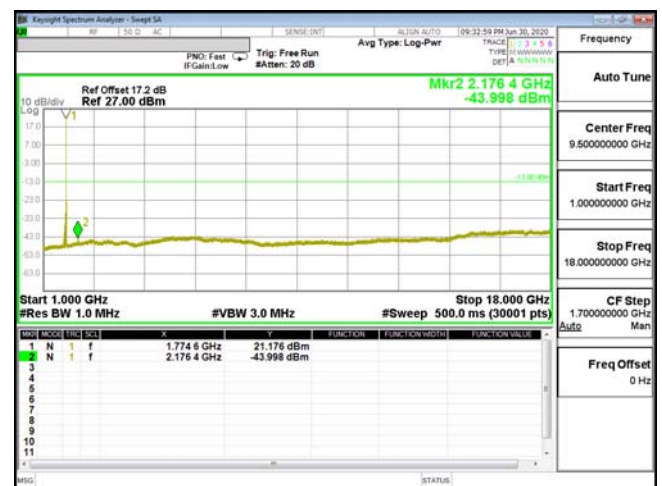
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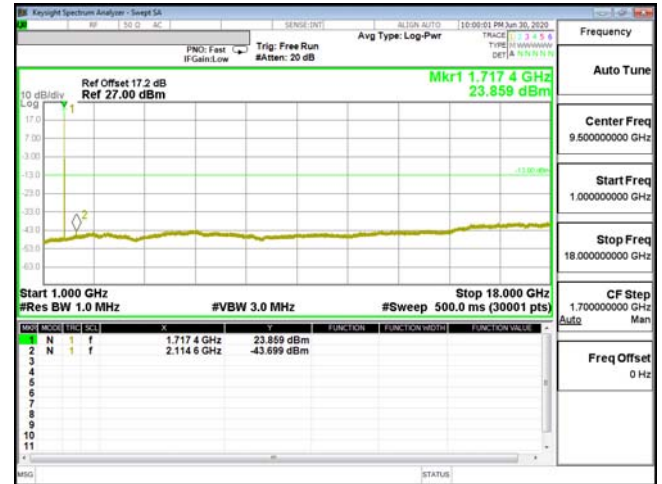
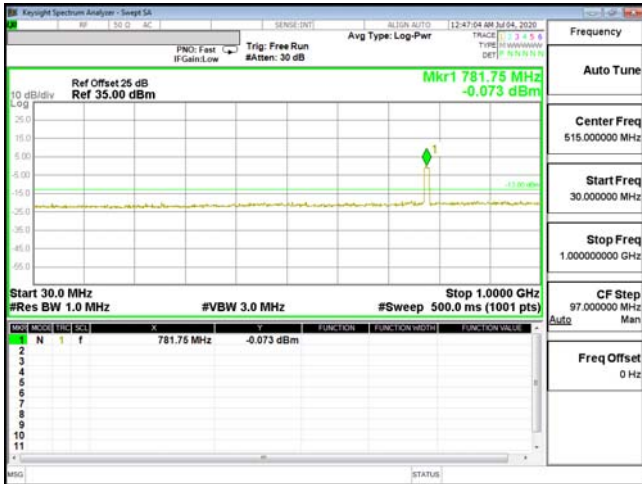
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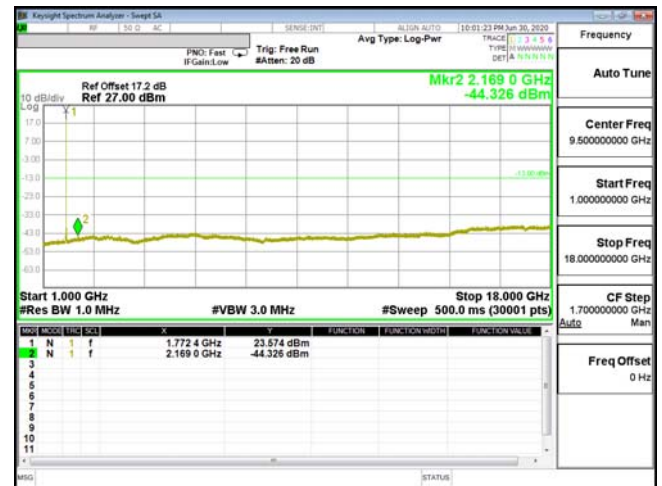
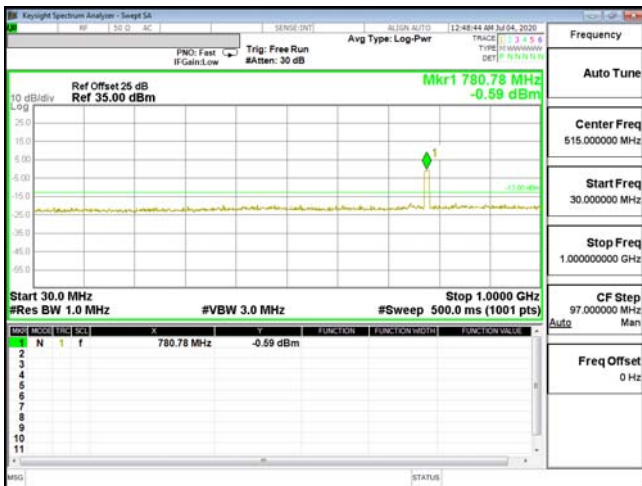
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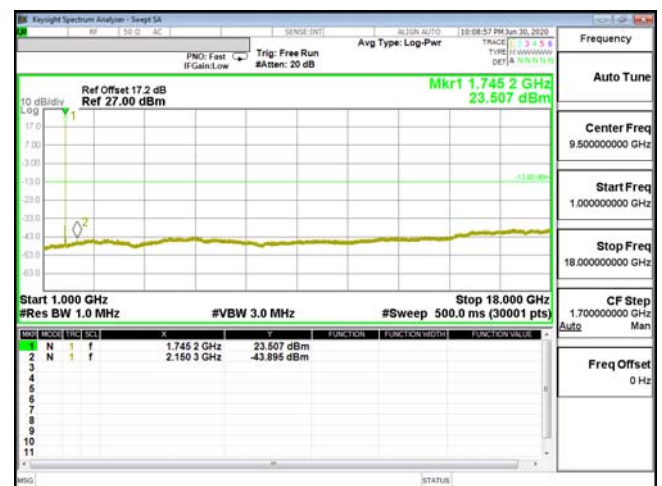
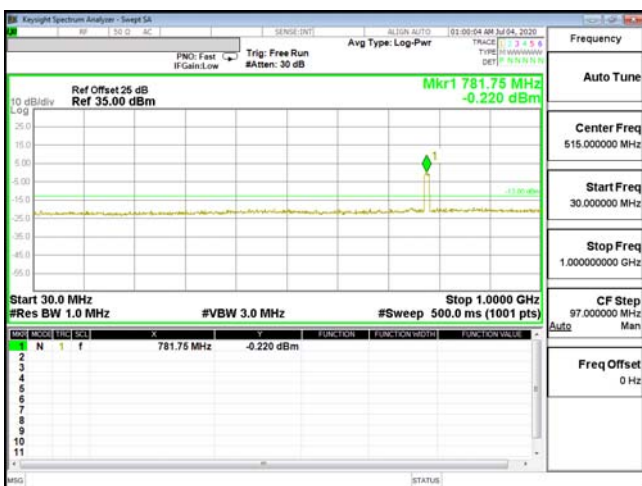
CSE-ENDC\_13A\_n66-PI2-BPSK\_10M\_CH355000\_1775(1,26)



CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH343500\_1717.5(1,39) CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH343500\_1717.5(1,39)

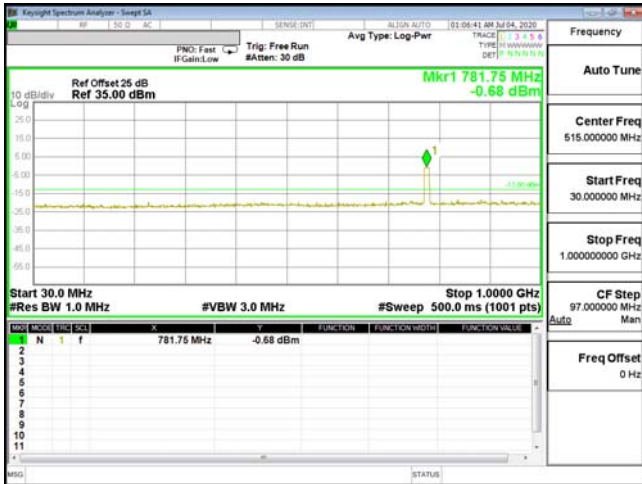


CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH354500\_1772.5(1,39) CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH354500\_1772.5(1,39)

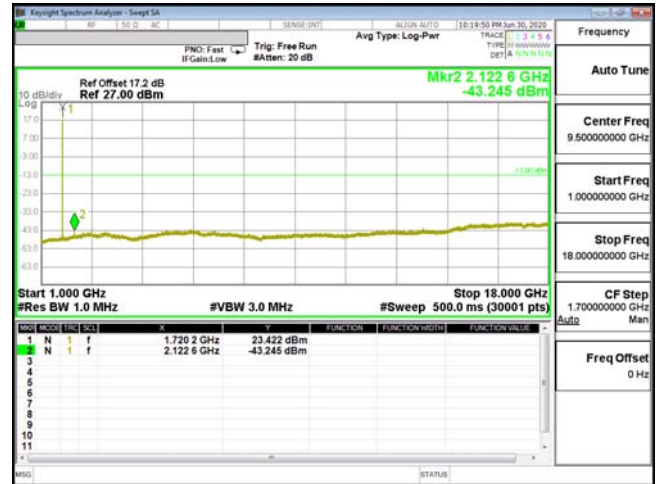


CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH354900\_1745(1,39) CSE-ENDC\_13A\_n66-PI2-BPSK\_15M\_CH354900\_1745(1,39)

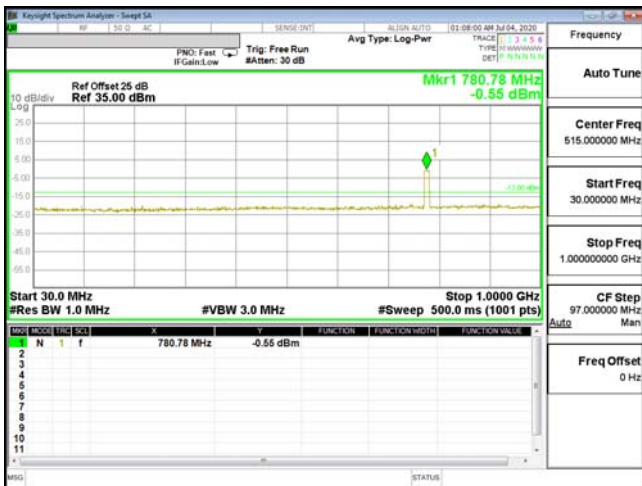




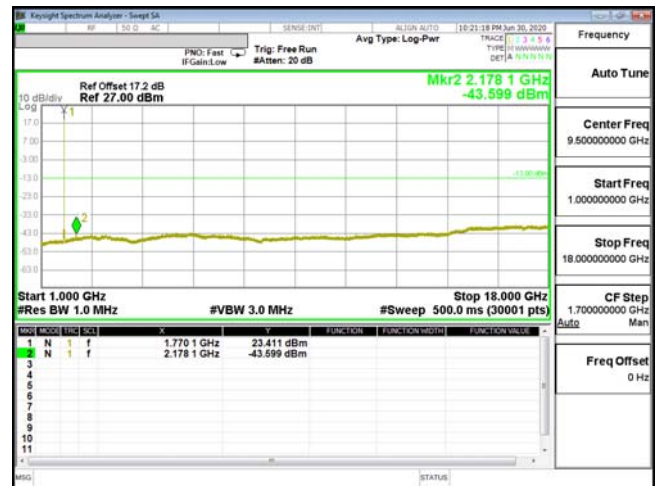
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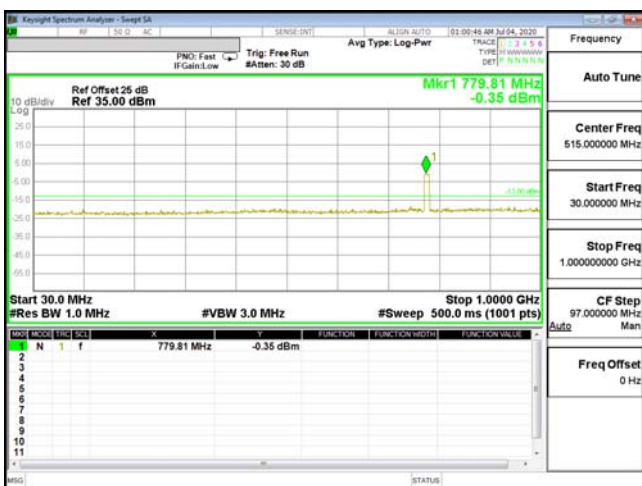
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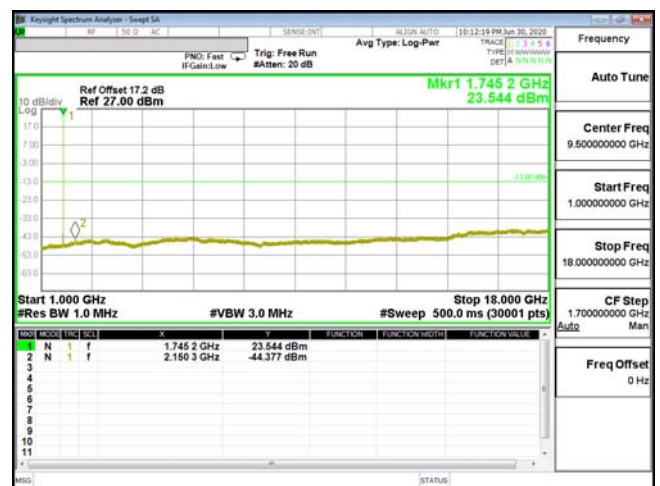
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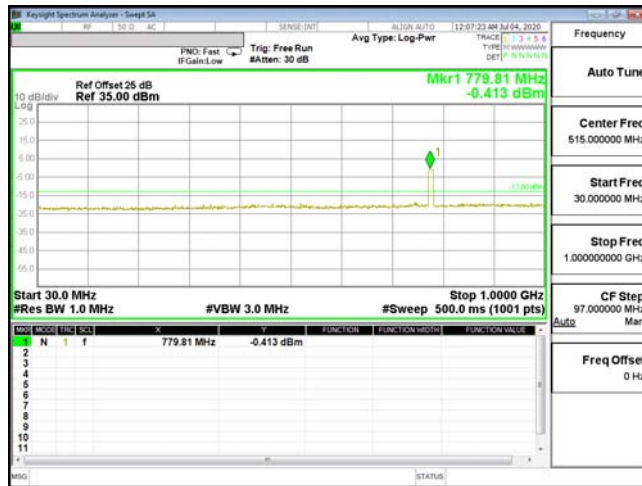
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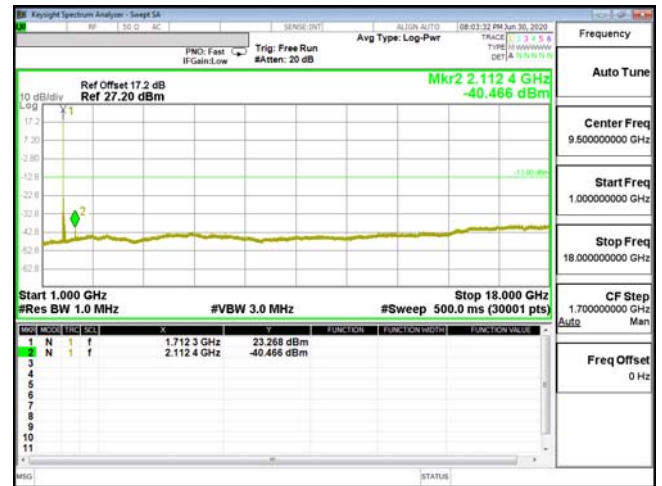
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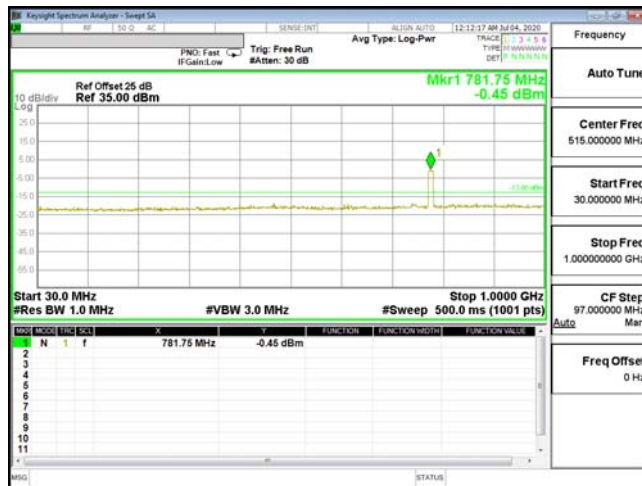
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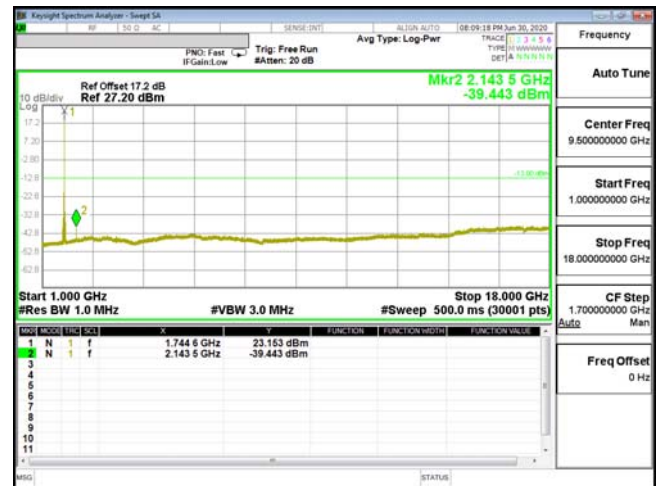
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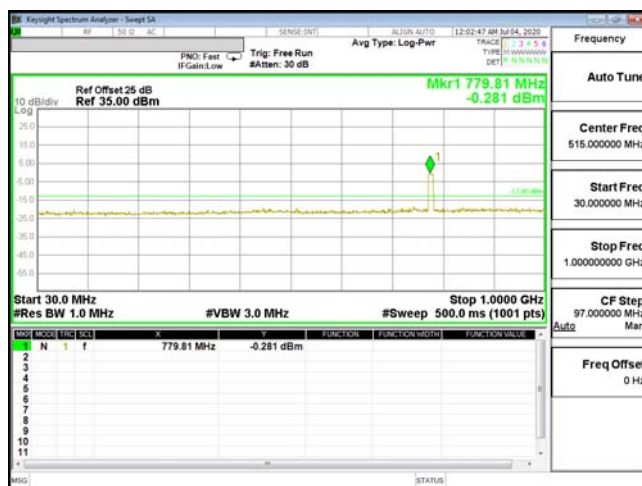
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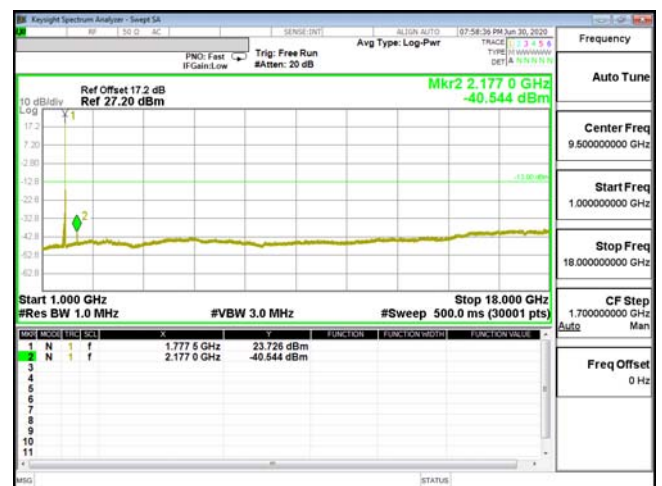
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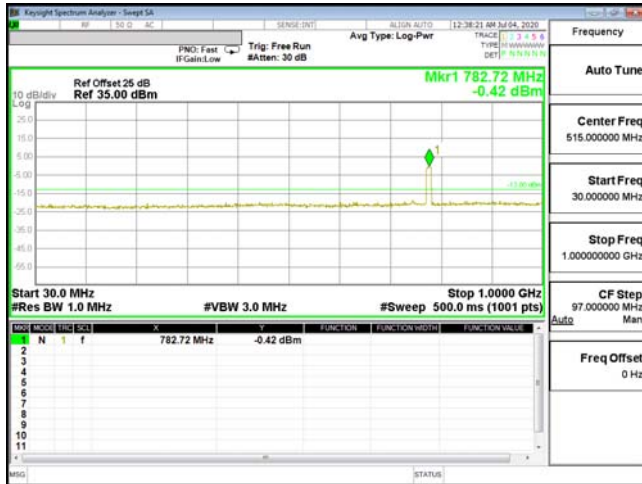
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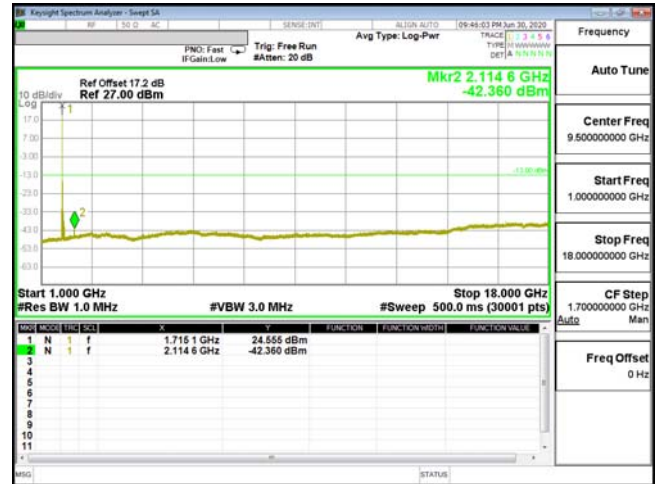
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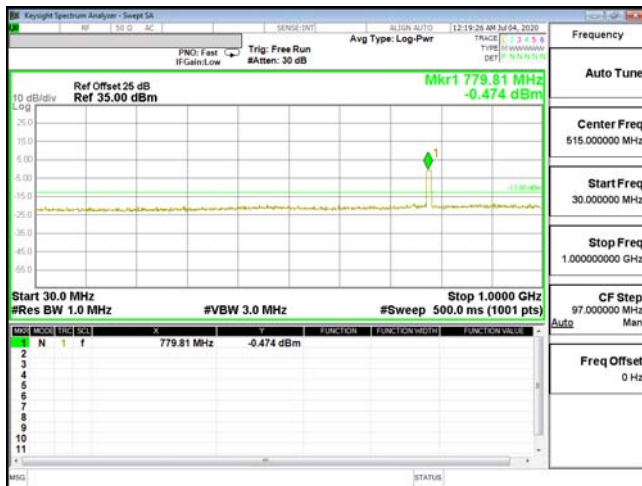
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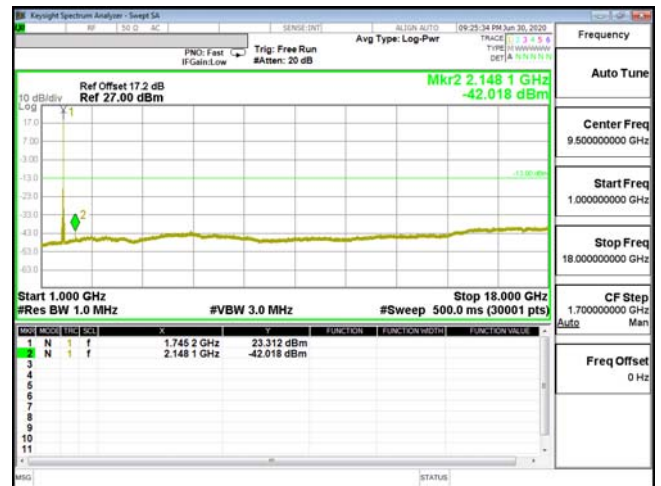
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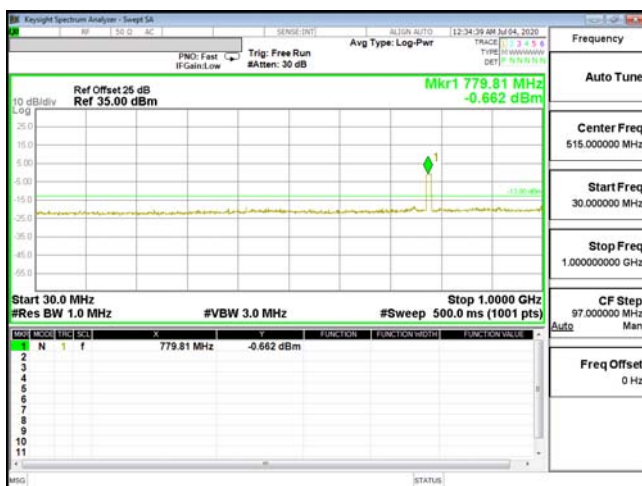
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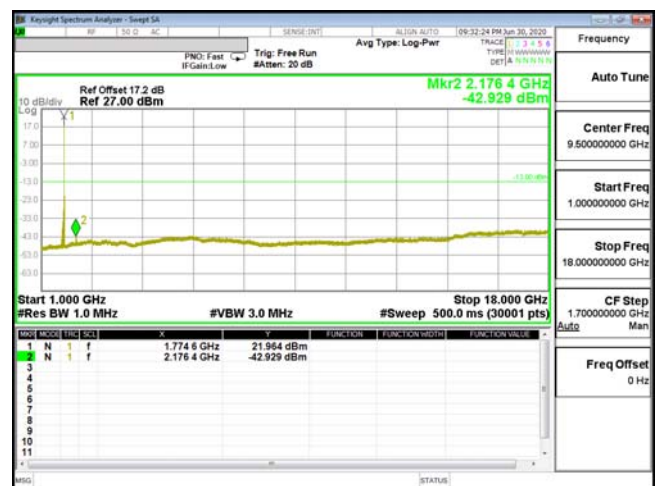
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CSE-ENDC\_13A\_n66-QPSK\_10M\_CH349000\_1745(1,26)

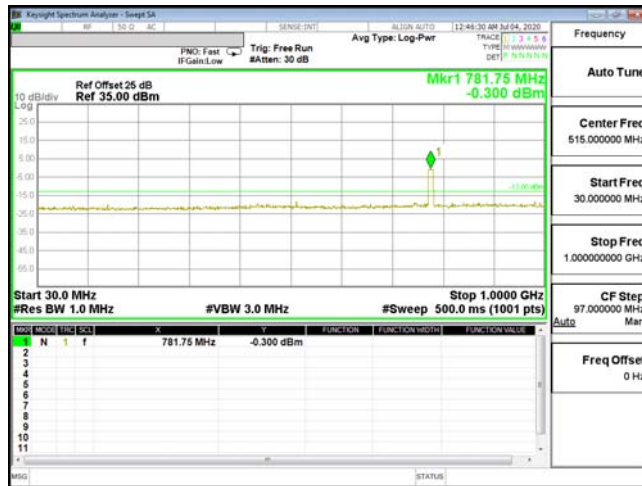


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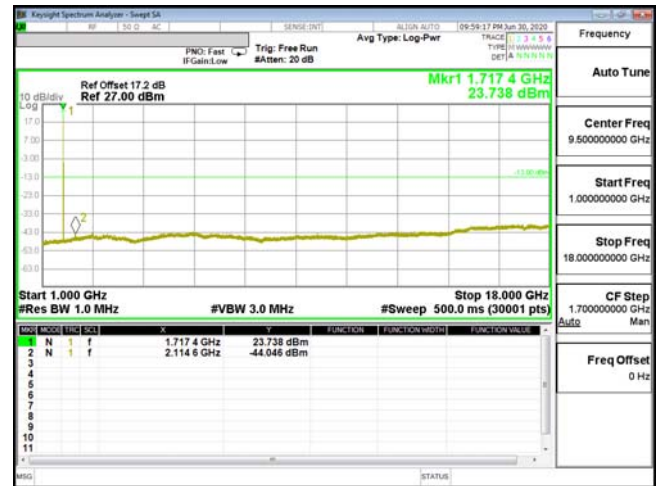


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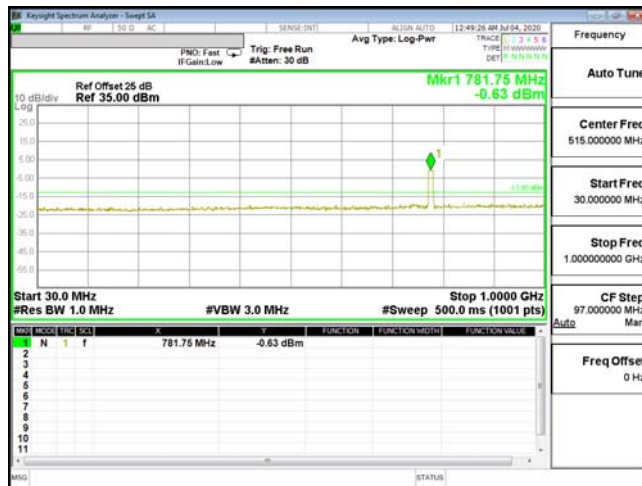




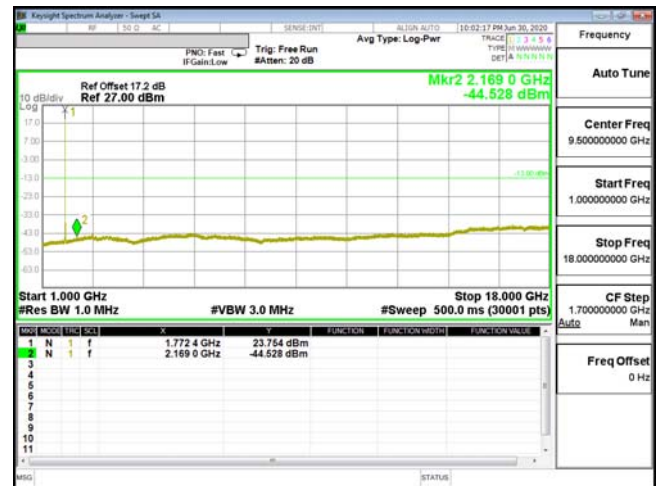
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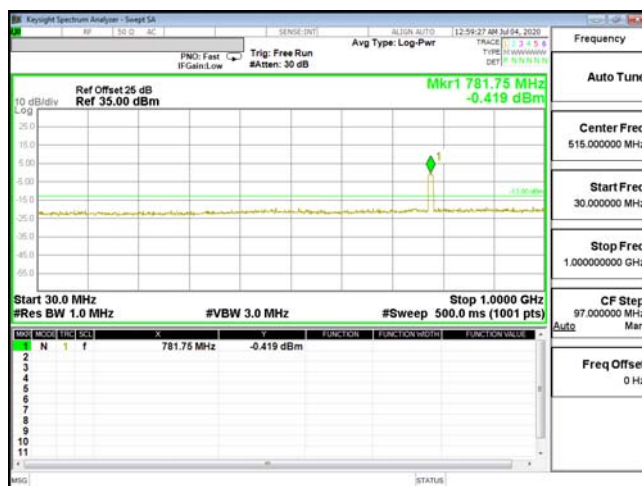
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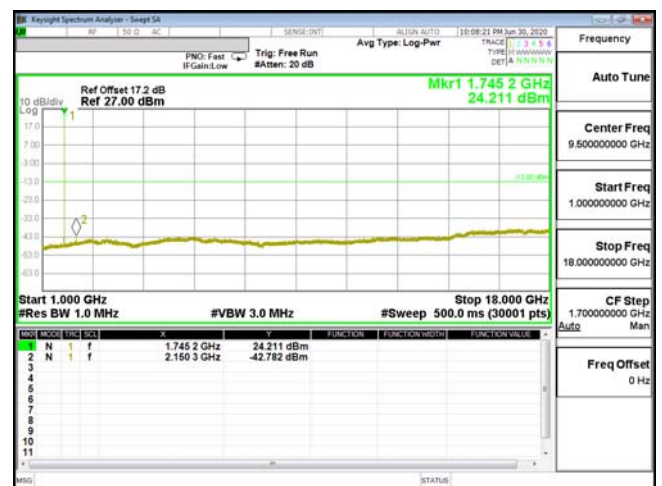
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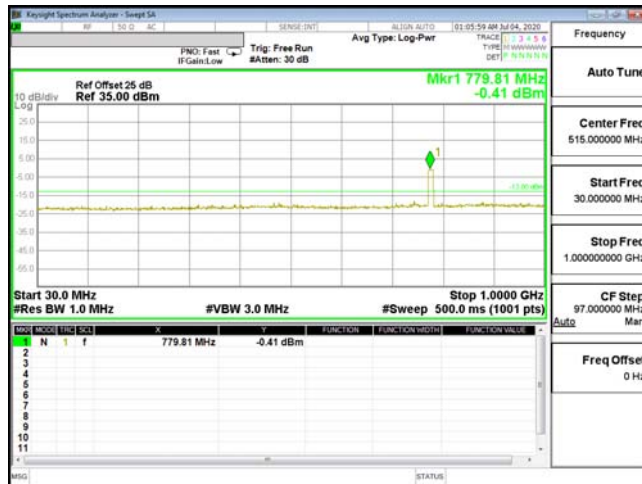
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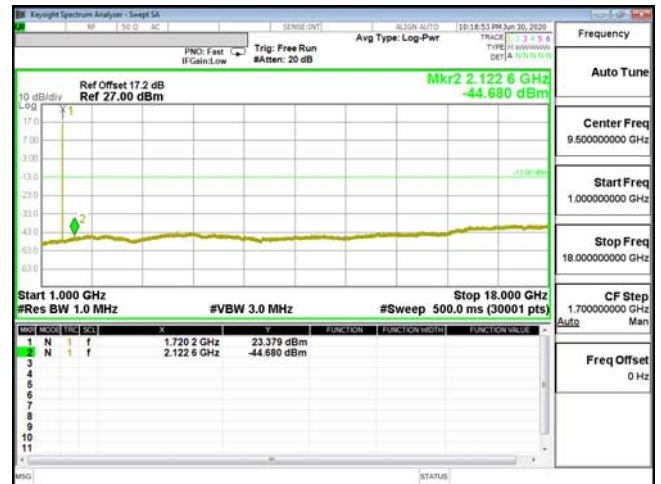
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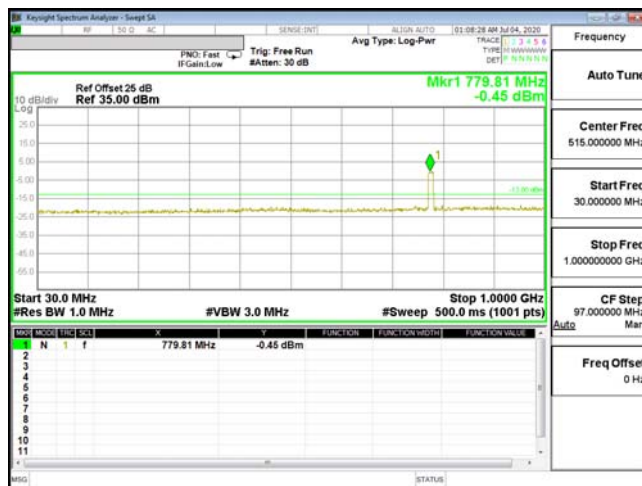
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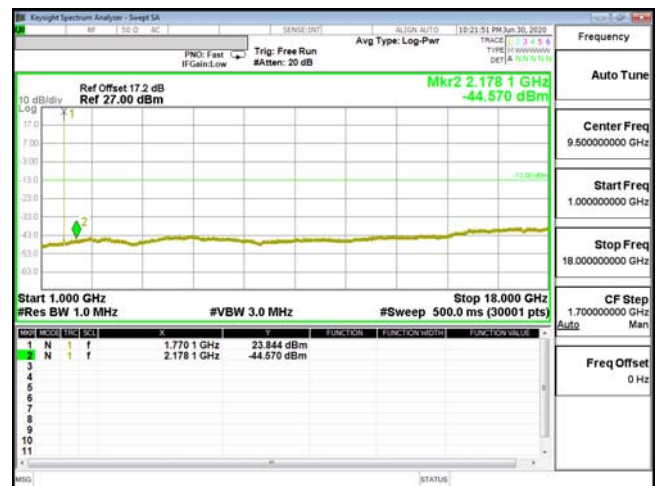
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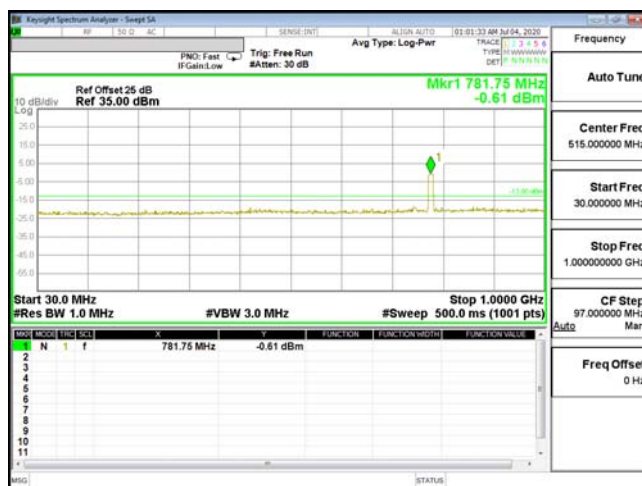
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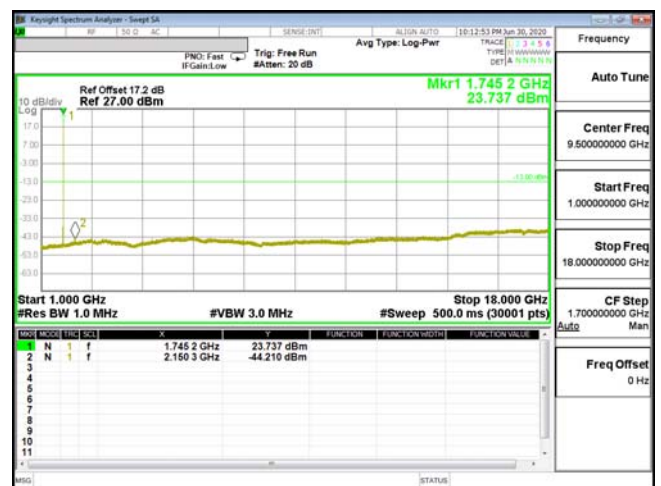
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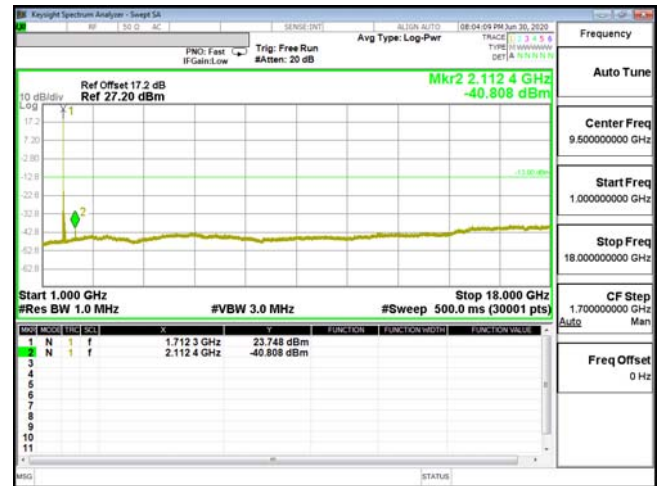
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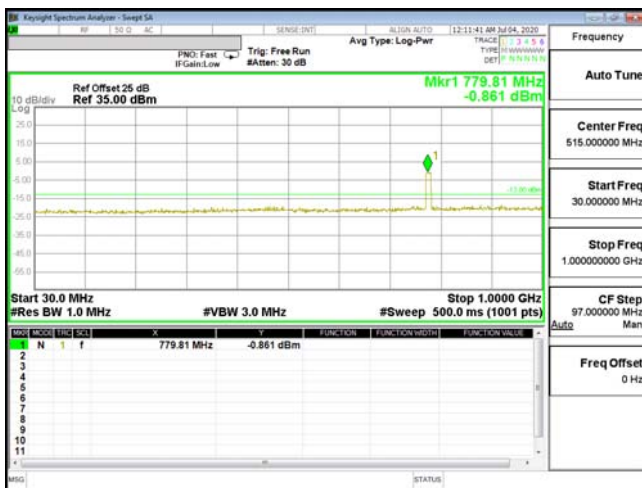
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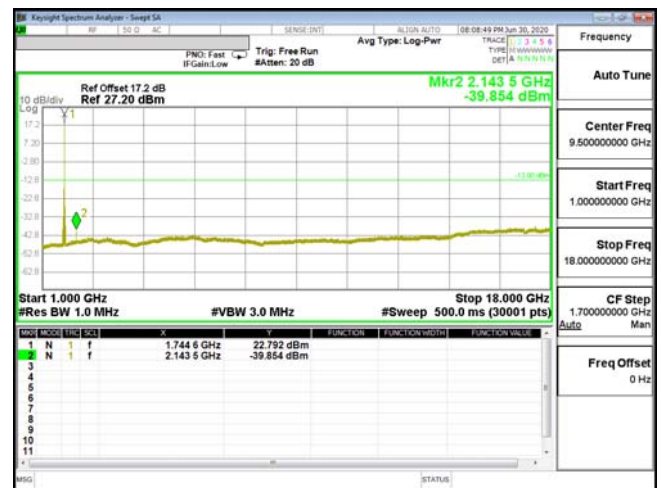
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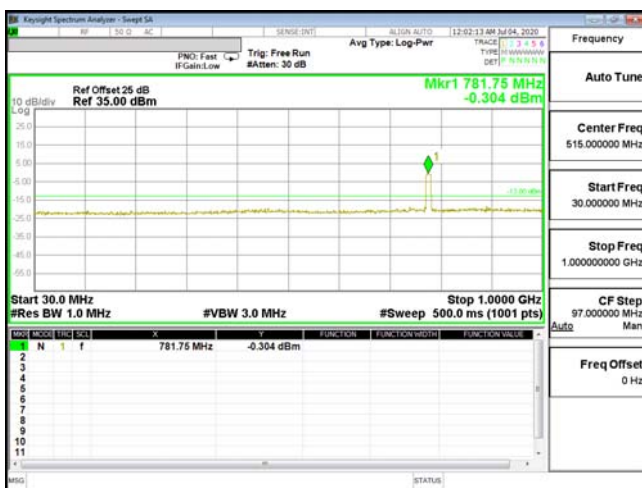
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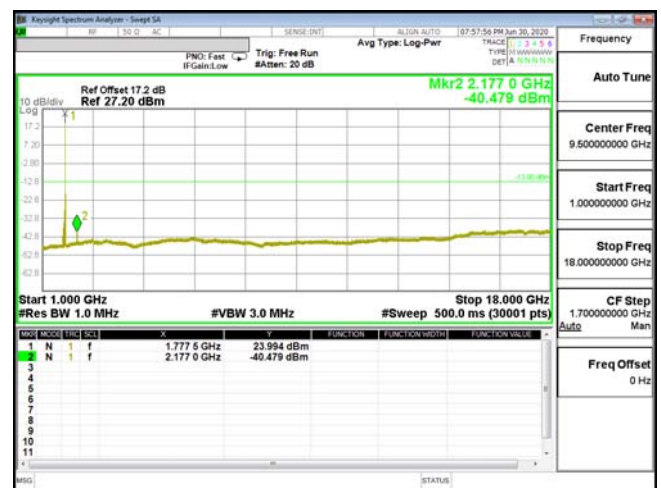
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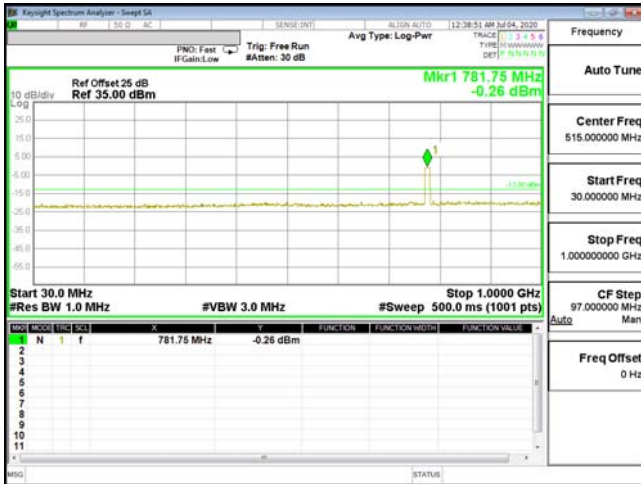


CSE-ENDC\_13A\_n66-16QAM\_5M\_CH355500\_1777.5(1,12)

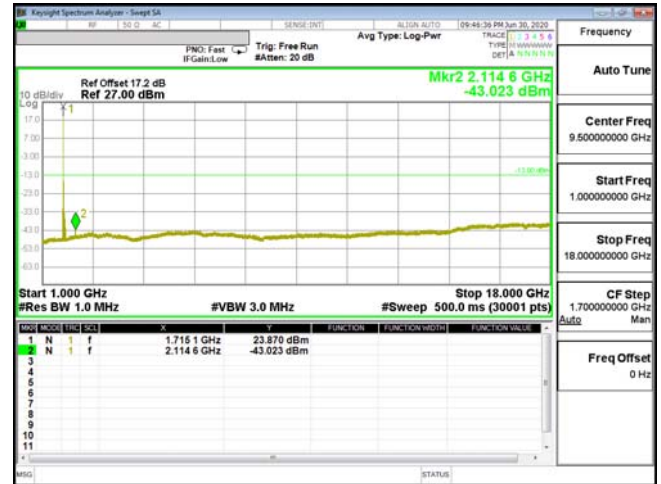


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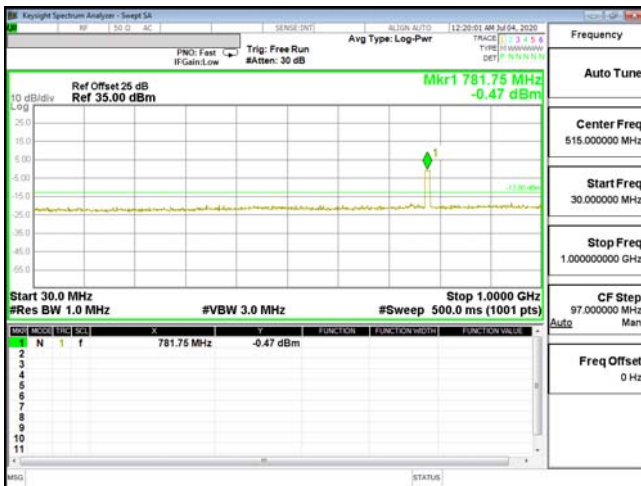




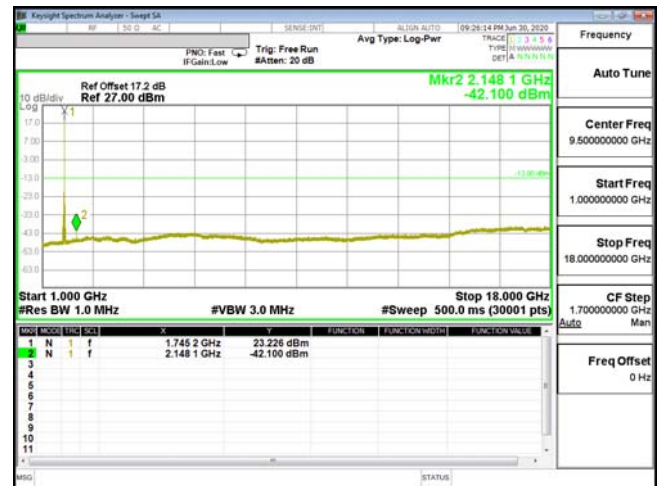
CSE-ENDC\_13A\_n66-16QAM\_10M\_CH343000\_1715(1,26)



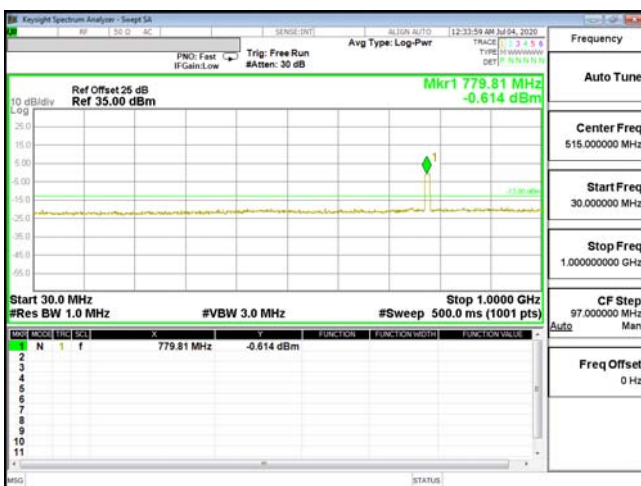
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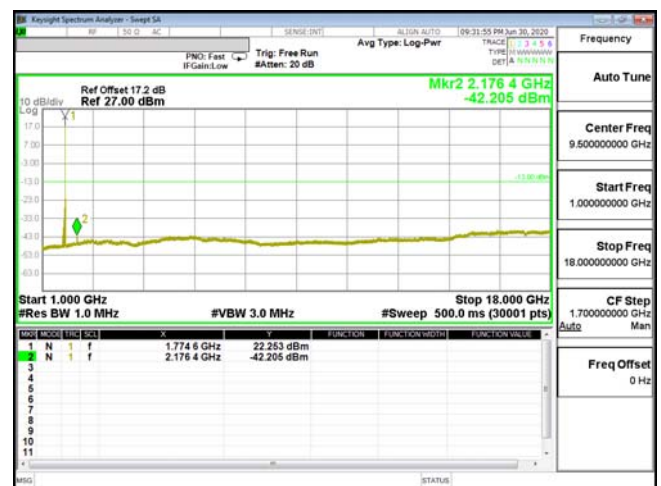
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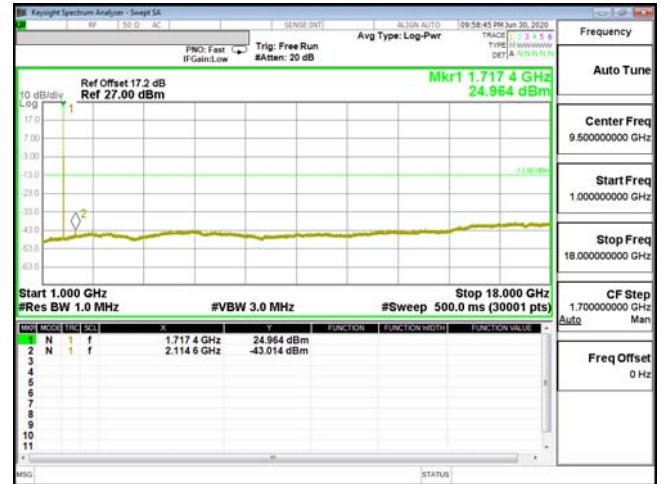
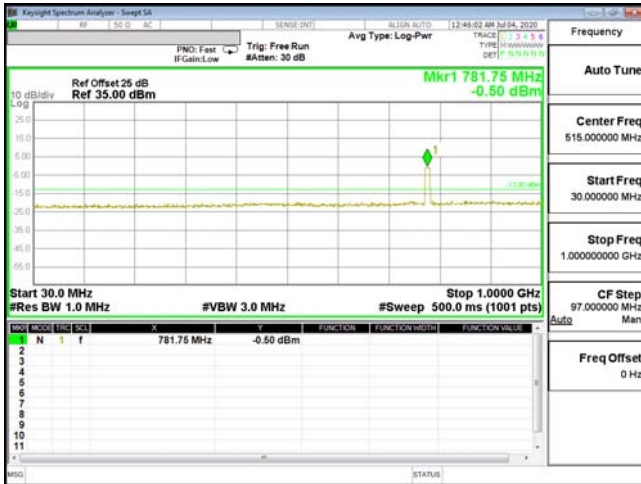
CSE-ENDC\_13A\_n66-16QAM\_10M\_CH349000\_1745(1,26)



CSE-ENDC\_13A\_n66-16QAM\_10M\_CH355000\_1775(1,26)

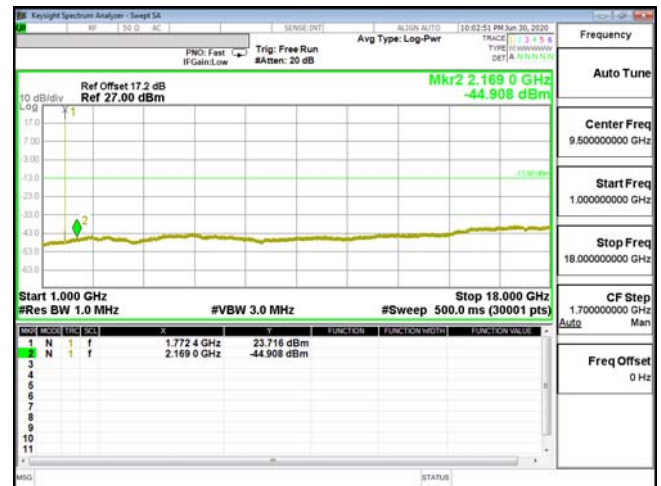
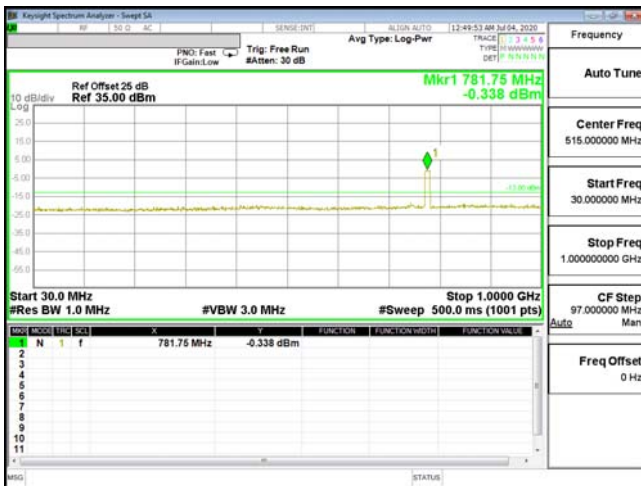


CSE-ENDC\_13A\_n66-16QAM\_10M\_CH355000\_1775(1,26)



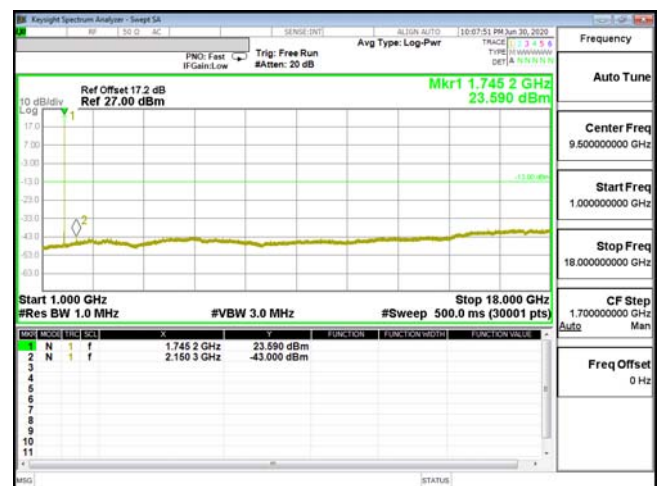
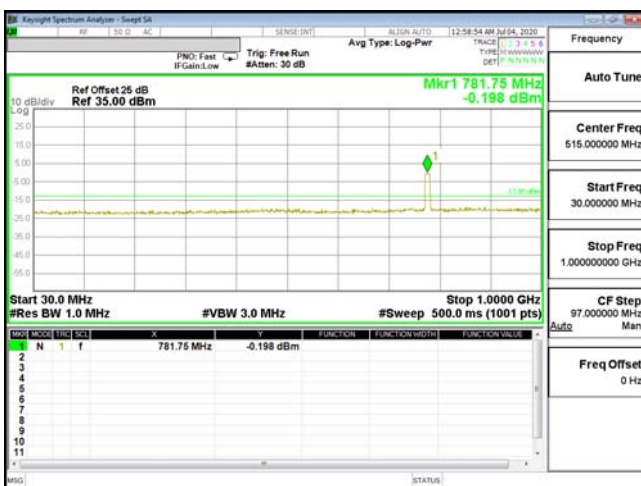
CSE-ENDC\_13A\_n66-16QAM\_15M\_CH343500\_1717.5(1,39)

CSE-ENDC\_13A\_n66-16QAM\_15M\_CH343500\_1717.5(1,39)



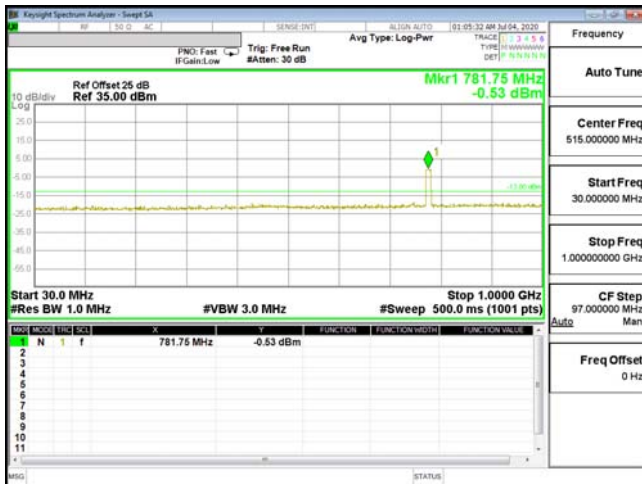
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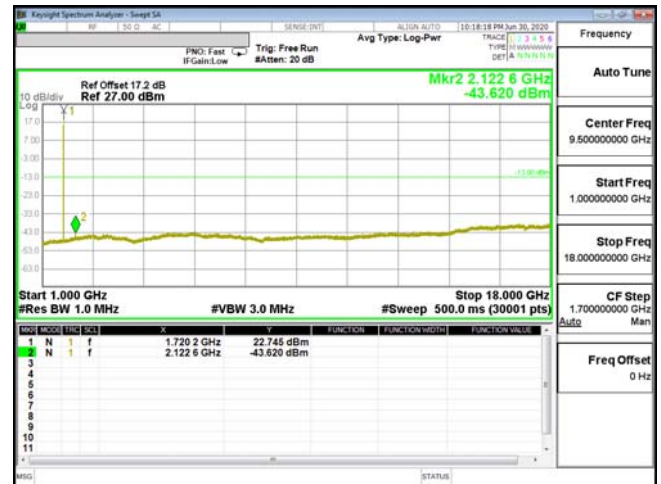


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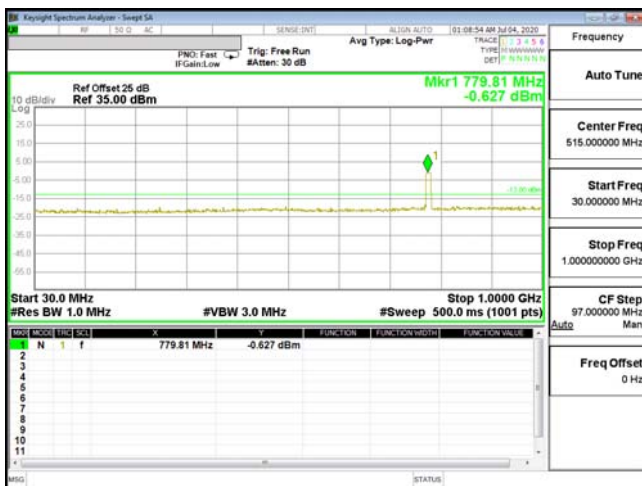
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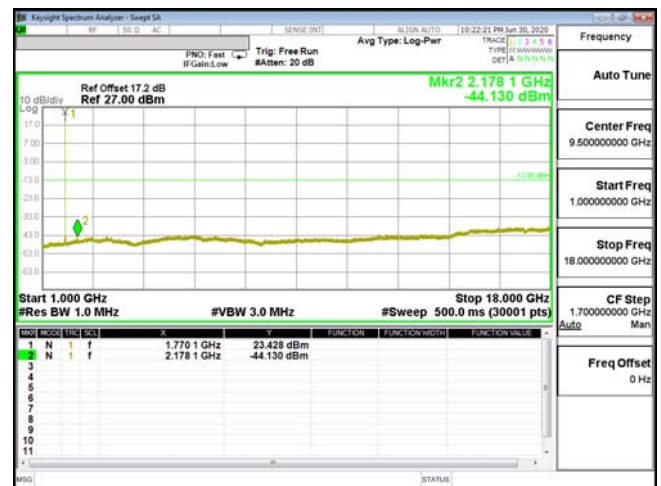
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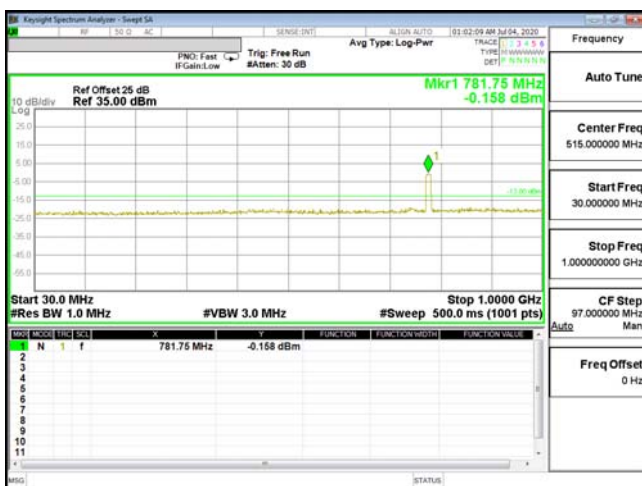
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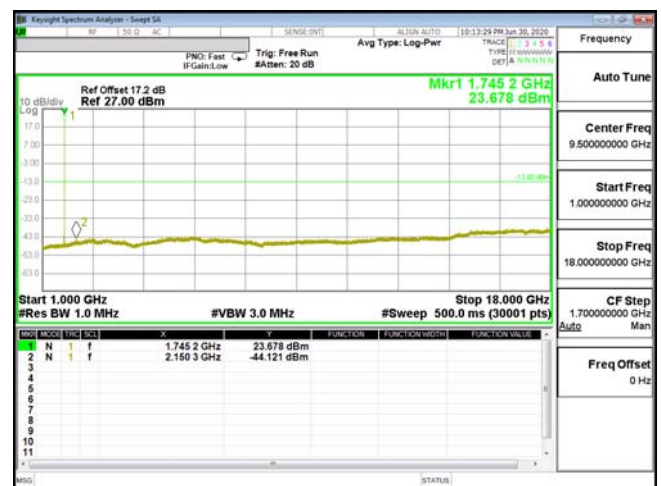
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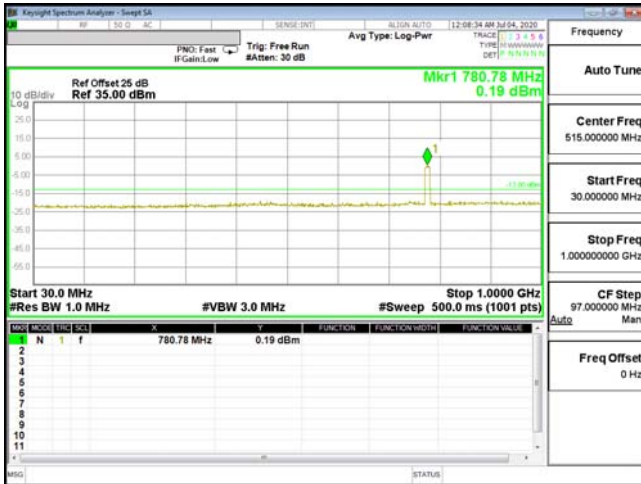


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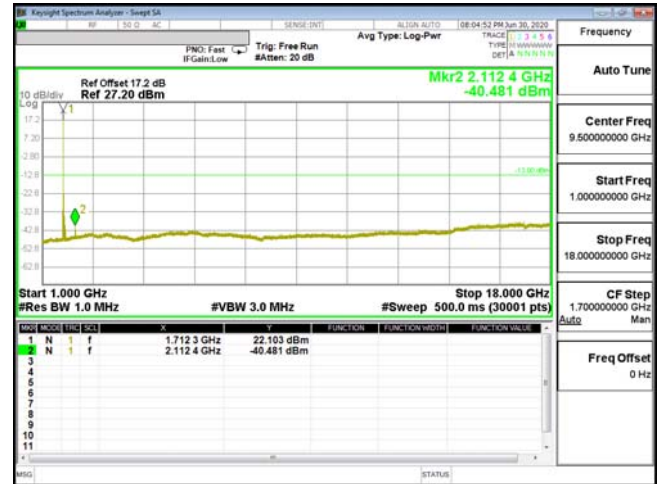


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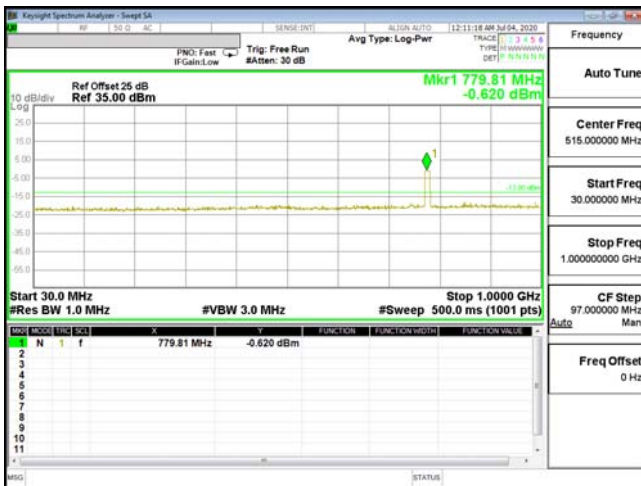




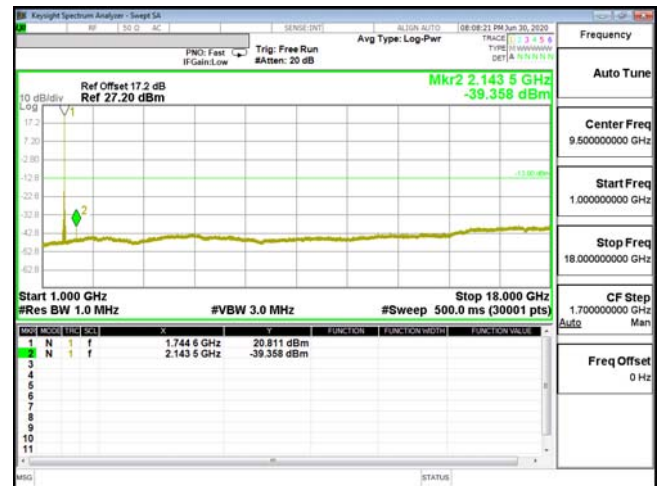
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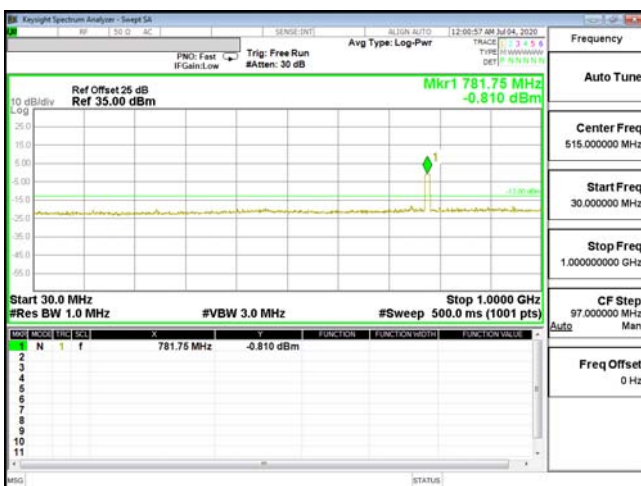
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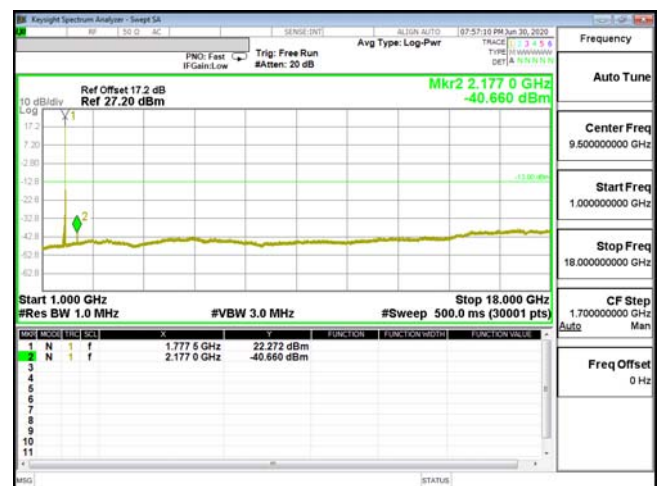
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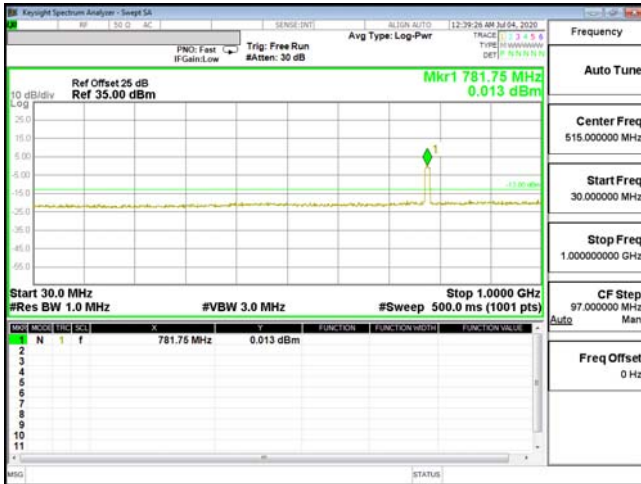
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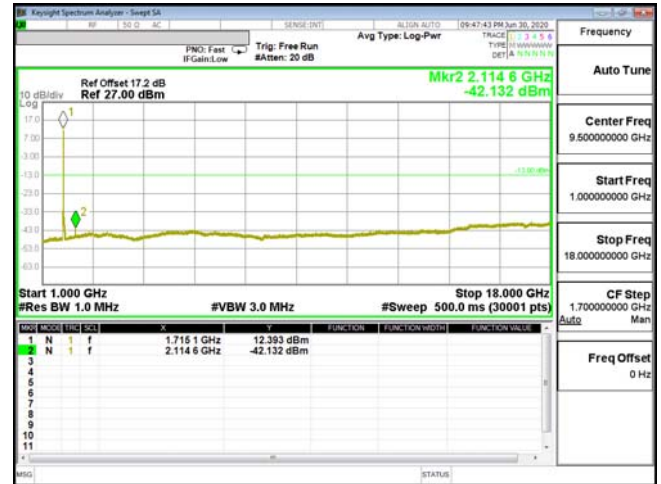
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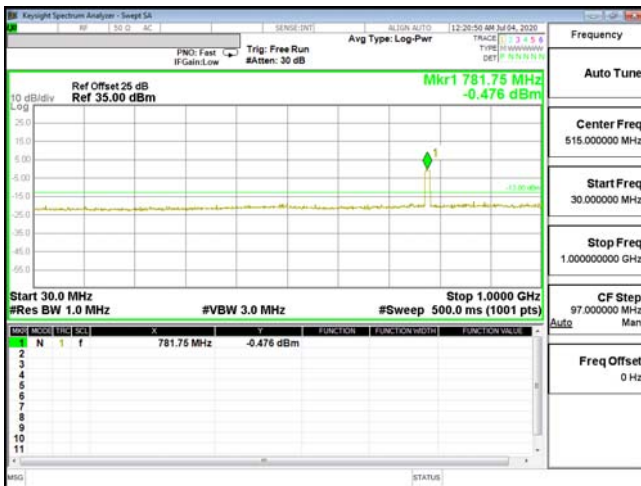
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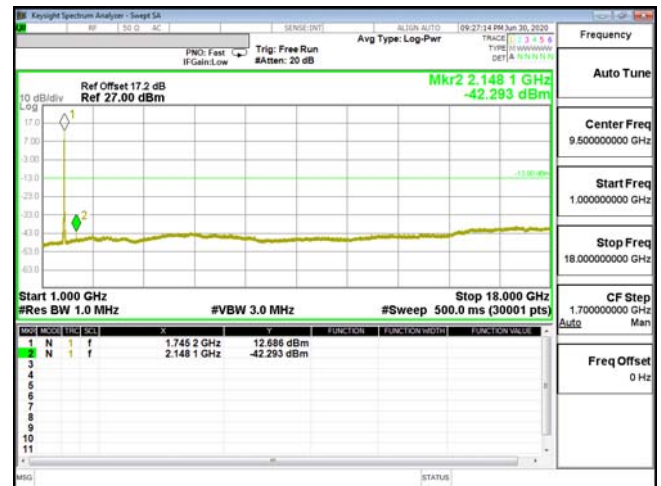
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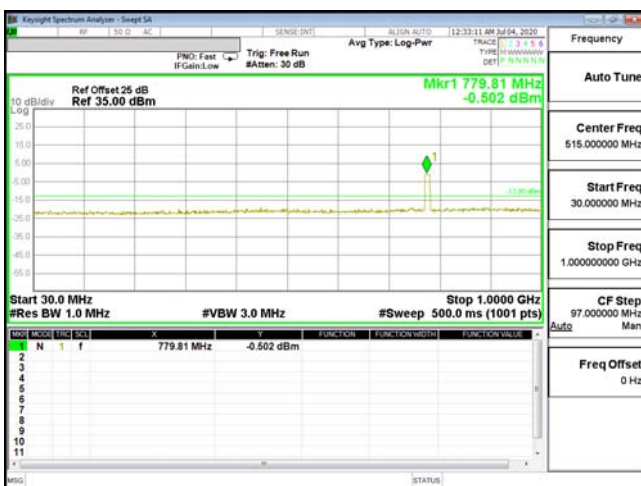
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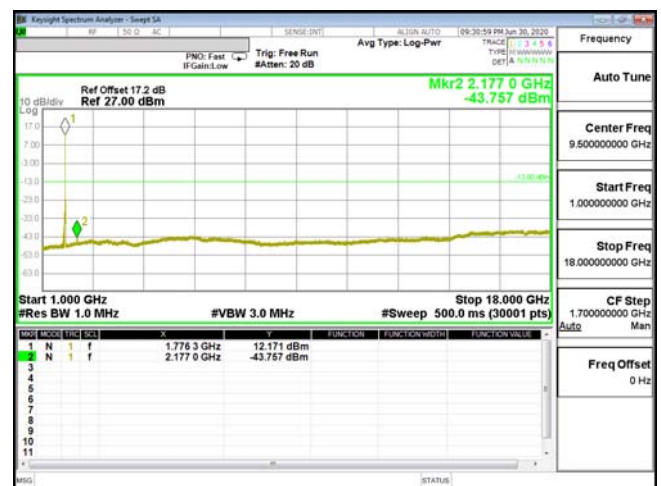
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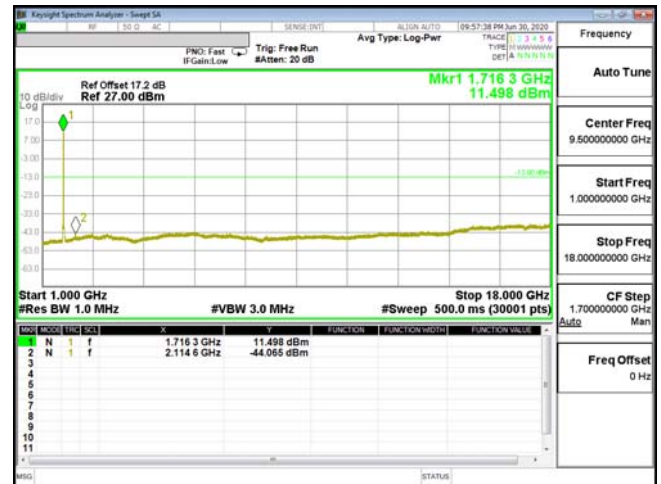
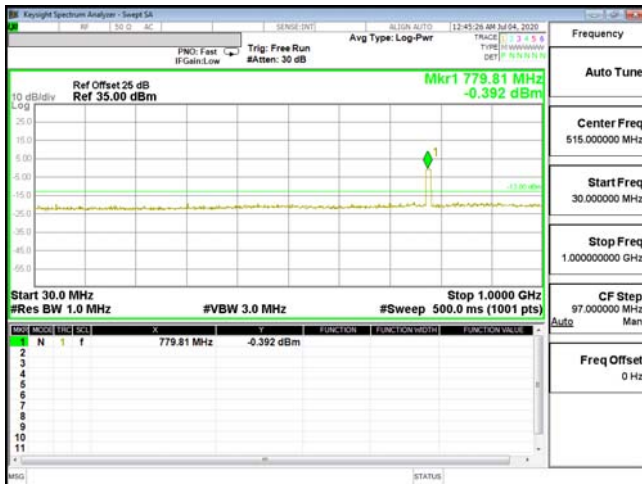
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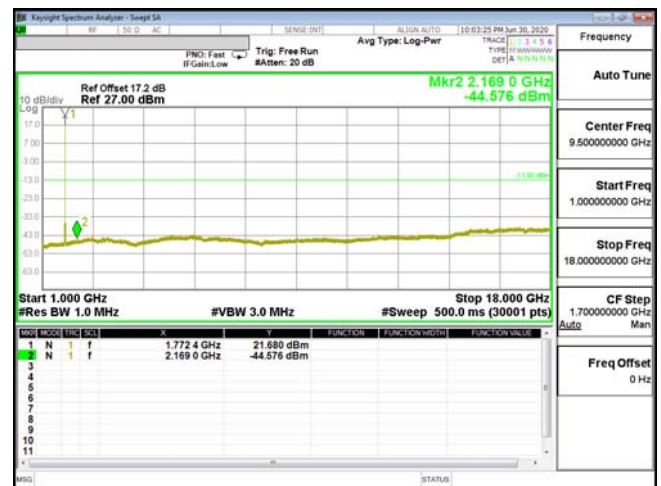
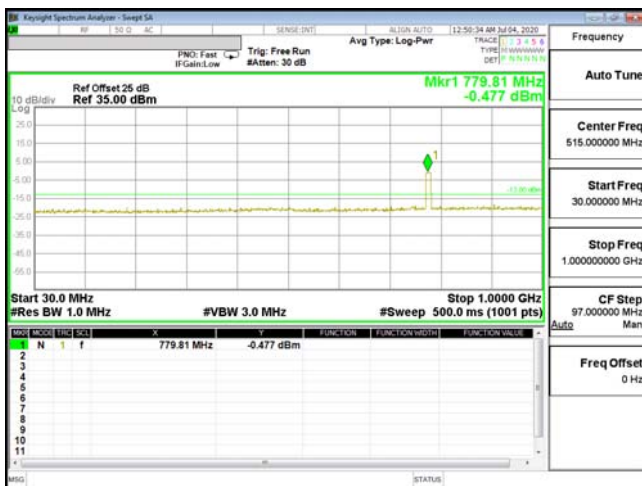


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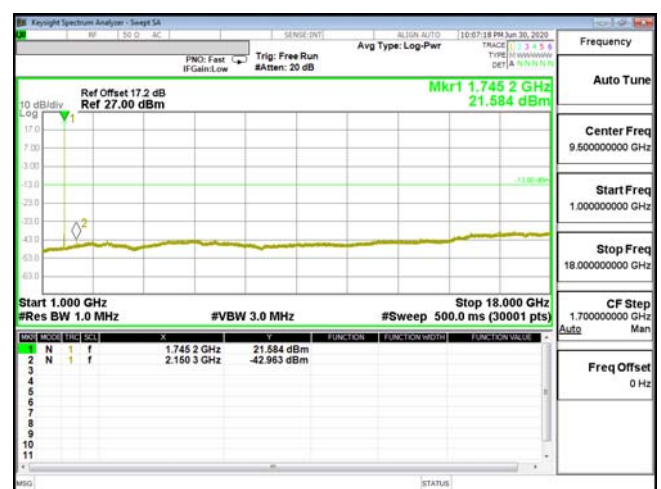
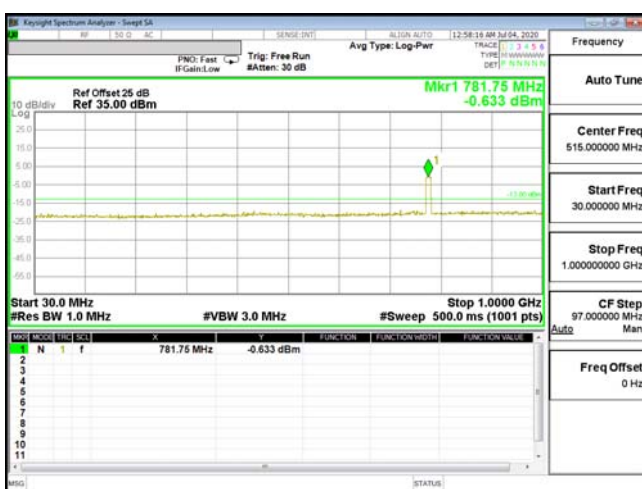
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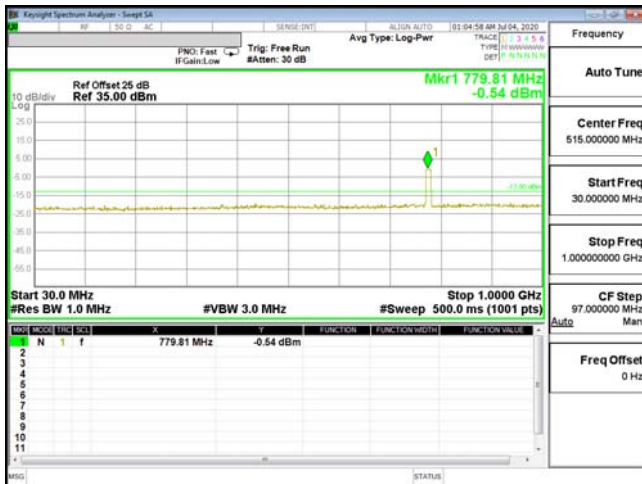
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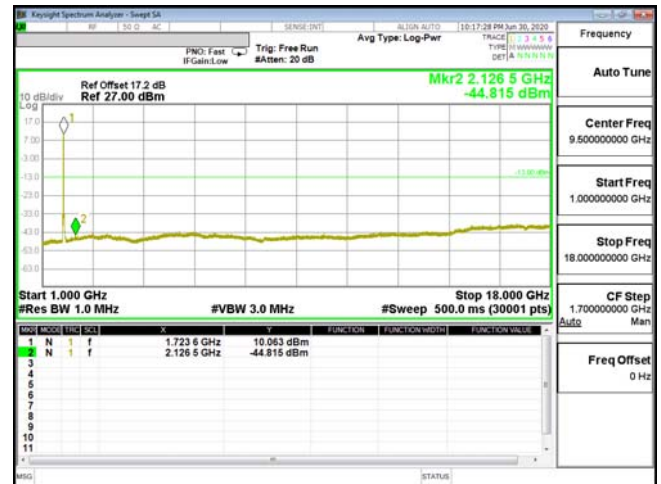
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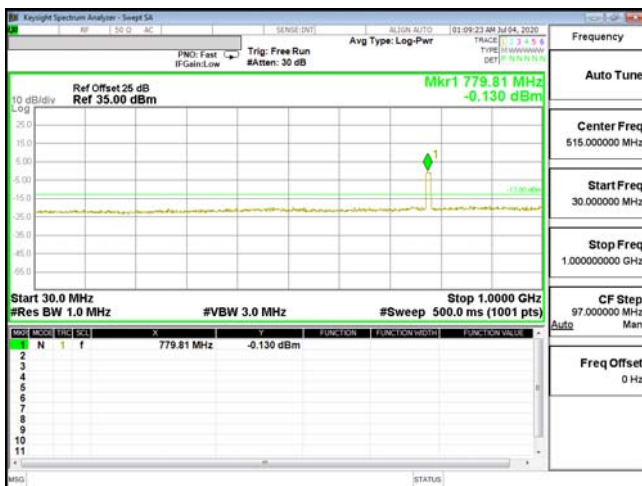




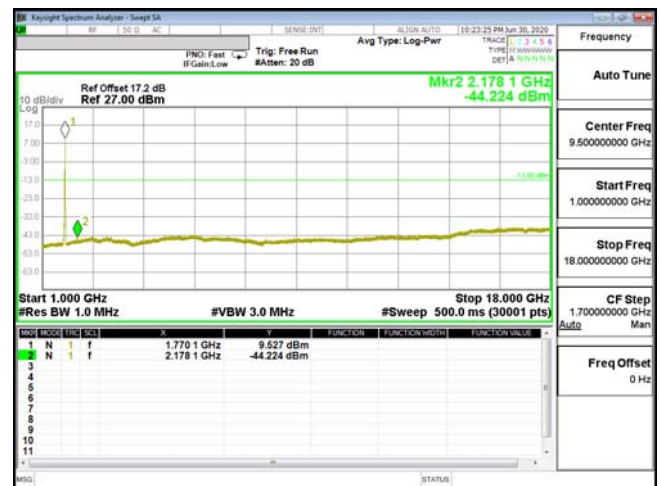
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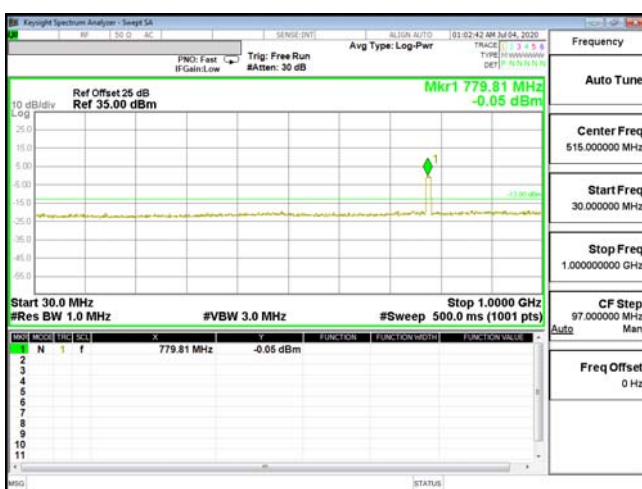
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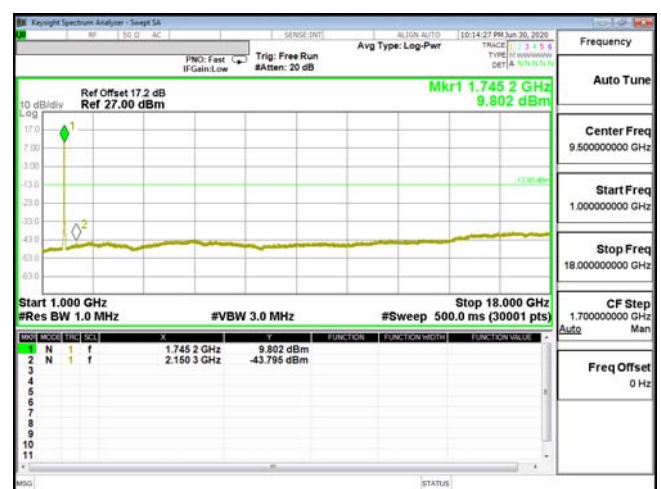
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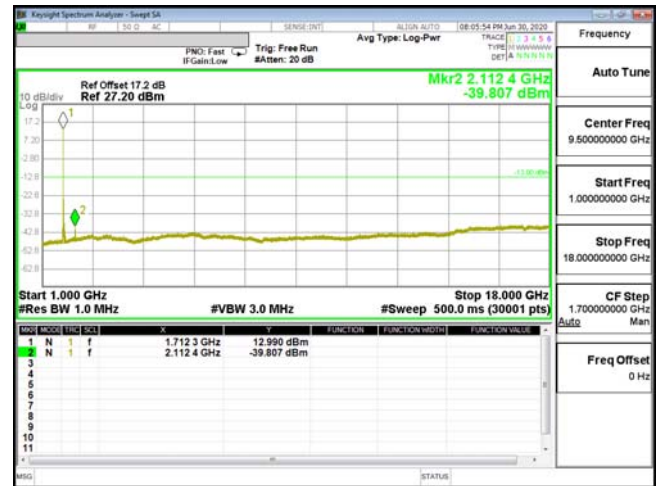
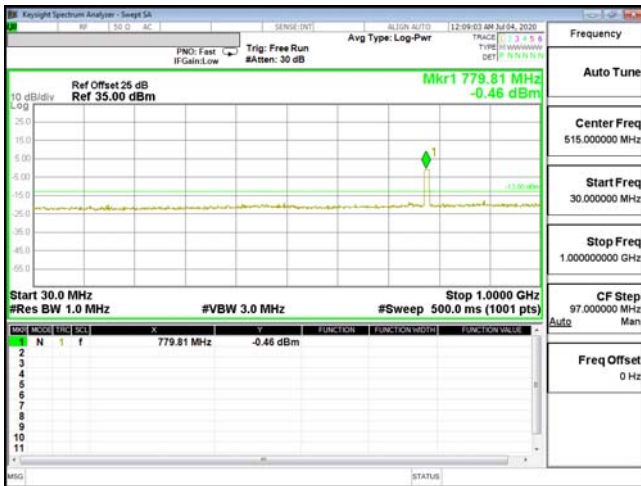
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CSE-ENDC\_13A\_n66-64QAM\_20M\_CH354900\_1745(100,6)

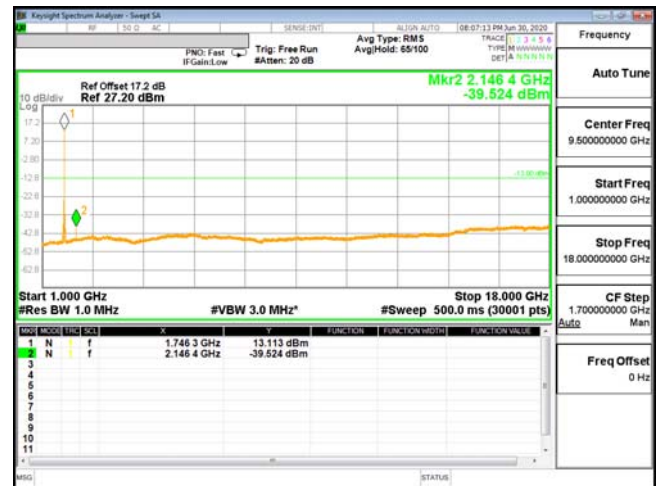


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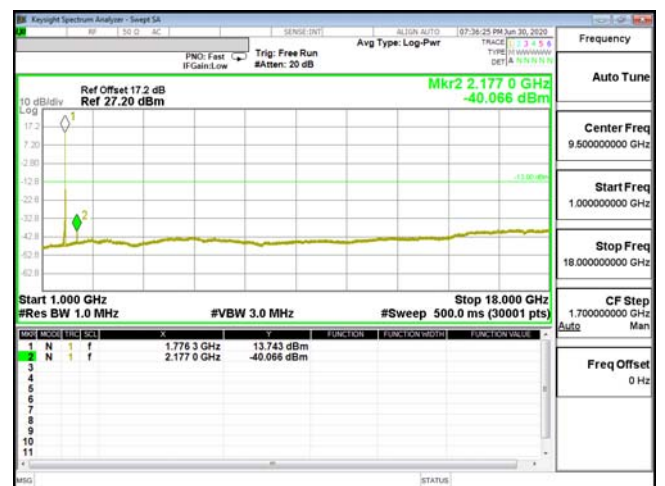
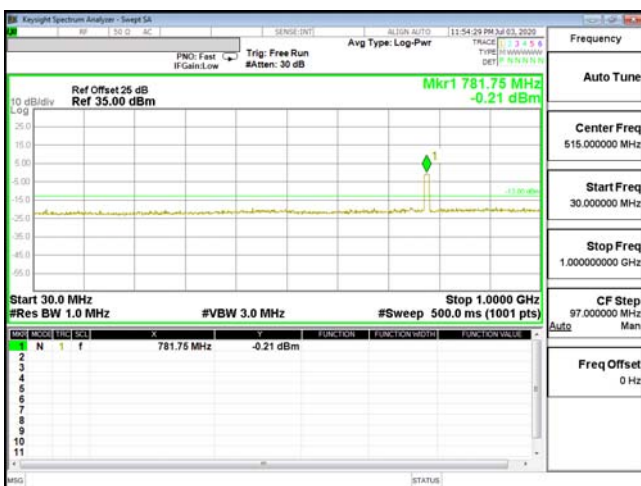
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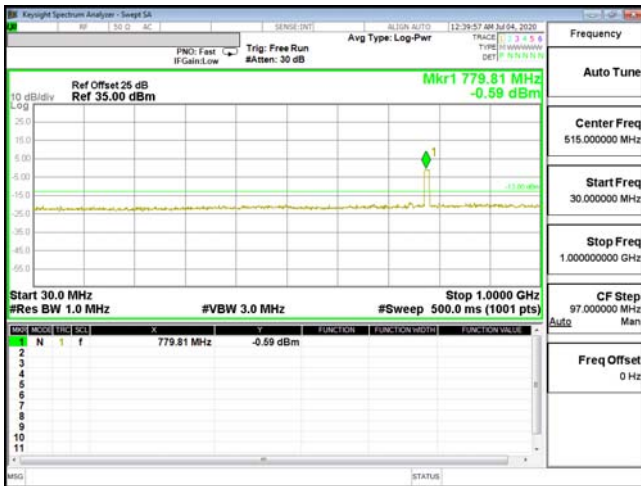
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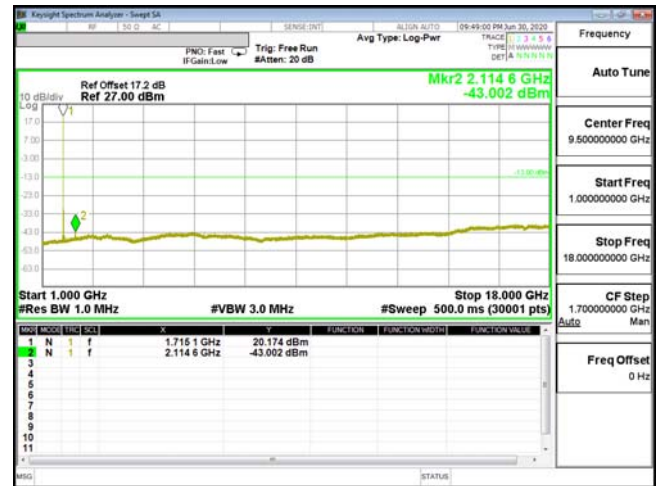


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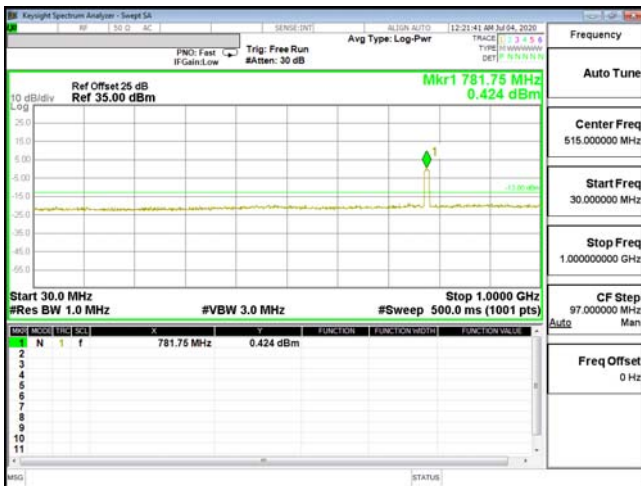
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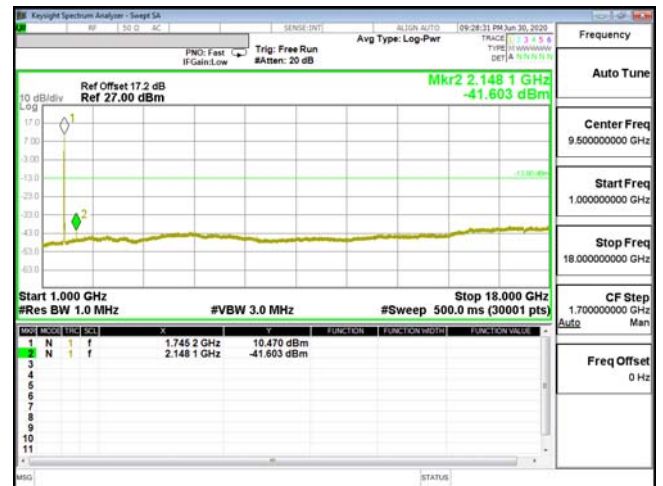
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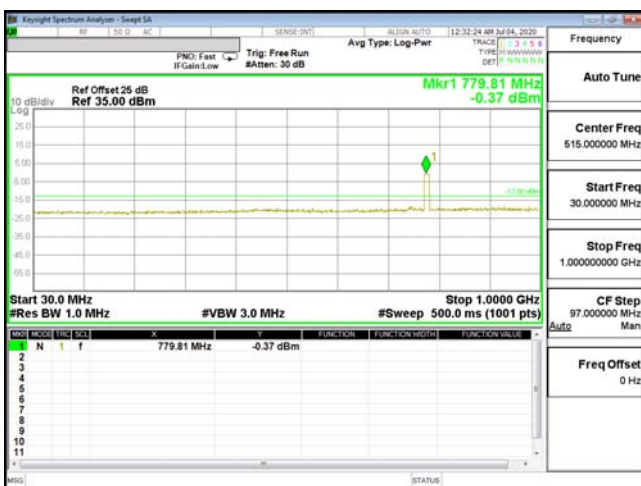
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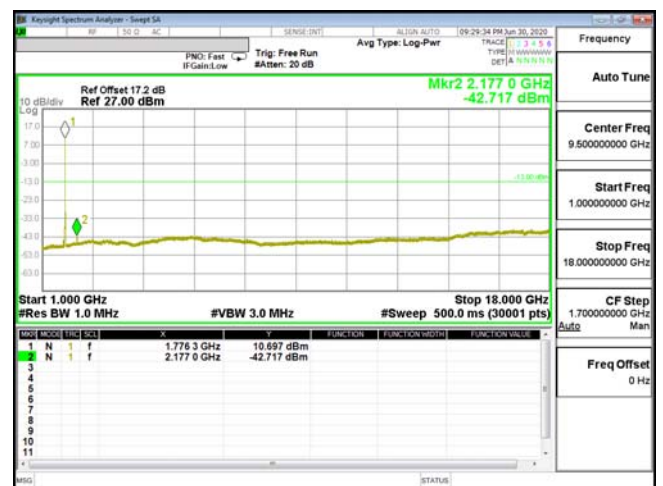
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CSE-ENDC\_13A\_n66-256QAM\_10M\_CH349000\_1745(50,2)



CSE-ENDC\_13A\_n66-256QAM\_10M\_CH355000\_1775(50,2)



CSE-ENDC\_13A\_n66-256QAM\_10M\_CH355000\_1775(50,2)