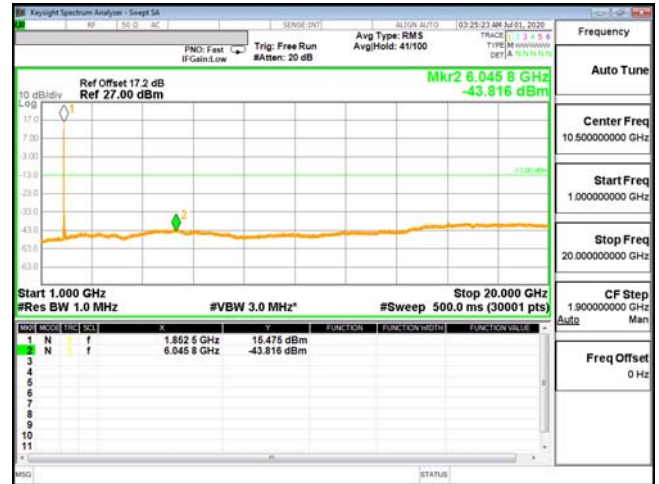
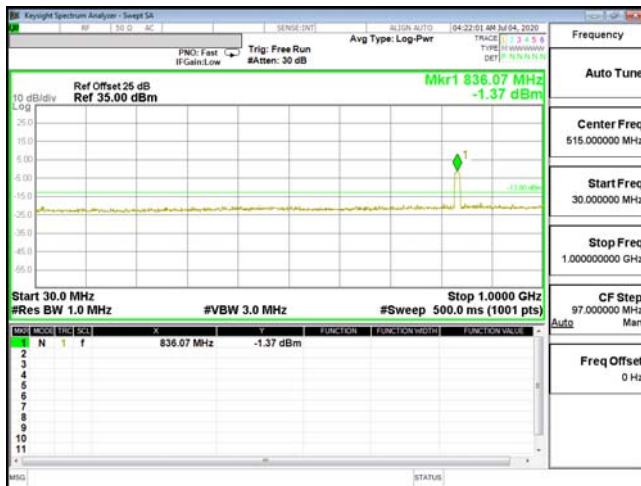


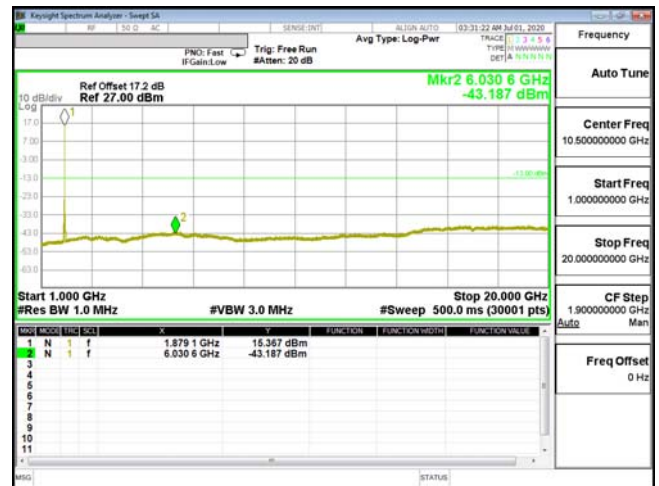
CSE-ENDC_5A_n2-64QAM_5M_CH370500_1852.5(25,0)



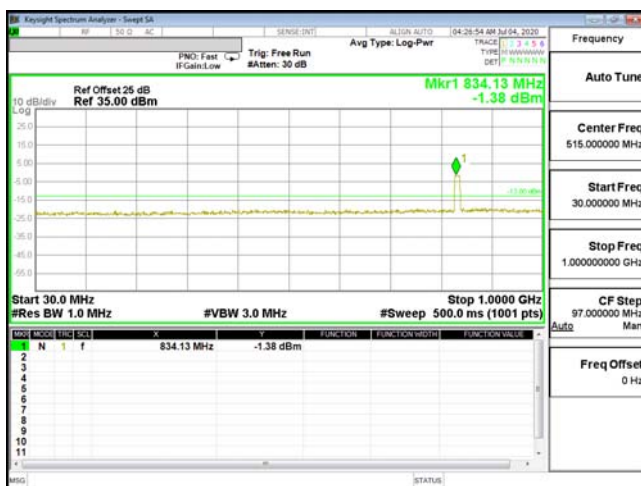
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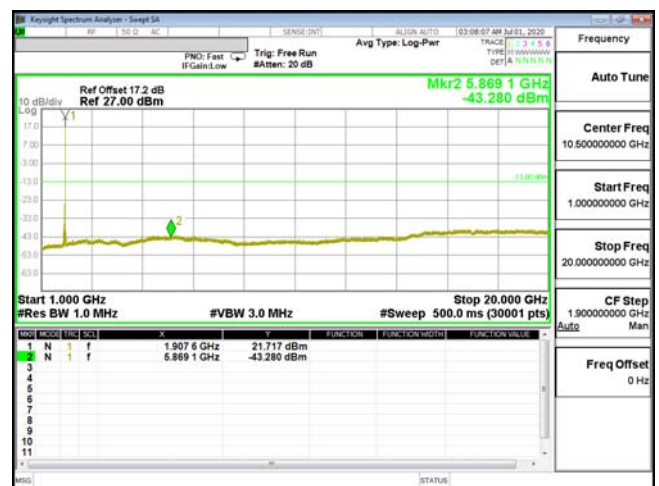
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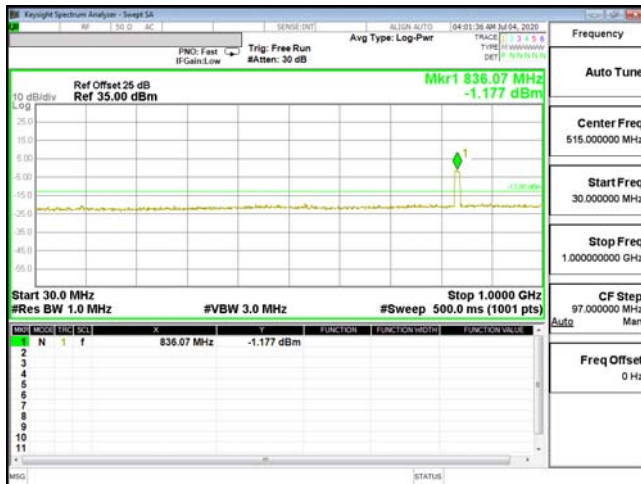
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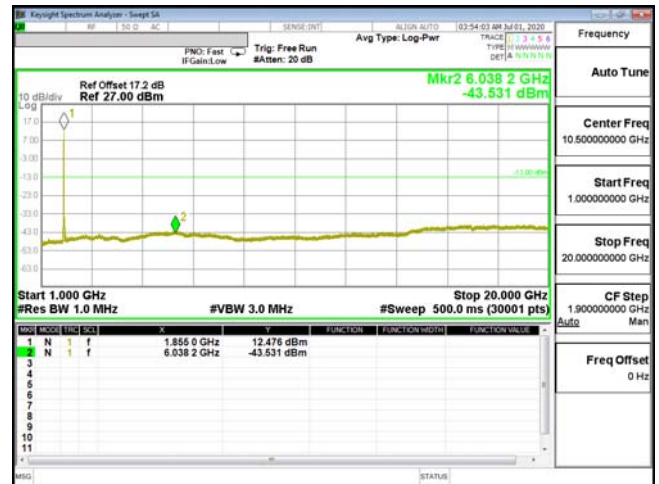
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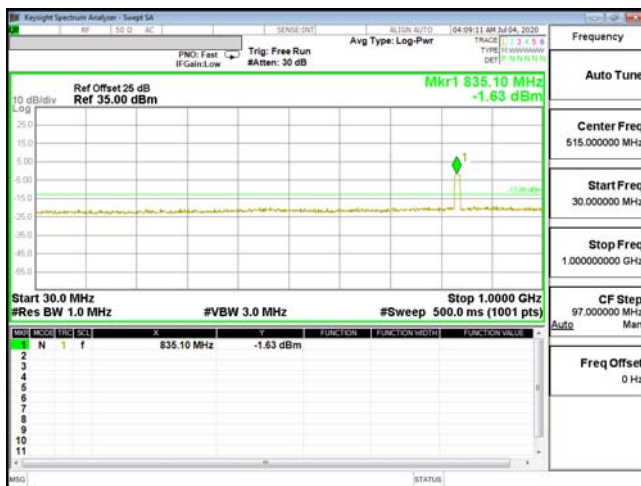
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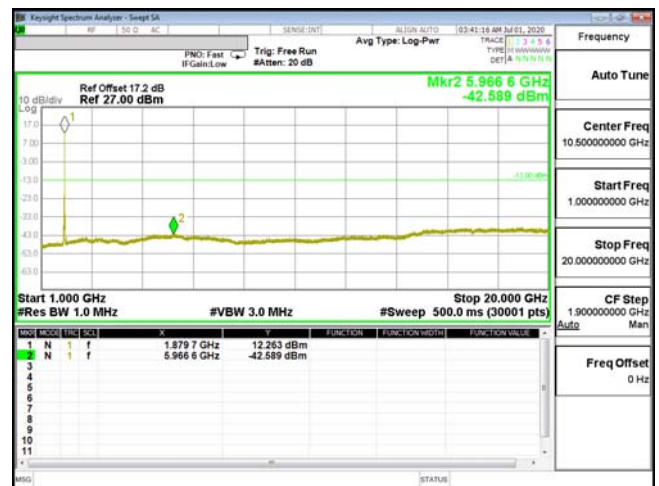
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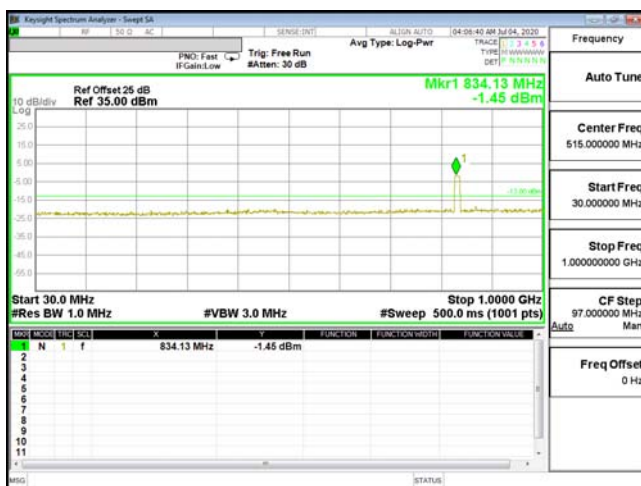
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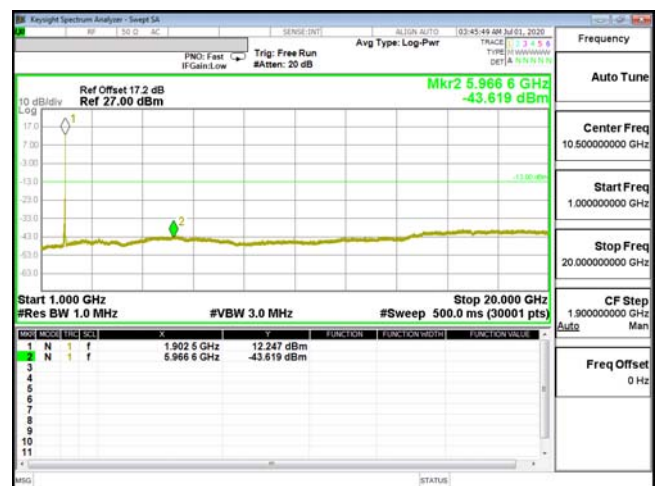
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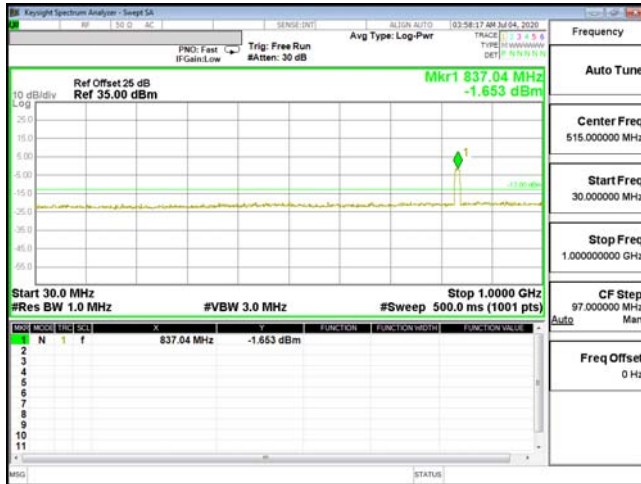
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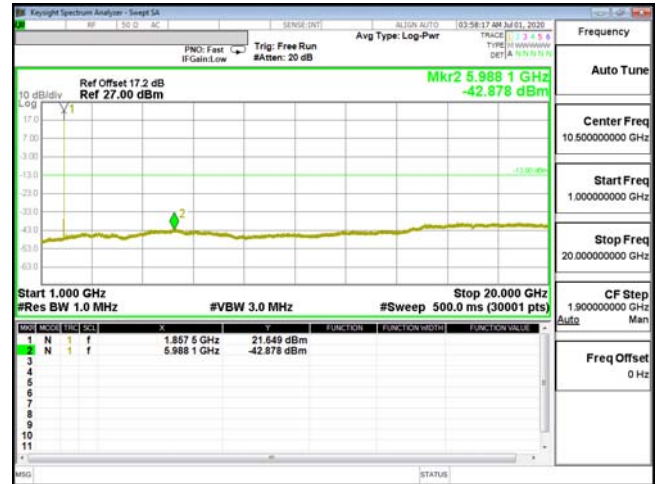
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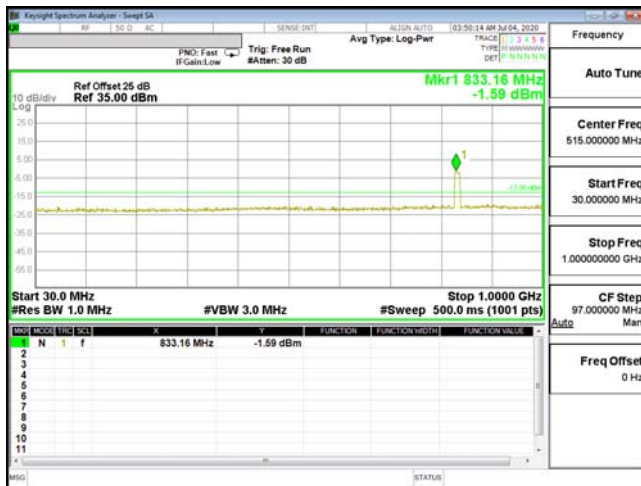
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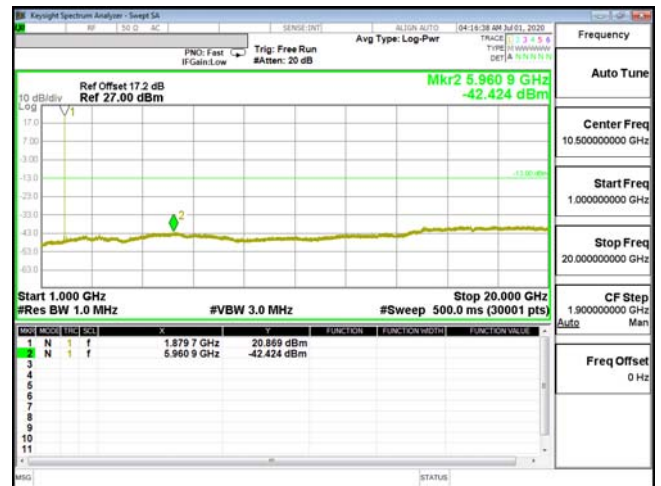
CSE-ENDC_5A_n2-64QAM_15M_CH371500_1857.5(1,39)



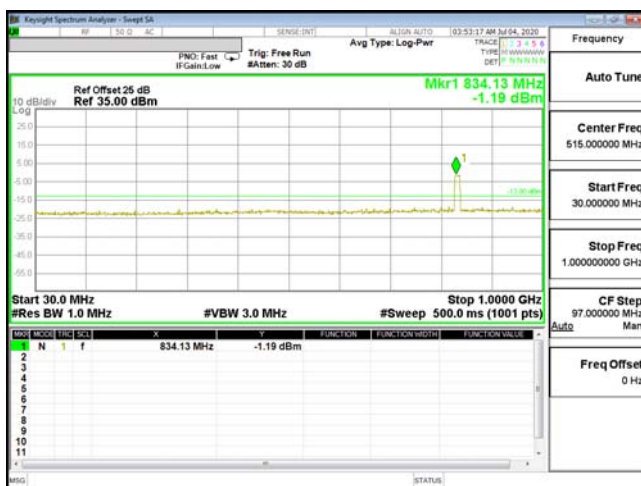
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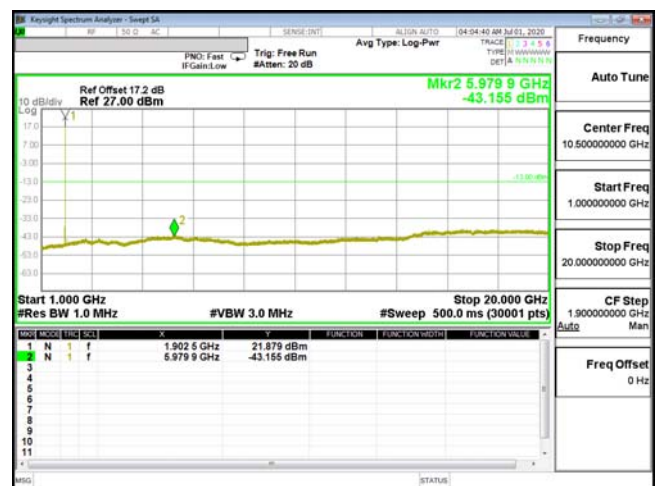
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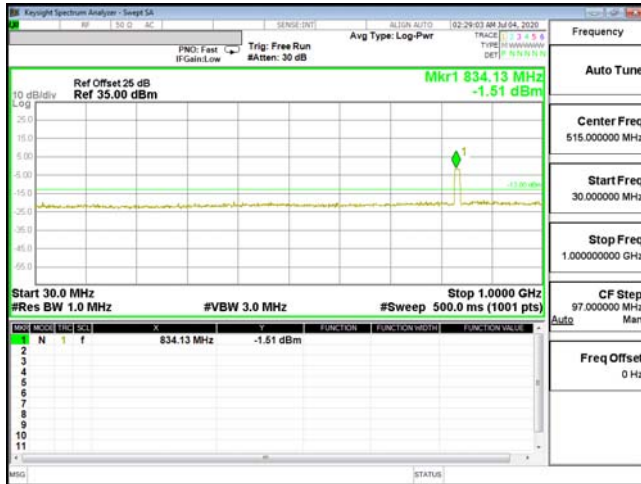
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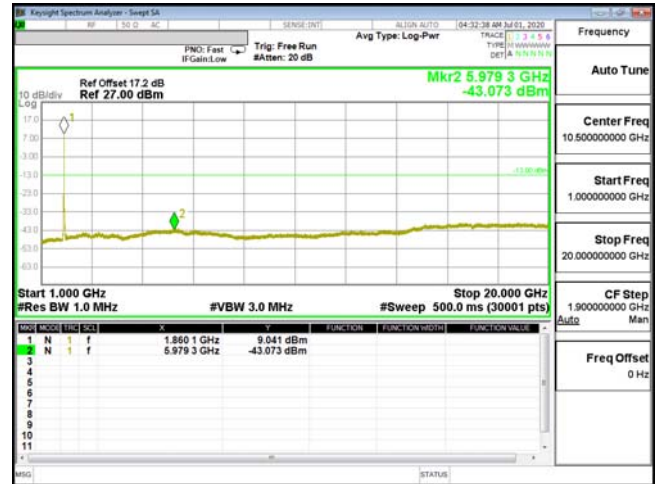
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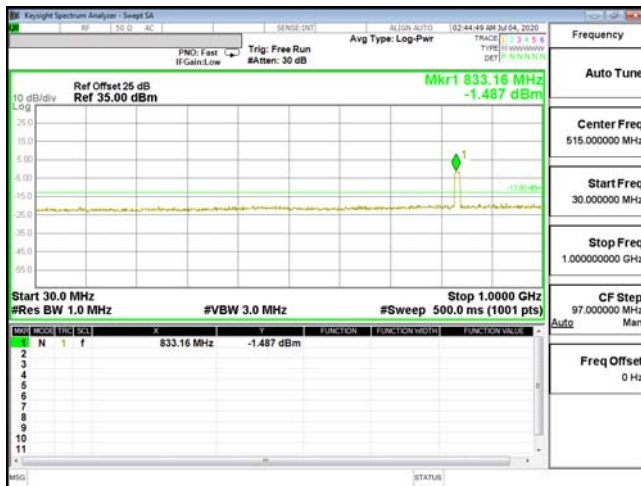
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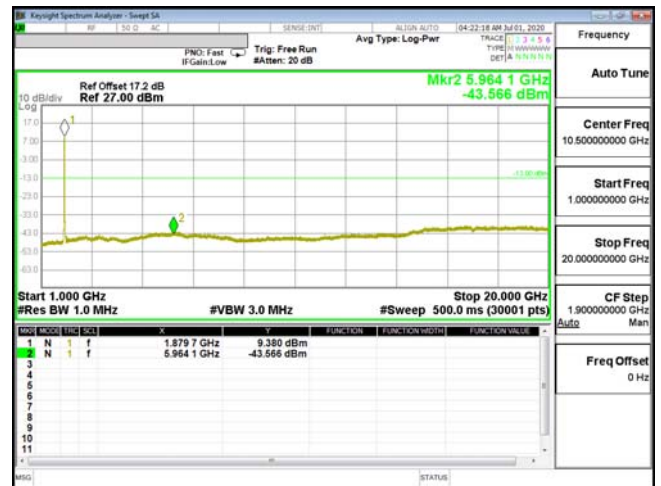
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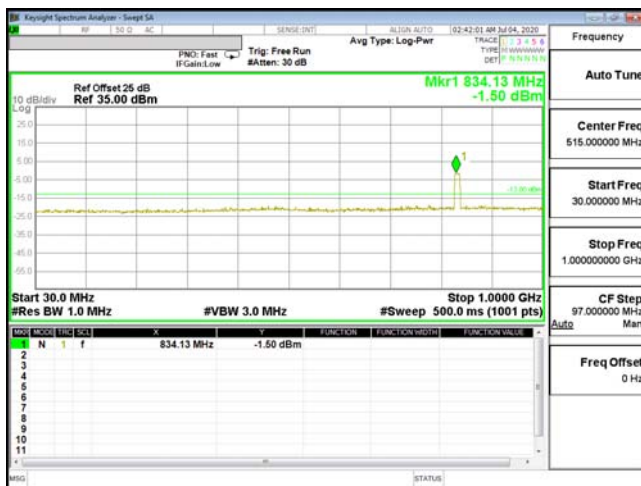
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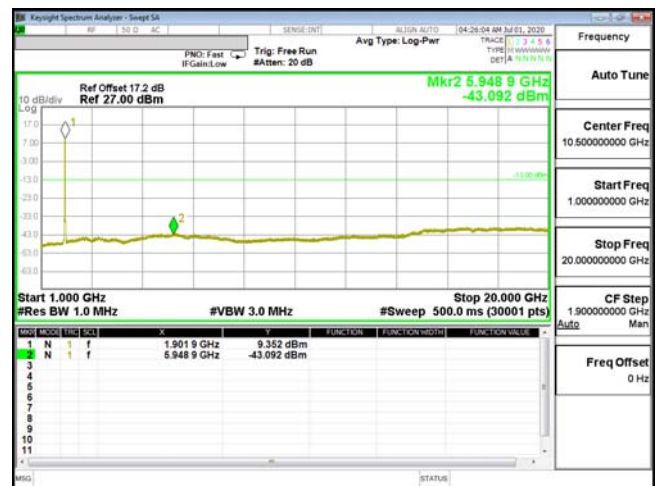
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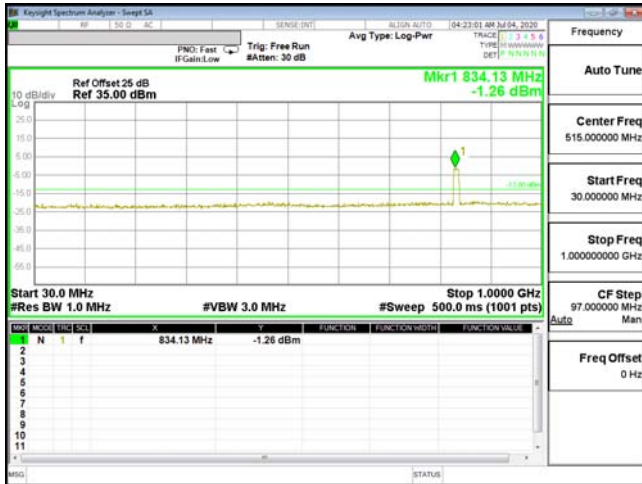
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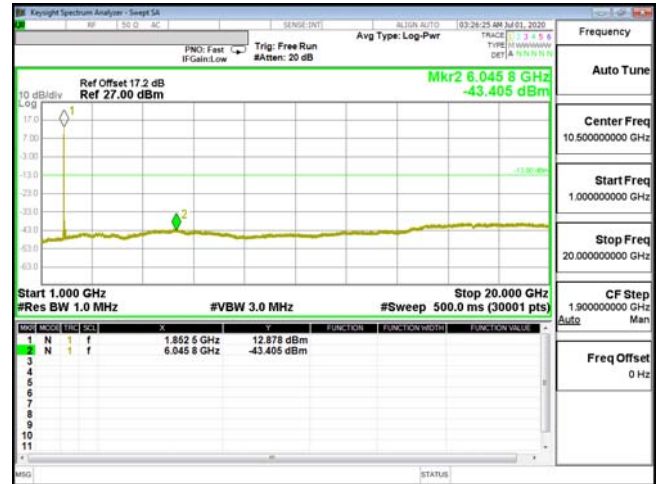
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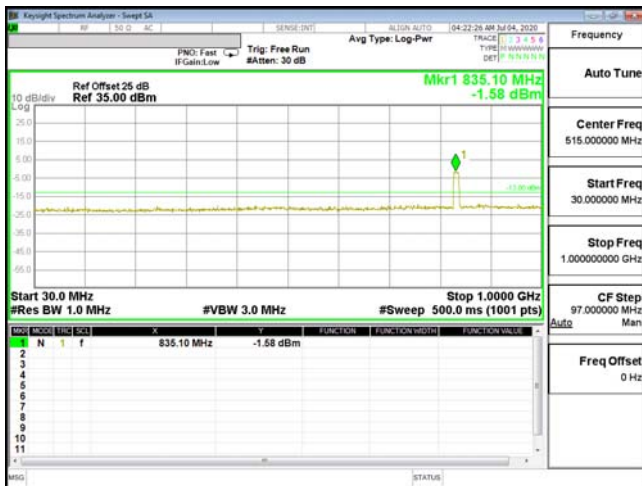
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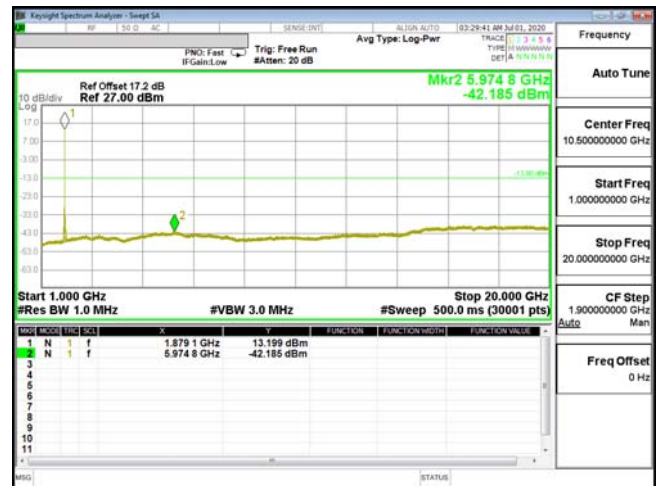
CSE-ENDC_5A_n2-256QAM_5M_CH370500_1852.5(25,0)



CSE-ENDC_5A_n2-256QAM_5M_CH370500_1852.5(25,0)



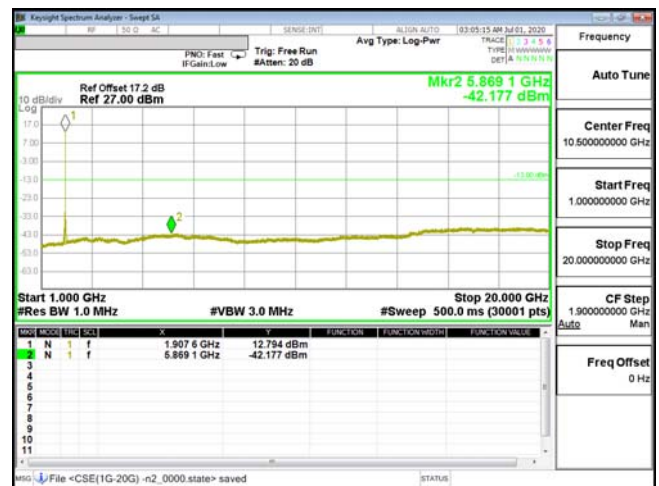
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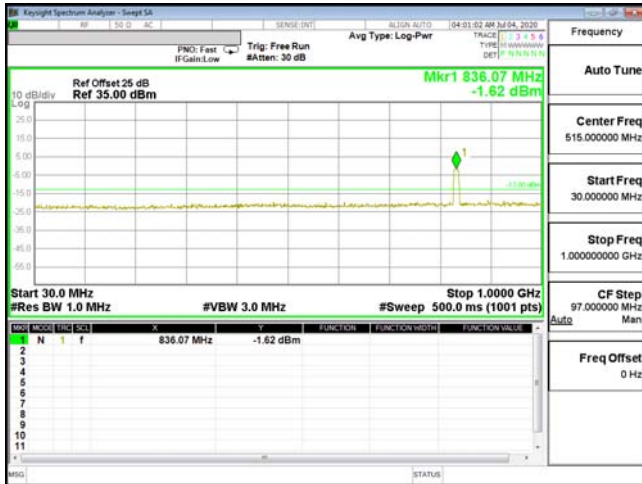
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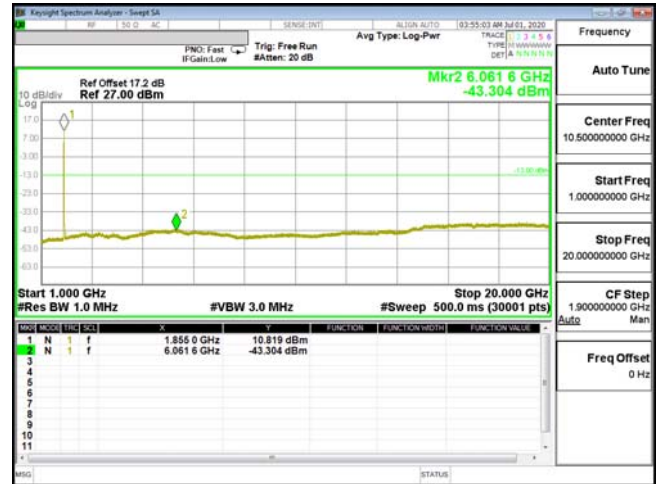
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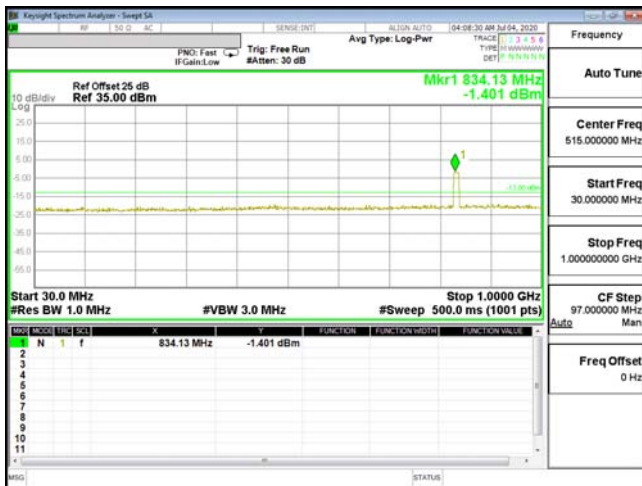
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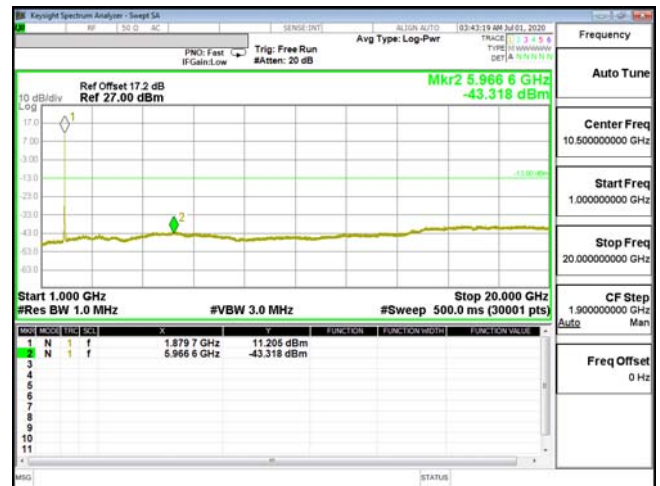
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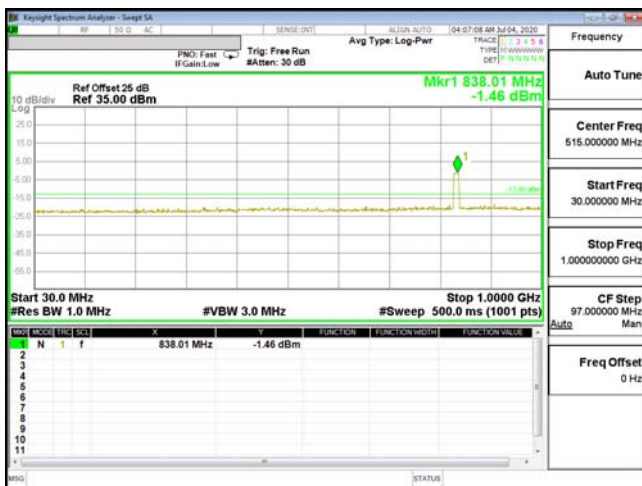
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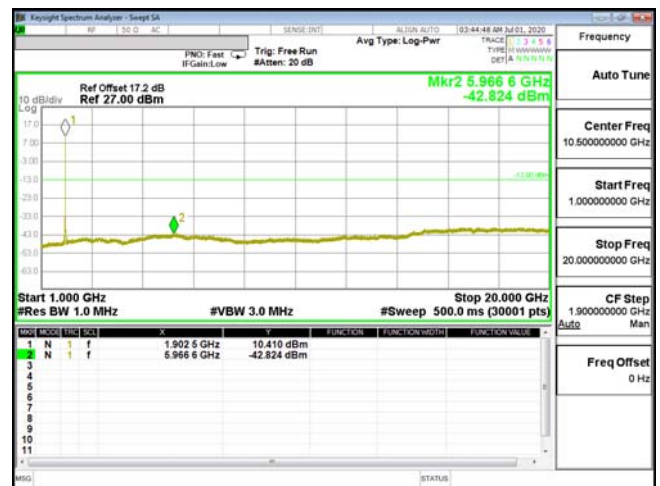
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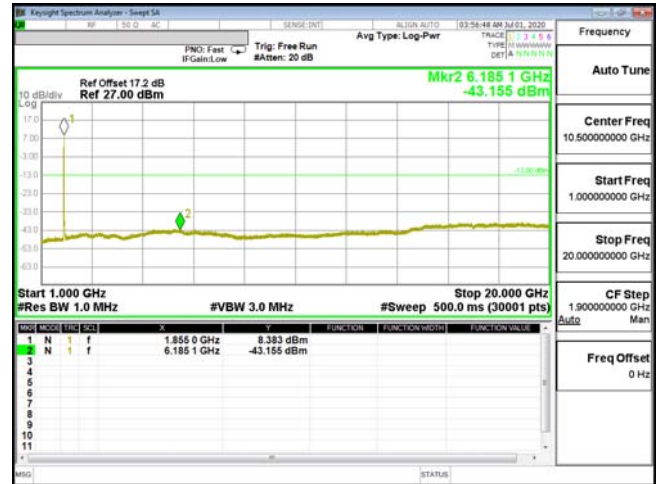
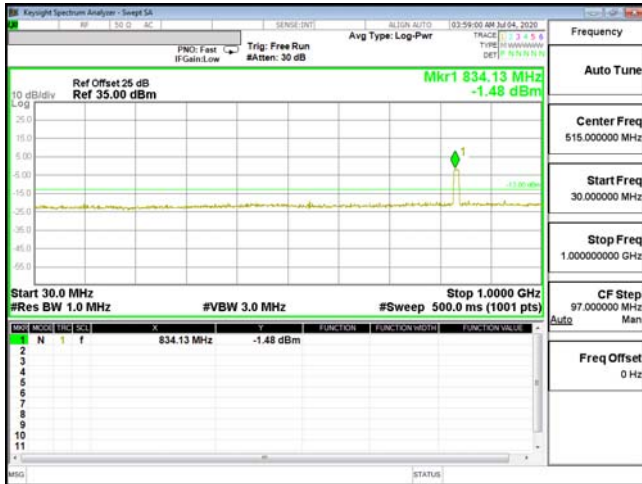
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CSE-ENDC_5A_n2-256QAM_10M_CH381000_1905(50,0)

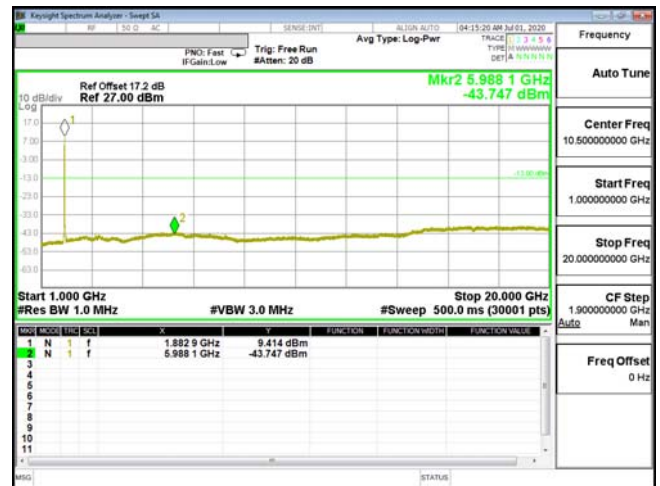
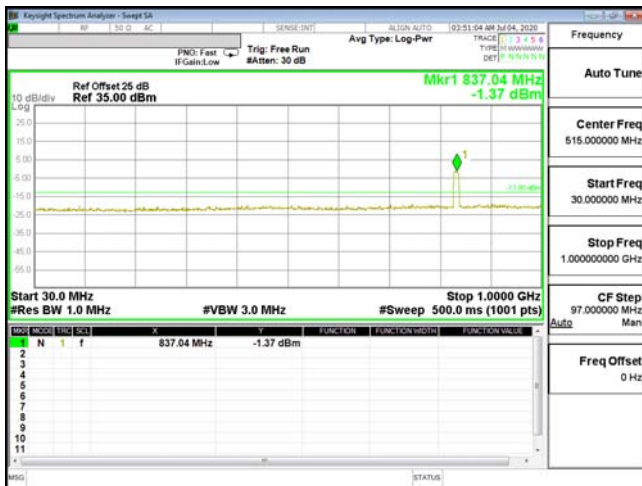


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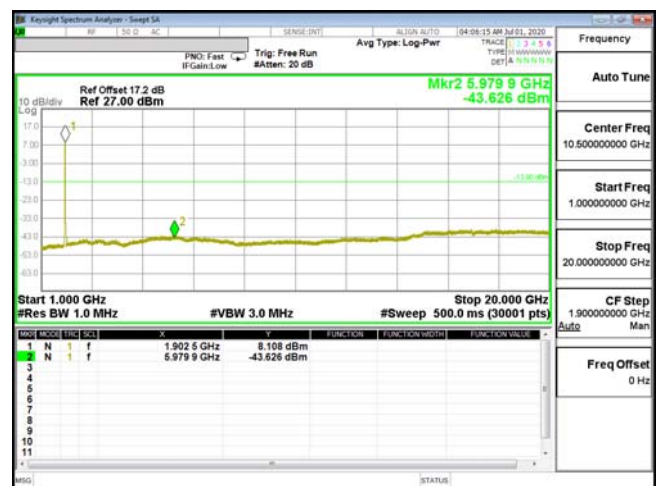
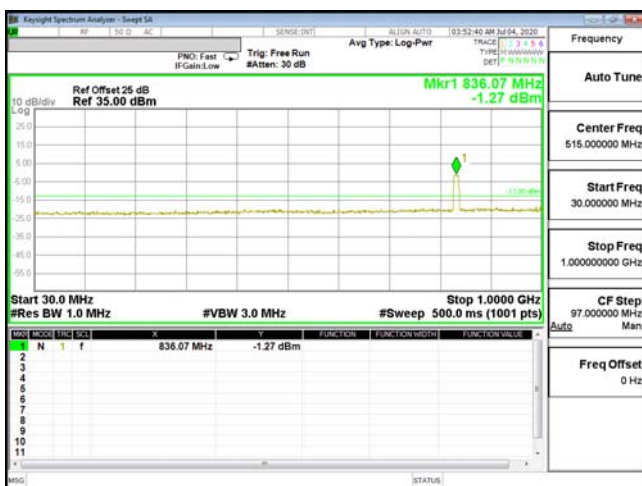
CSE-ENDC_5A_n2-256QAM_15M_CH371500_1857.5(75,0)

CSE-ENDC_5A_n2-256QAM_15M_CH371500_1857.5(75,0)



CSE-ENDC_5A_n2-256QAM_15M_CH376000_1880(75,4)

CSE-ENDC_5A_n2-256QAM_15M_CH376000_1880(75,4)

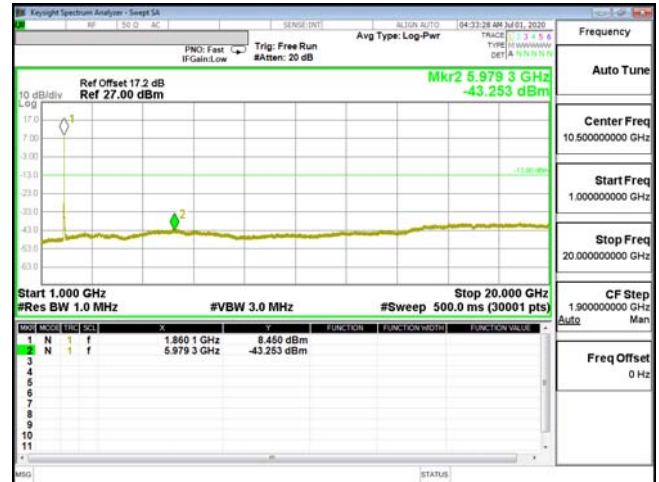


CSE-ENDC_5A_n2-256QAM_15M_CH380500_1902.5(75,4)

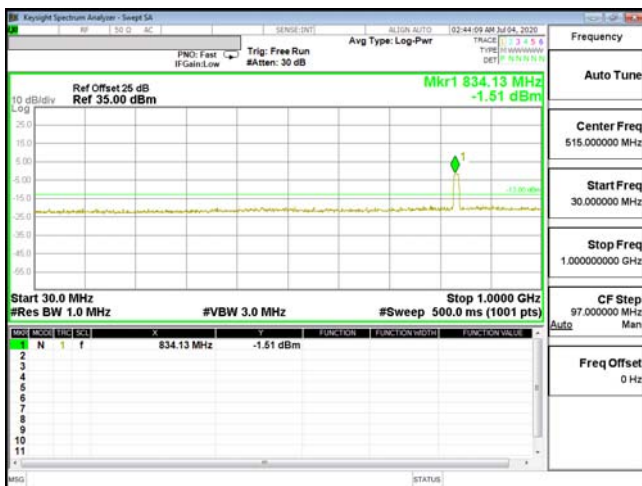
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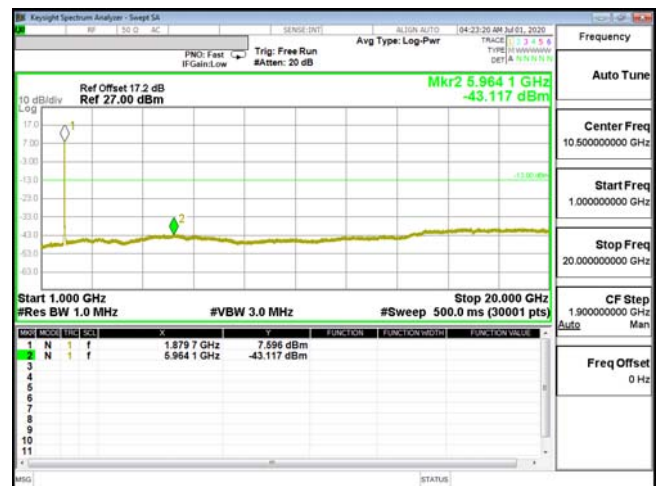
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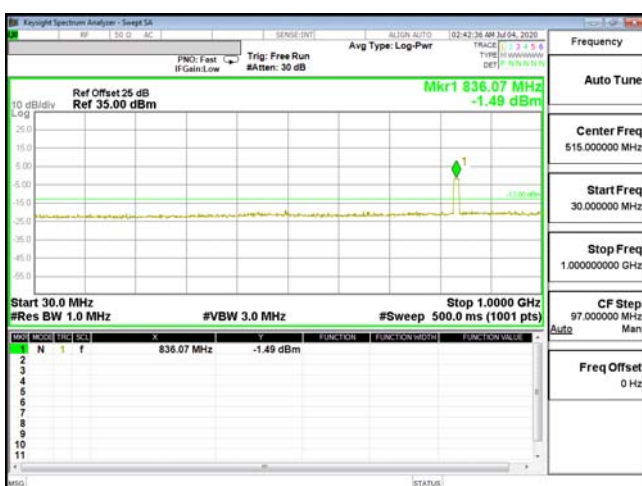
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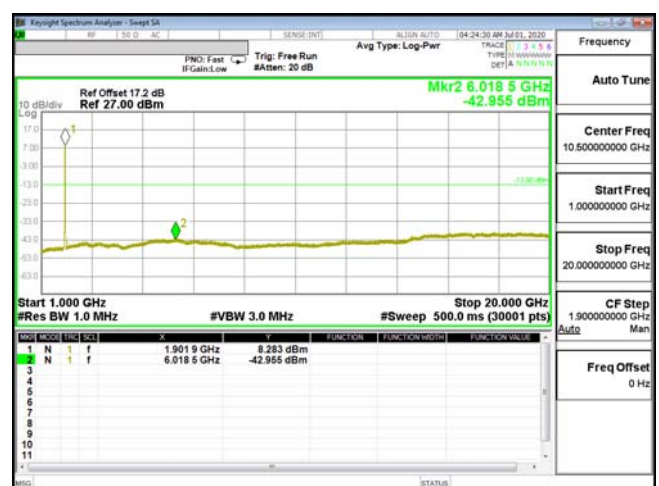
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CSE-ENDC_5A_n2-256QAM_20M_CH376000_1880(100,0)

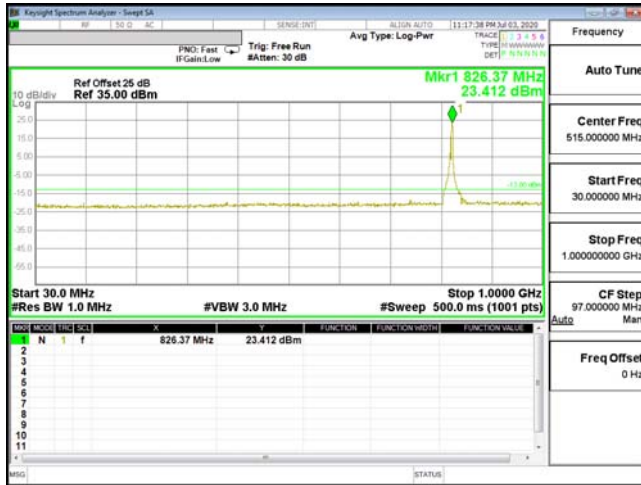


CSE-ENDC_5A_n2-256QAM_20M_CH380000_1900(100,0)



CSE-ENDC_5A_n2-256QAM_20M_CH380000_1900(100,0)

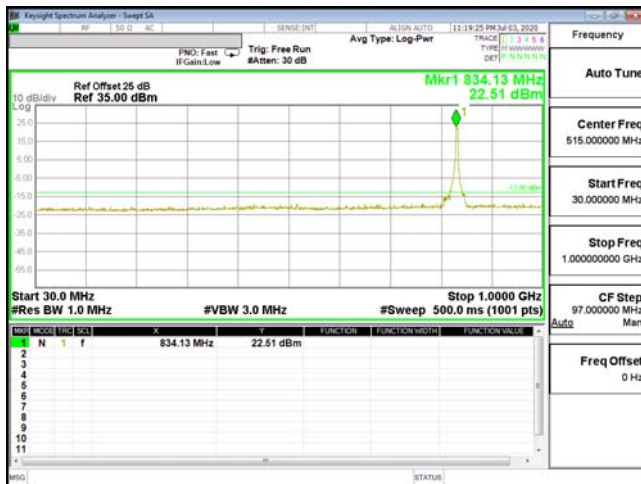
Product	LV55		
Test Item	Spurious Emissions (Conducted)		
Date of Test	2020/07/02	Test Site	SR12-H
Test Condition	ENDC n5	Test Range	30MHz~10GHz



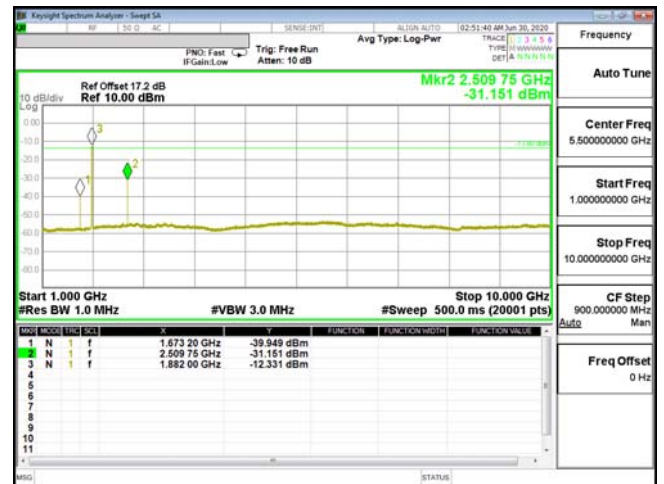
CSE-ENDC_2A_n5-PI2-BPSK_5M_CH165300_826.5(1,12)



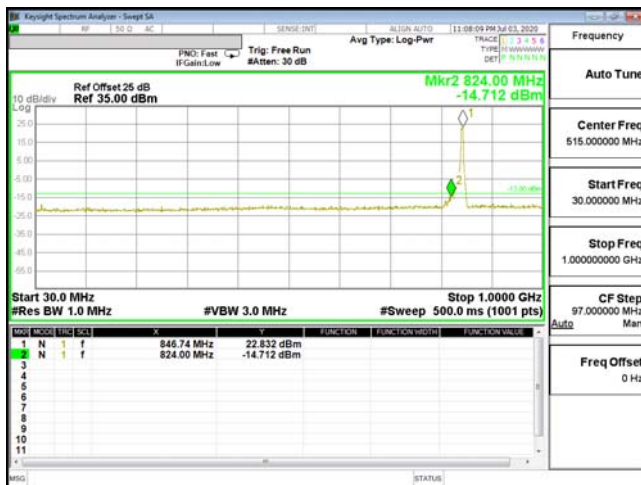
CSE-ENDC_2A_n5-PI2-BPSK_5M_CH165300_826.5(1,12)



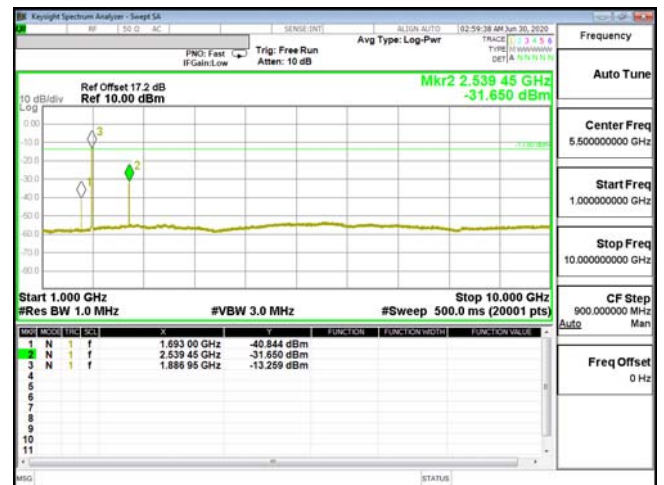
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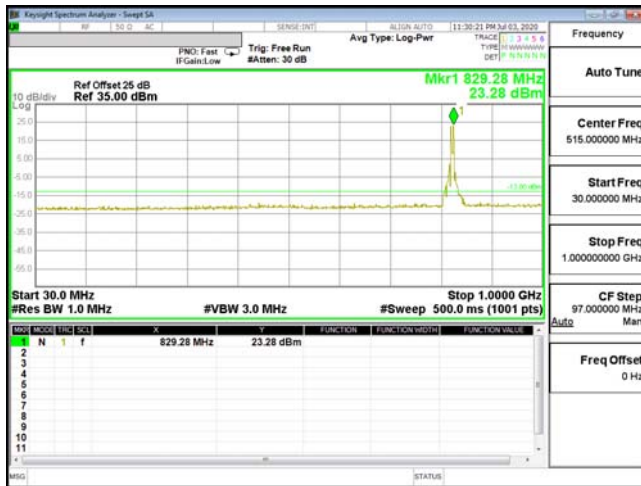
CSE-ENDC_2A_n5-PI2-BPSK_5M_CH167300_836.5(1,12)



CSE-ENDC_2A_n5-PI2-BPSK_5M_CH169300_846.5(1,12)



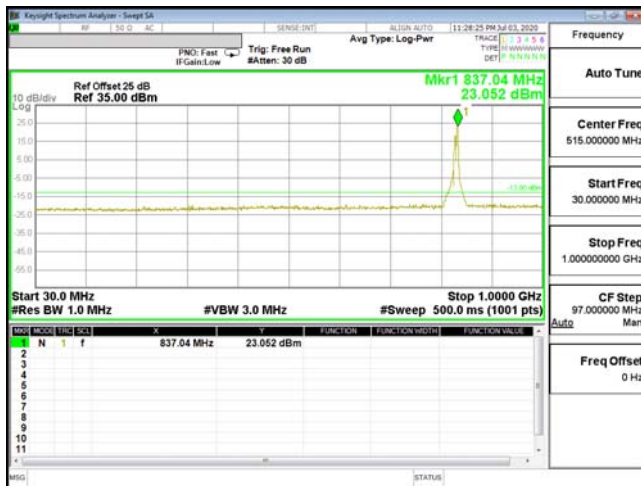
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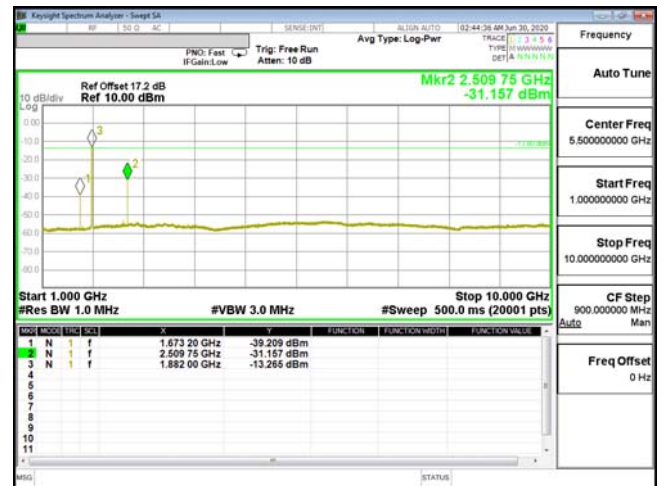
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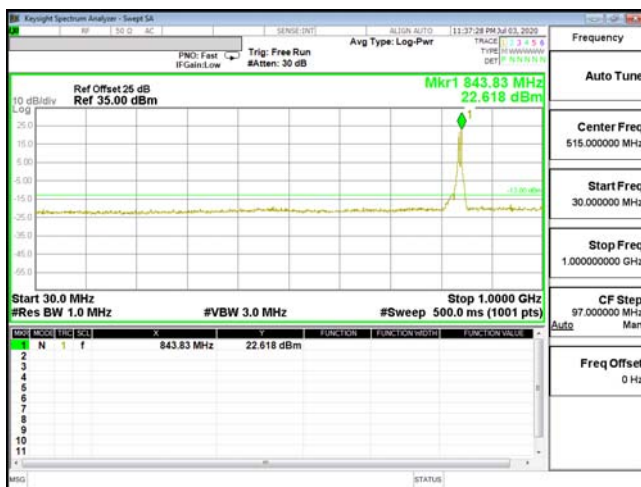
CSE-ENDC_2A_n5-PI2-BPSK_10M_CH165800_829(1,26)



CSE-ENDC_2A_n5-PI2-BPSK_10M_CH167300_836.5(1,26)



CSE-ENDC_2A_n5-PI2-BPSK_10M_CH167300_836.5(1,26)



CSE-ENDC_2A_n5-PI2-BPSK_10M_CH168800_844(1,26)



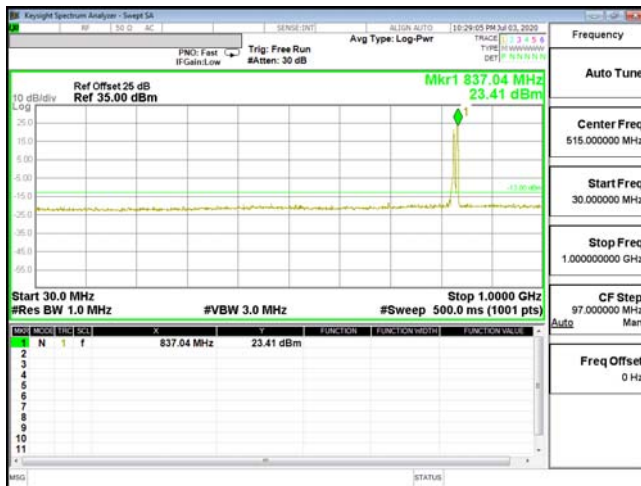
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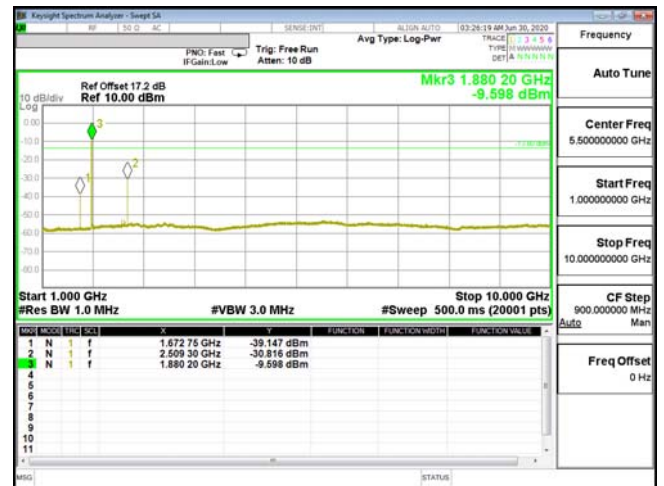
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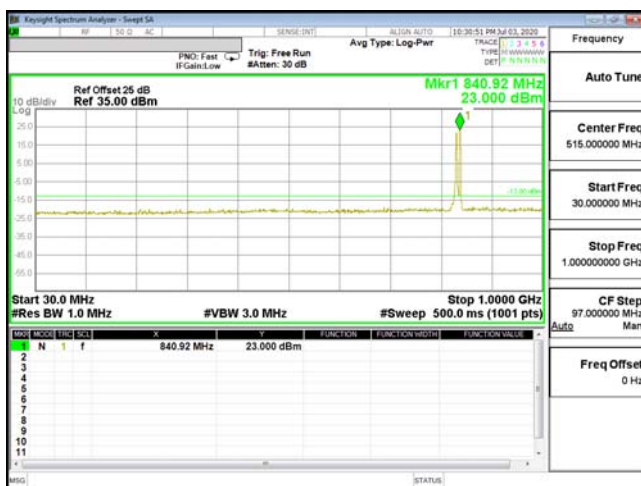
CSE-ENDC_2A_n5-PI2-BPSK_15M_CH166300_831.5(1,39)



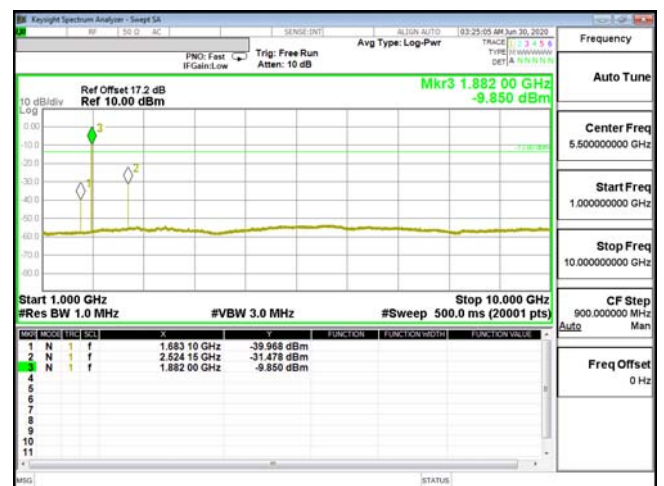
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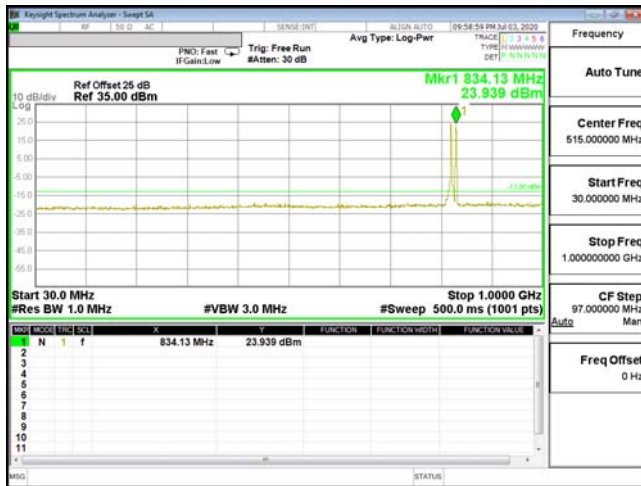
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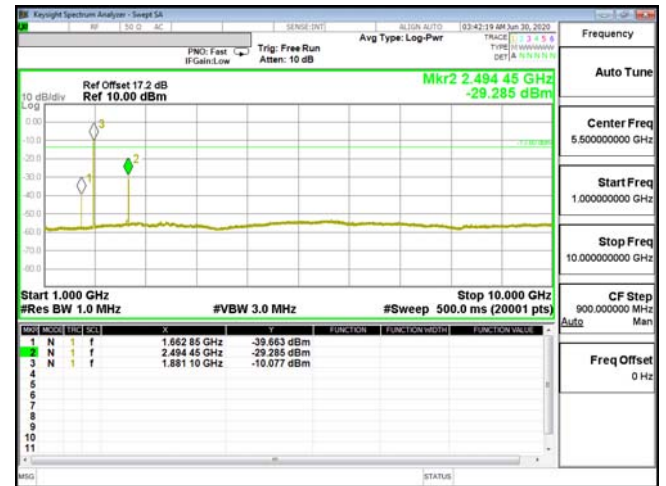
CSE-ENDC_2A_n5-PI2-BPSK_15M_CH168300_841.5(1,39)



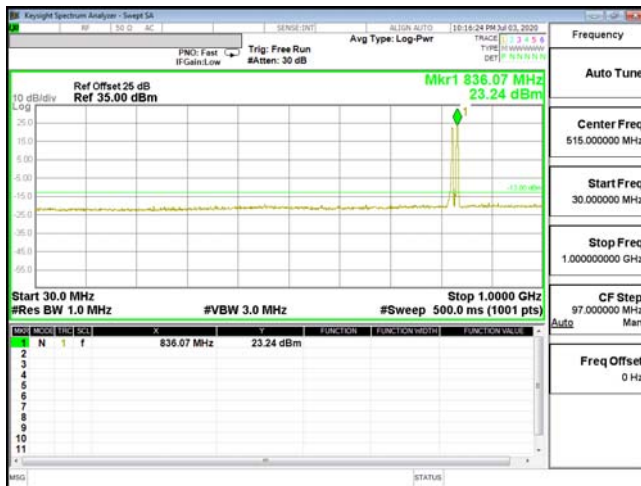
CSE-ENDC_2A_n5-PI2-BPSK_15M_CH168300_841.5(1,39)



CSE-ENDC_2A_n5-PI2-BPSK_20M_CH166800_834(1,53)



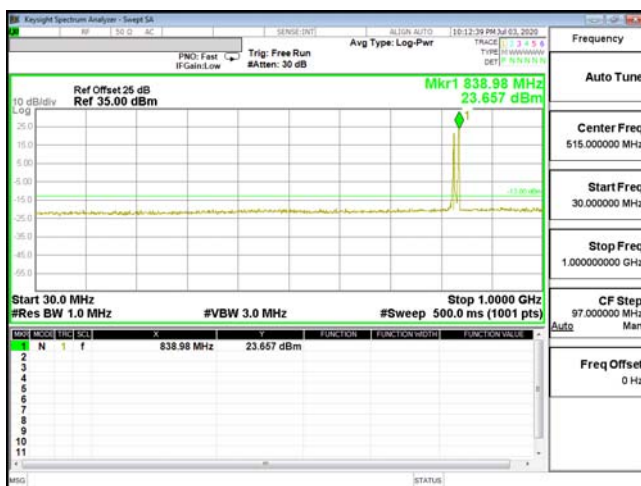
CSE-ENDC_2A_n5-PI2-BPSK_20M_CH166800_834(1,53)



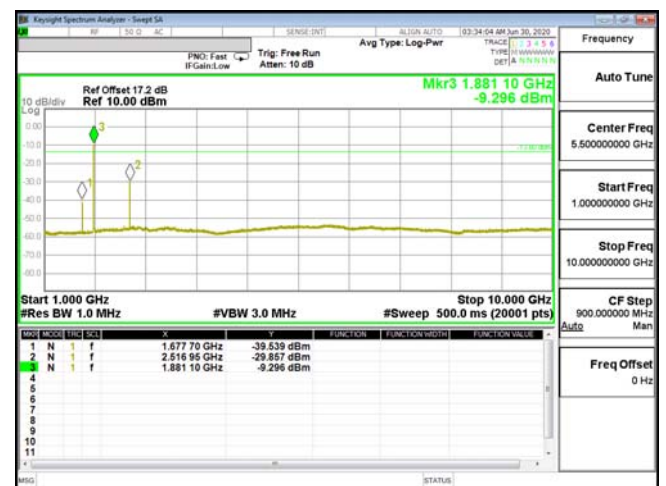
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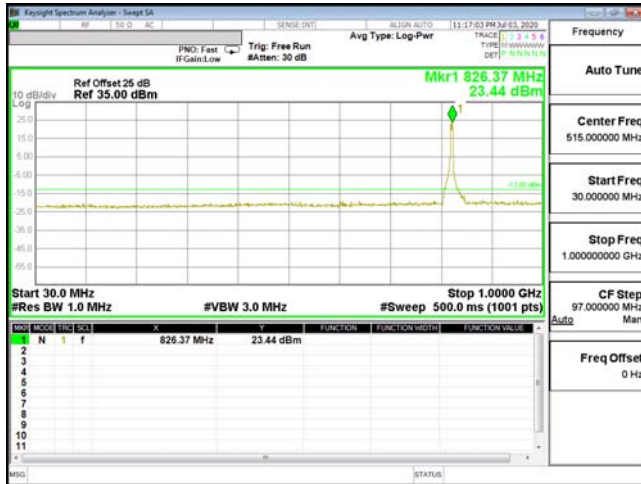
CSE-ENDC_2A_n5-PI2-BPSK_20M_CH167300_836.5(1,53)



CSE-ENDC_2A_n5-PI2-BPSK_20M_CH167800_839(1,53)



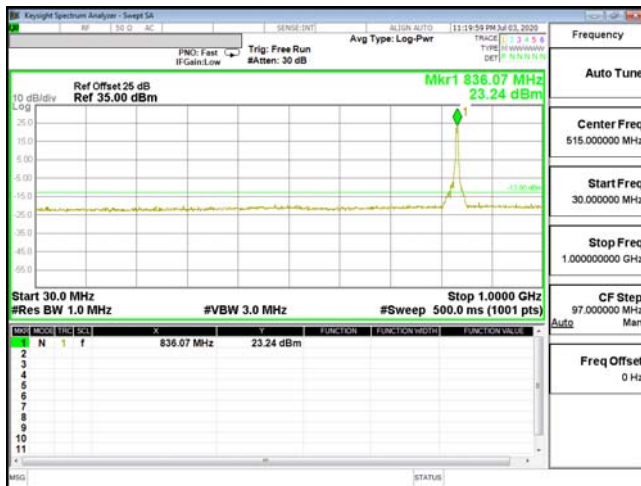
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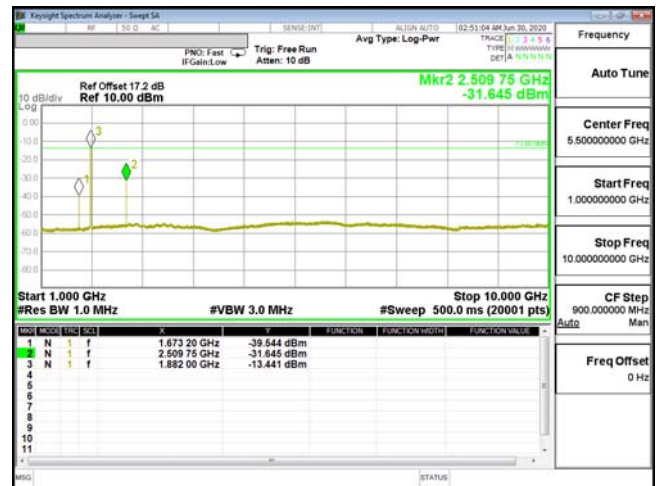
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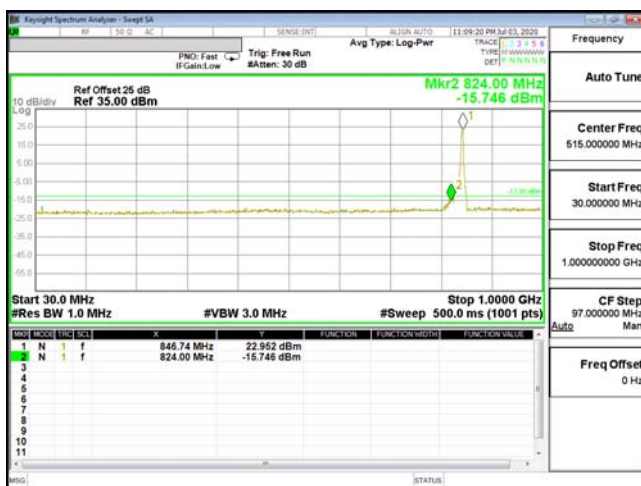
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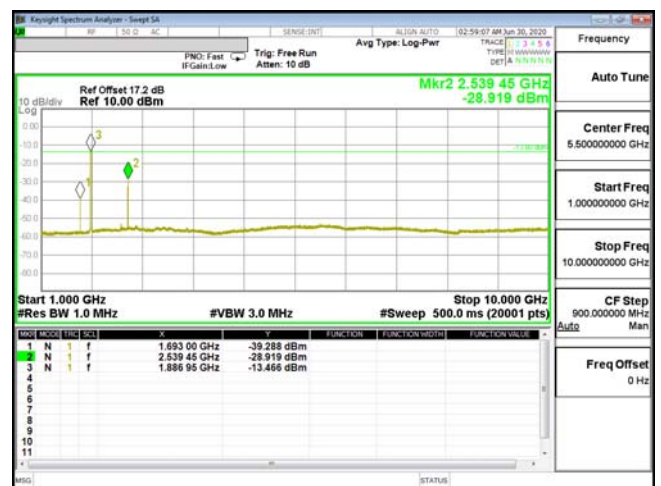
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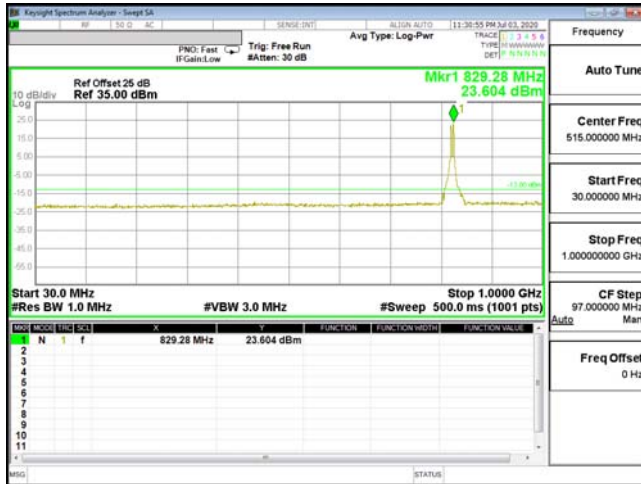
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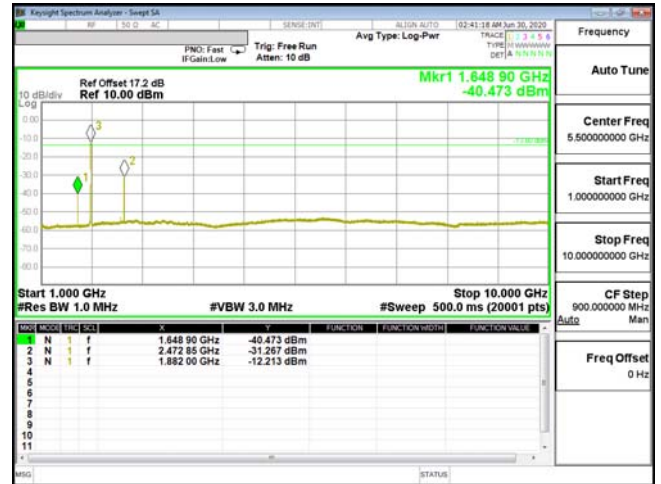
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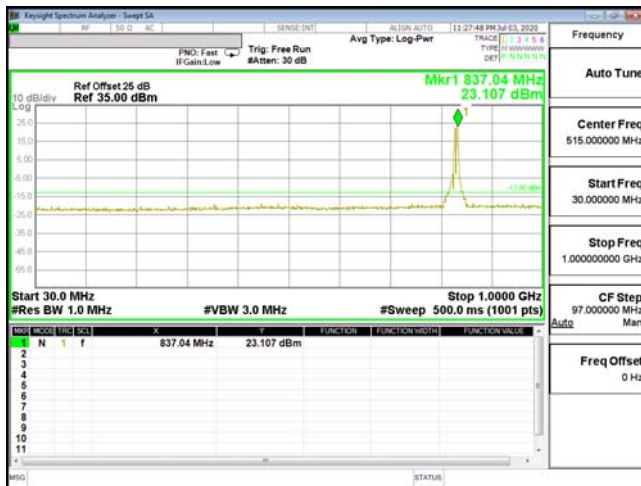
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CSE-ENDC_2A_n5-QPSK_10M_CH165800_829(1,26)



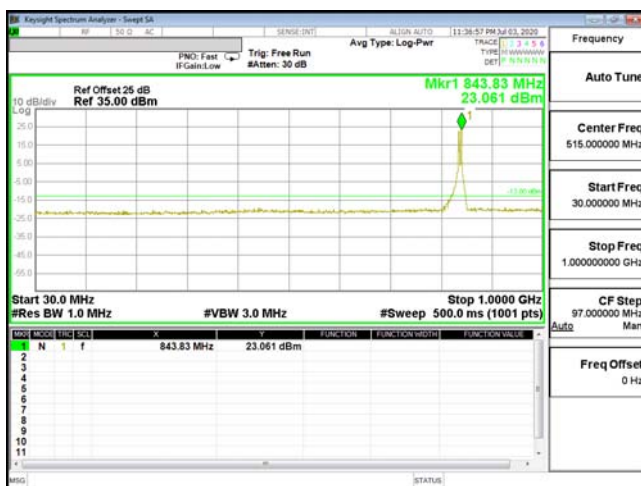
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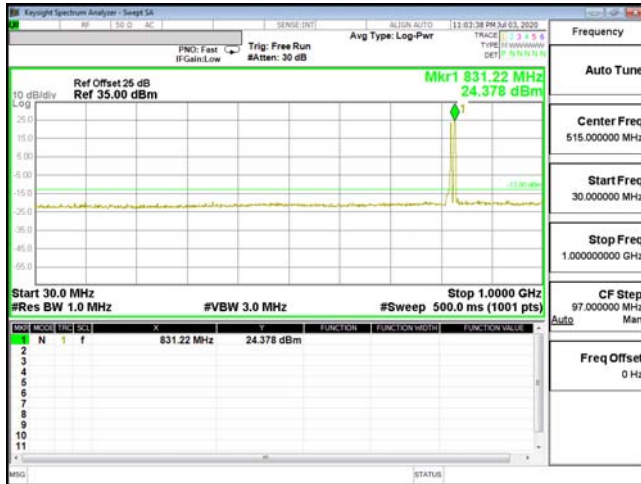
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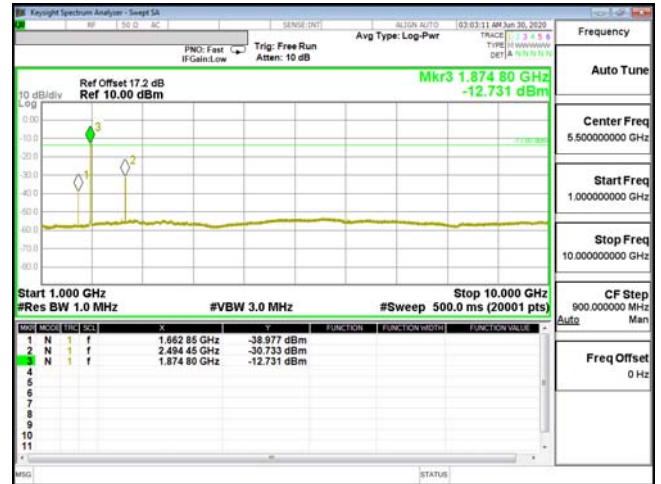
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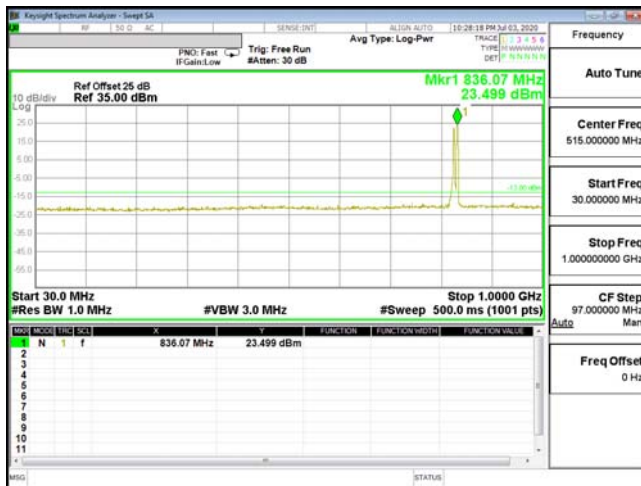
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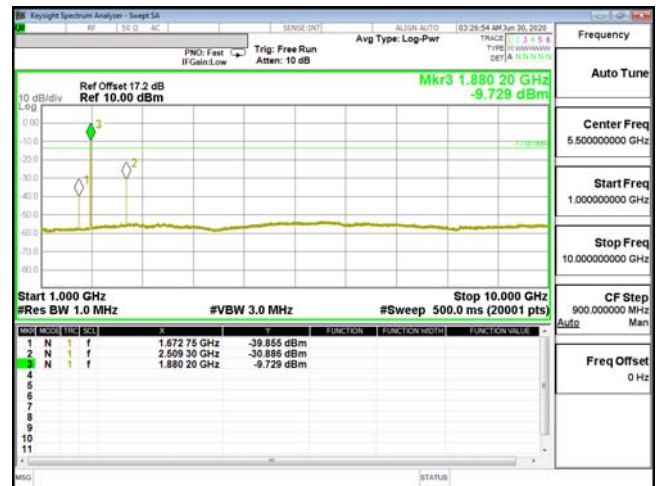
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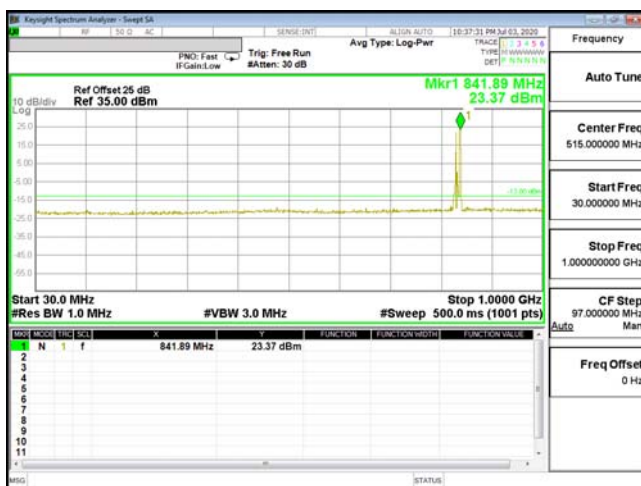
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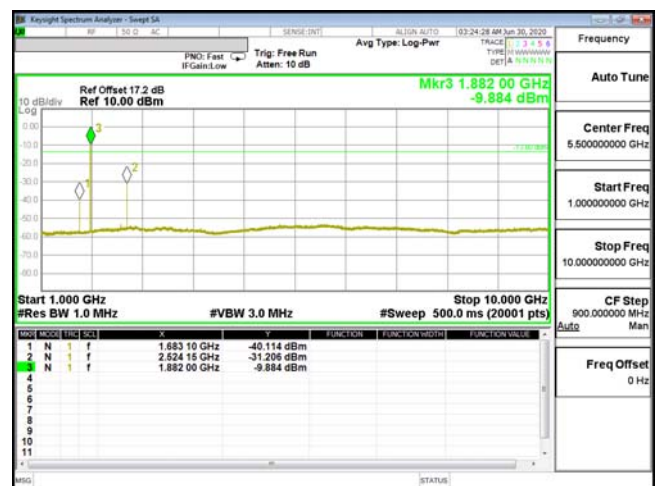
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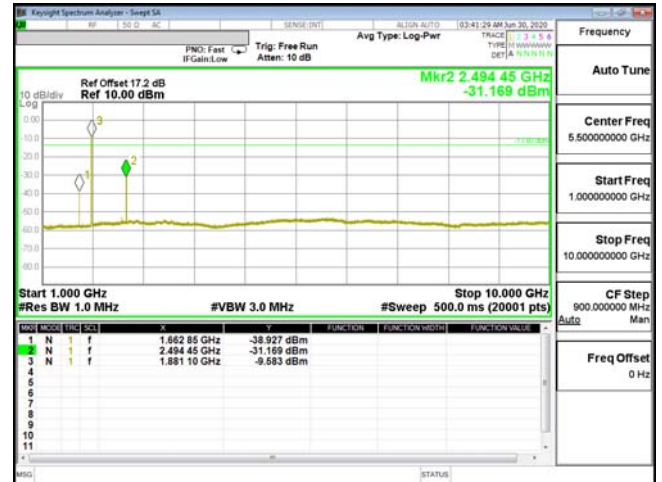
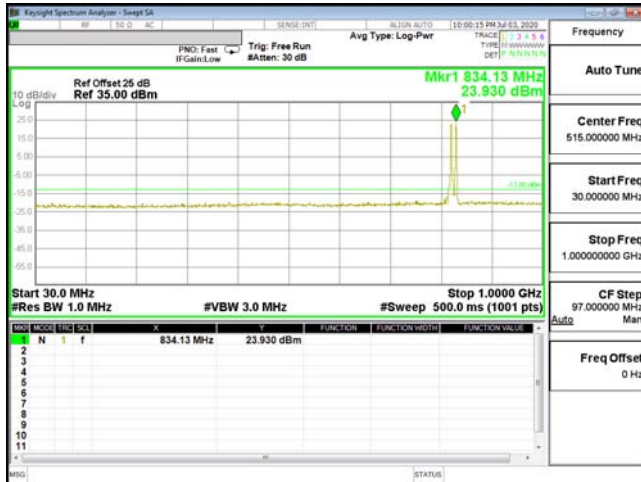
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CSE-ENDC_2A_n5-QPSK_15M_CH168300_841.5(1,39)

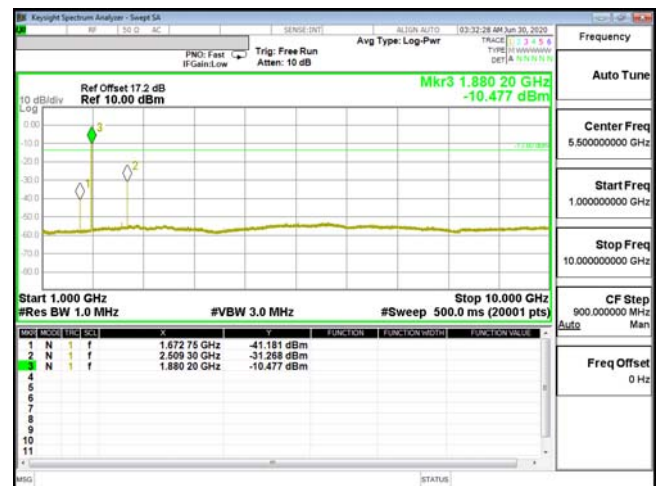
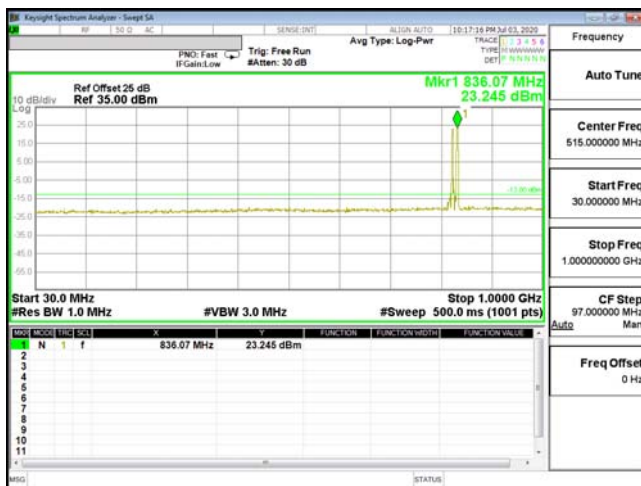


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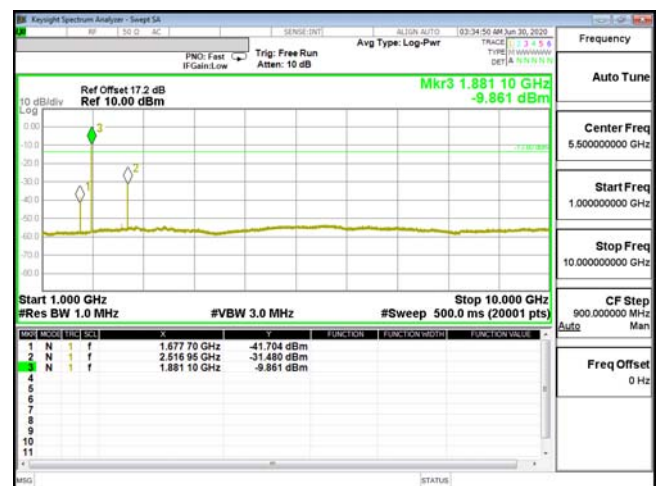
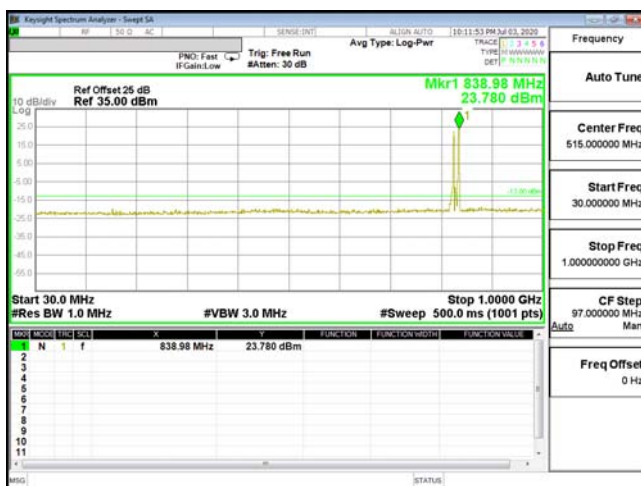
CSE-ENDC_2A_n5-QPSK_20M_CH166800_834(1,53)

CSE-ENDC_2A_n5-QPSK_20M_CH166800_834(1,53)



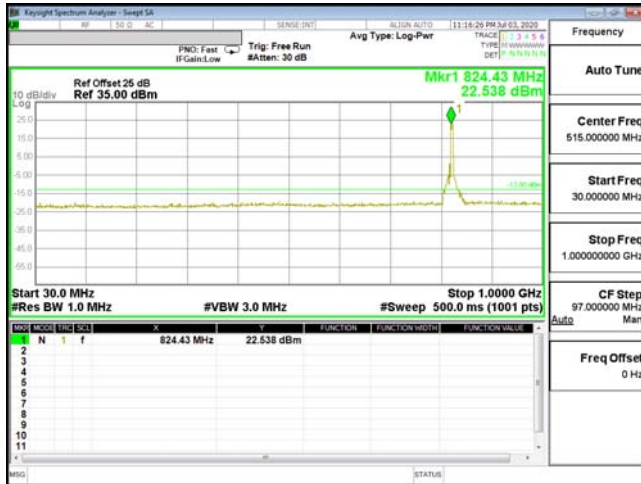
CSE-ENDC_2A_n5-QPSK_20M_CH167300_836.5(1,53)

CSE-ENDC_2A_n5-QPSK_20M_CH167300_836.5(1,53)



CSE-ENDC_2A_n5-QPSK_20M_CH167800_839(1,53)

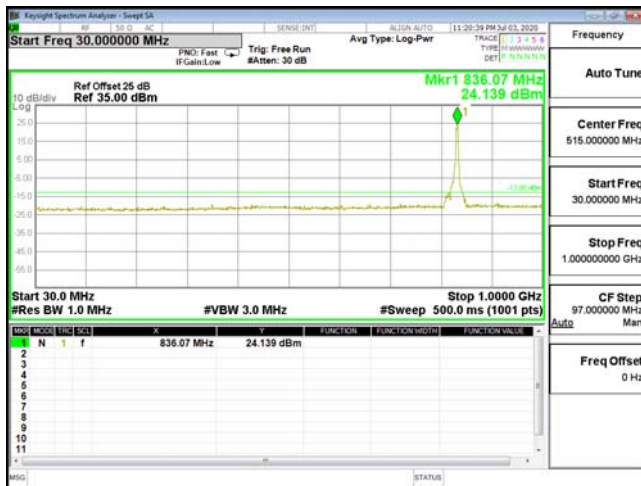
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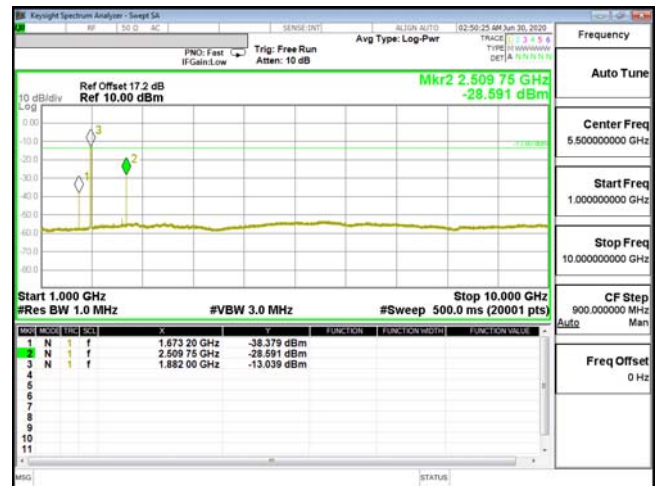
CSE-ENDC_2A_n5-16QAM_5M_CH165300_826.5(1,12)



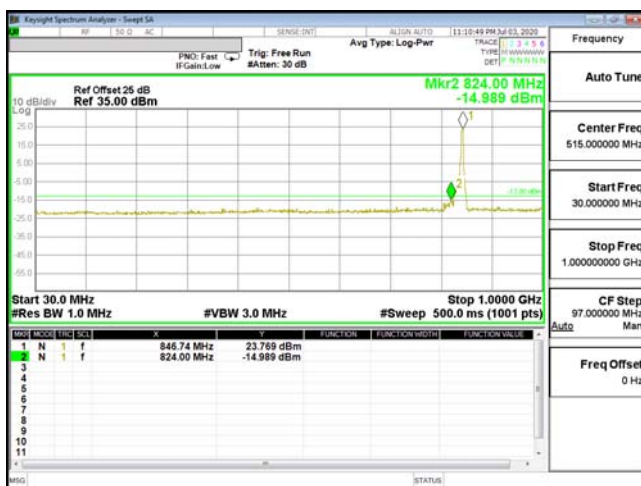
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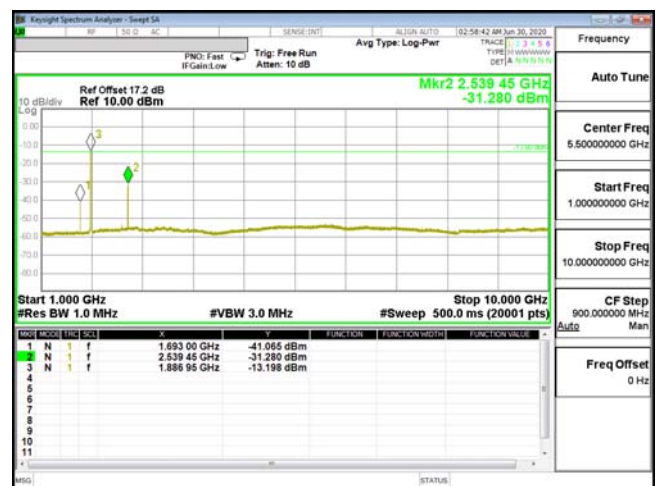
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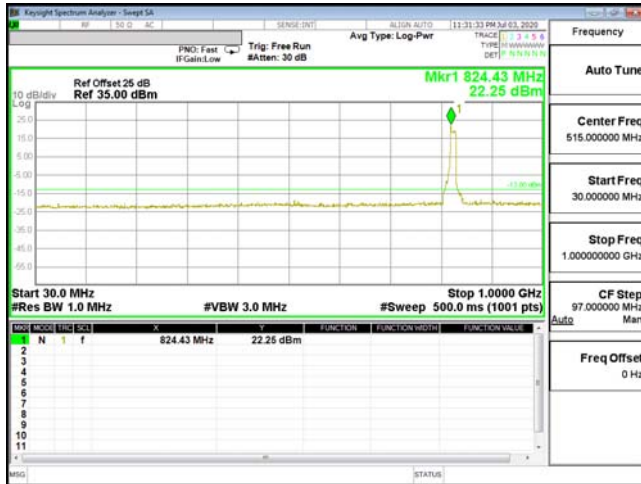
CSE-ENDC_2A_n5-16QAM_5M_CH167300_836.5(1,12)



CSE-ENDC_2A_n5-16QAM_5M_CH169300_846.5(1,12)



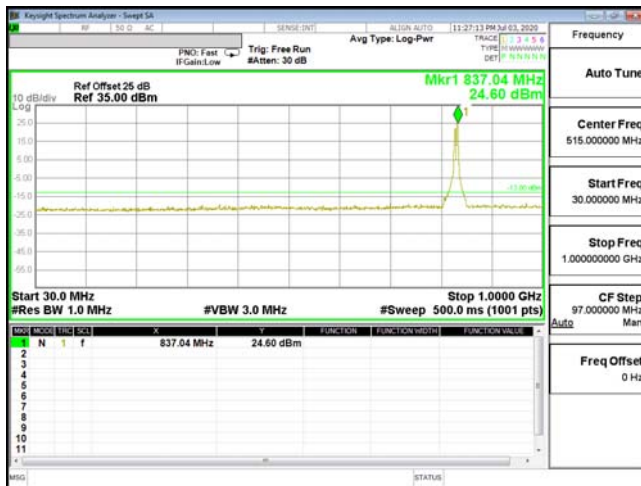
CSE-ENDC_2A_n5-16QAM_5M_CH169300_846.5(1,12)



CSE-ENDC_2A_n5-16QAM_10M_CH165800_829(50,0)



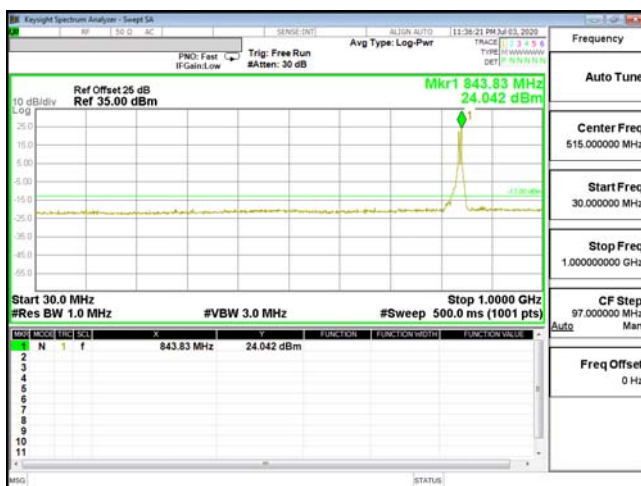
CSE-ENDC_2A_n5-16QAM_10M_CH165800_829(50,0)



CSE-ENDC_2A_n5-16QAM_10M_CH167300_836.5(1,26)



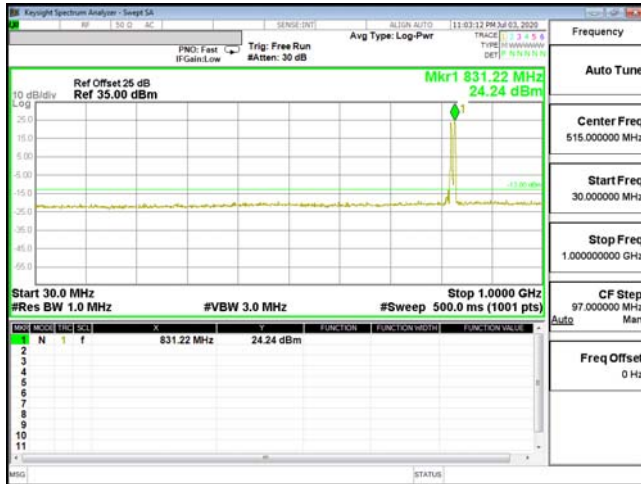
CSE-ENDC_2A_n5-16QAM_10M_CH167300_836.5(1,26)



CSE-ENDC_2A_n5-16QAM_10M_CH168800_844(1,26)



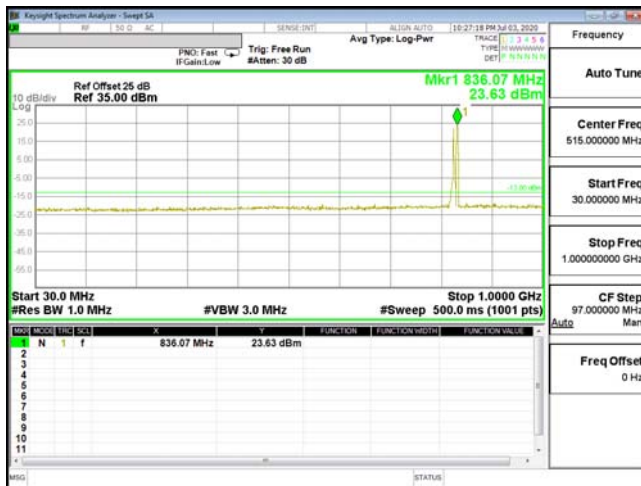
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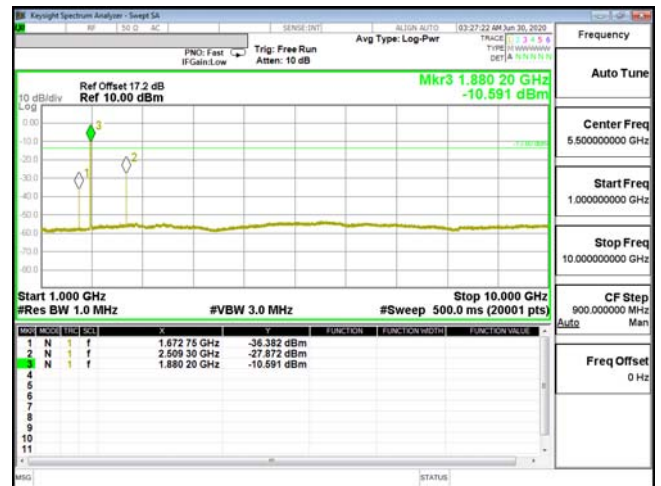
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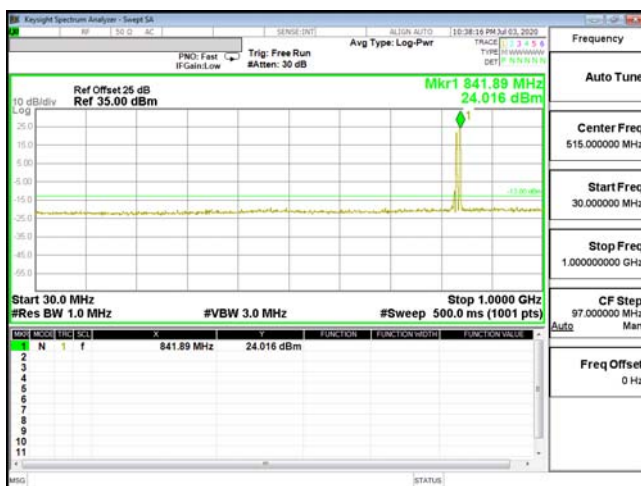
CSE-ENDC_2A_n5-16QAM_15M_CH166300_831.5(1,39)



CSE-ENDC_2A_n5-16QAM_15M_CH167300_836.5(1,39)



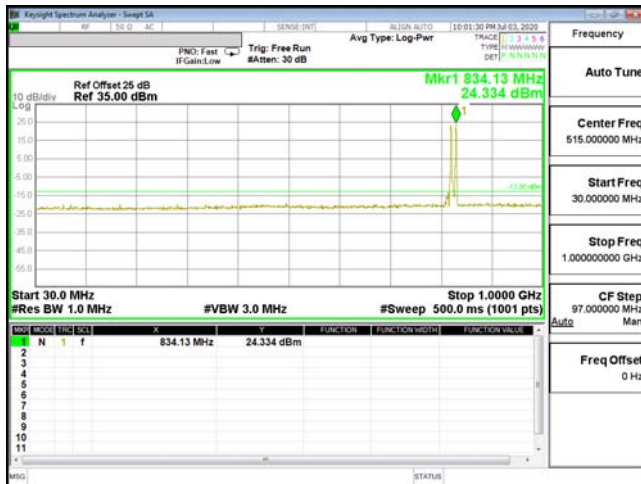
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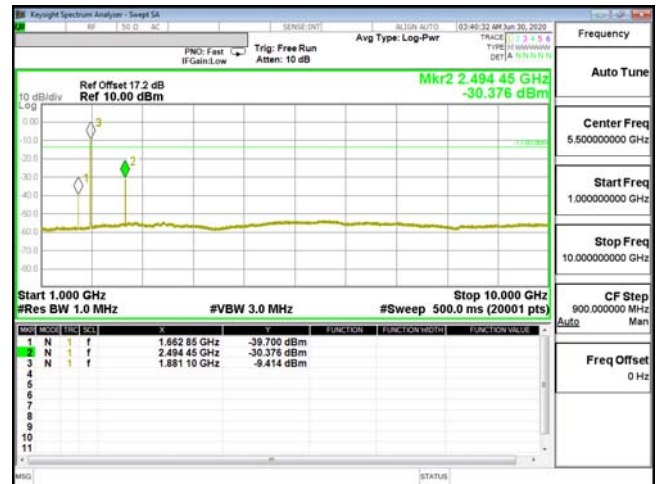
CSE-ENDC_2A_n5-16QAM_15M_CH168300_841.5(1,39)



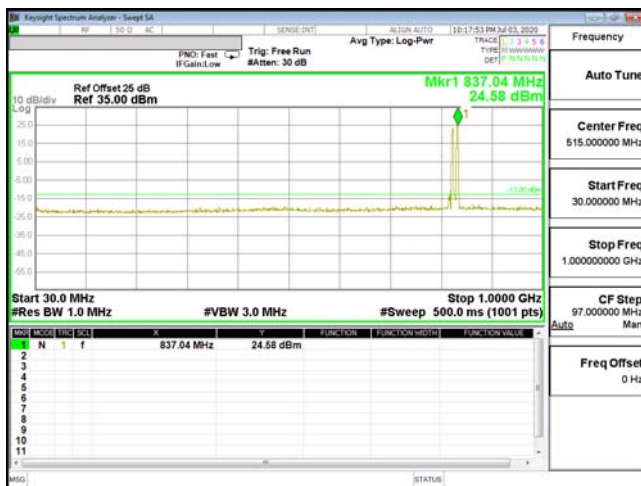
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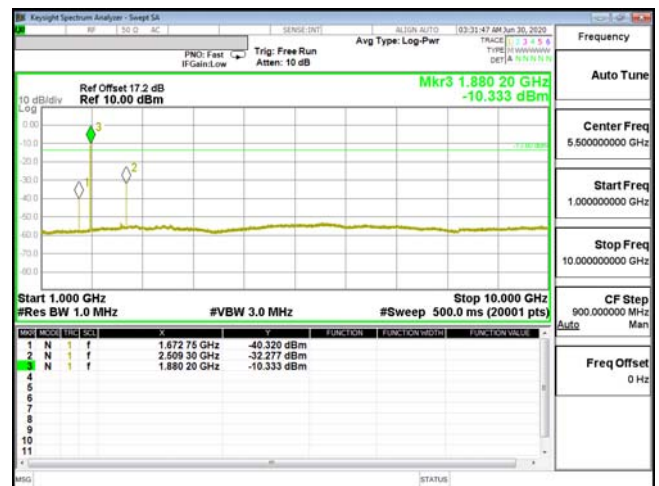
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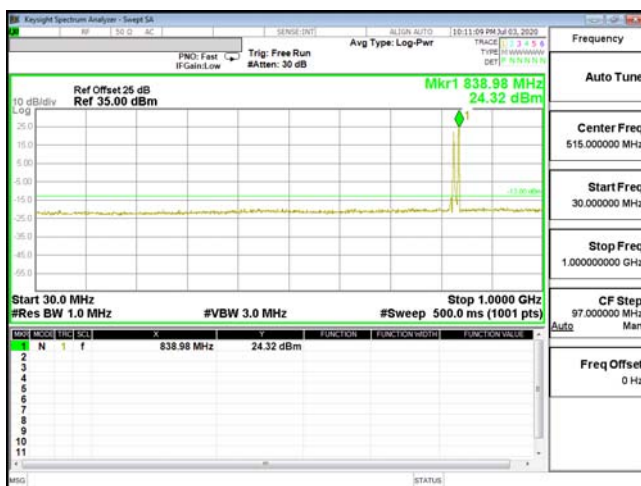
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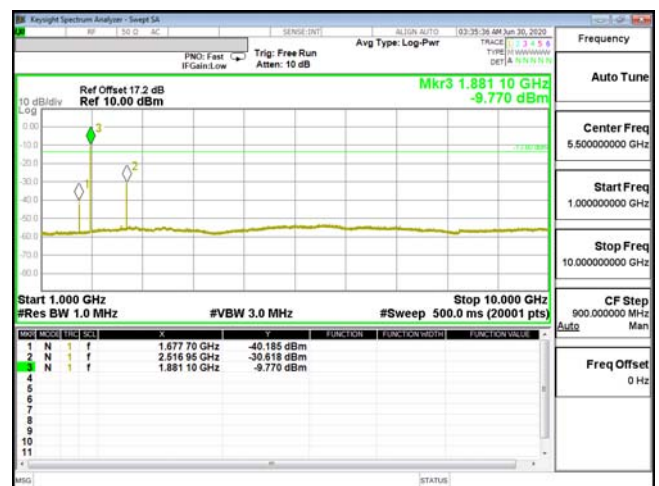
CSE-ENDC_2A_n5-16QAM_20M_CH167300_836.5(1,53)



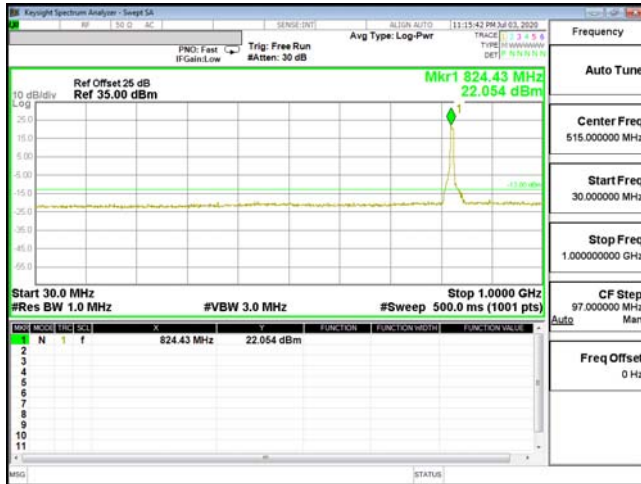
CSE-ENDC_2A_n5-16QAM_20M_CH167300_836.5(1,53)



CSE-ENDC_2A_n5-16QAM_20M_CH167800_839(1,53)



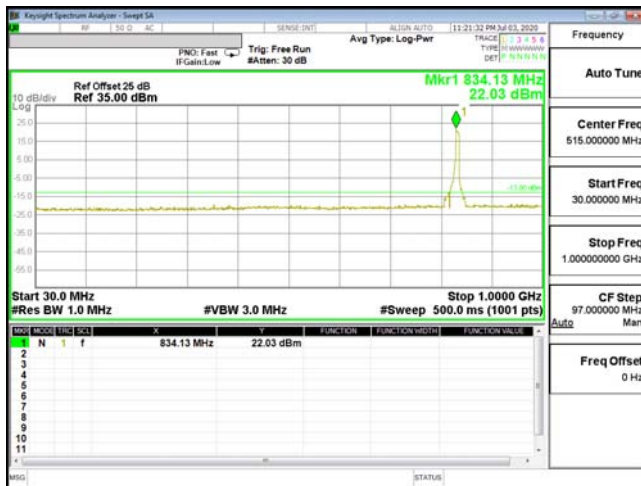
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CSE-ENDC_2A_n5-64QAM_5M_CH165300_826.5(25,0)



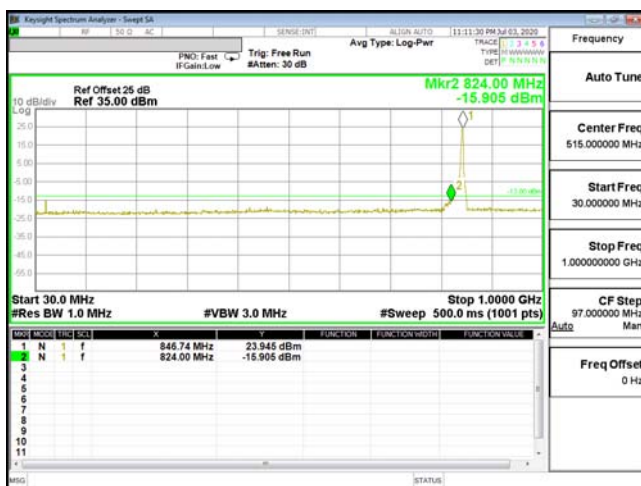
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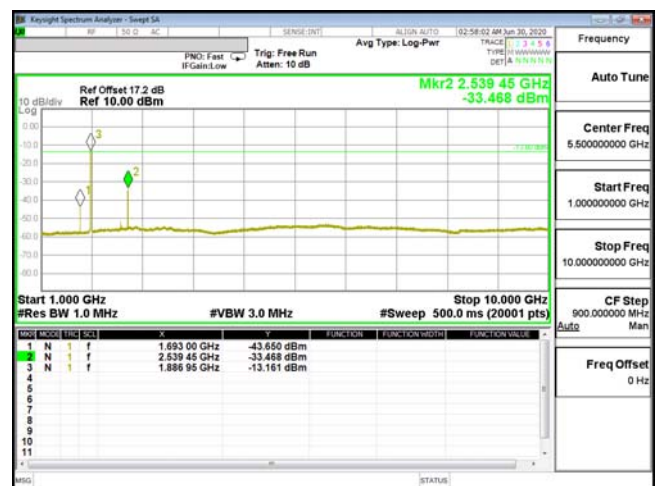
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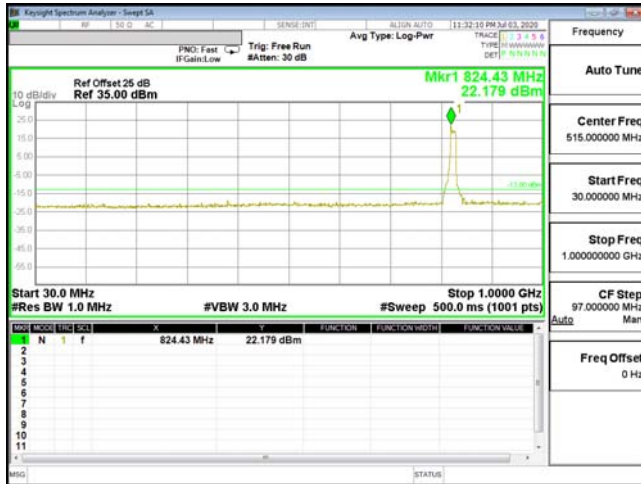
CSE-ENDC_2A_n5-64QAM_5M_CH167300_836.5(25,0)



CSE-ENDC_2A_n5-64QAM_5M_CH169300_846.5(1,12)



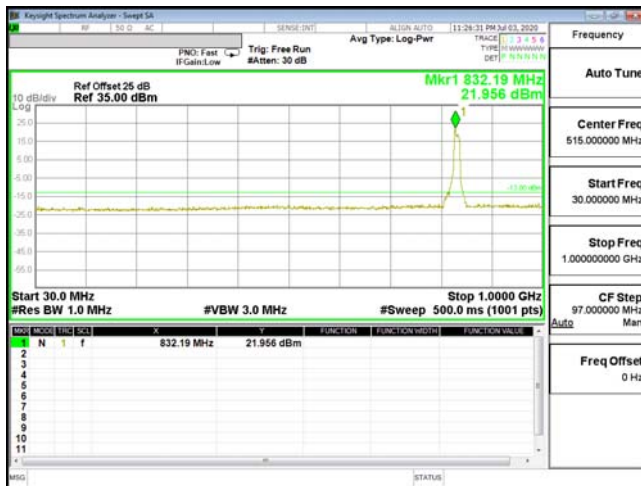
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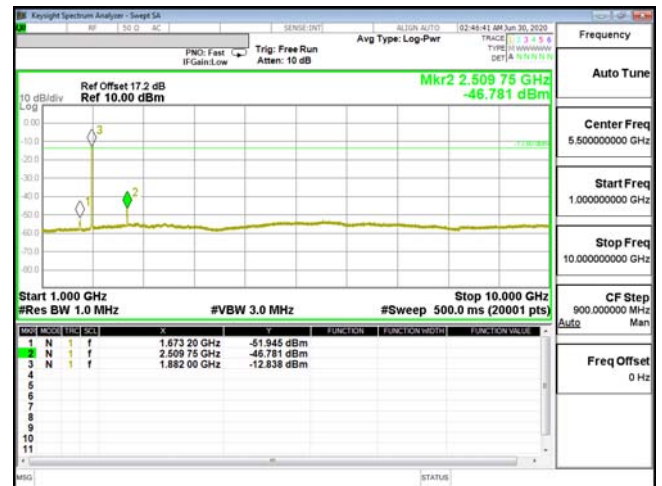
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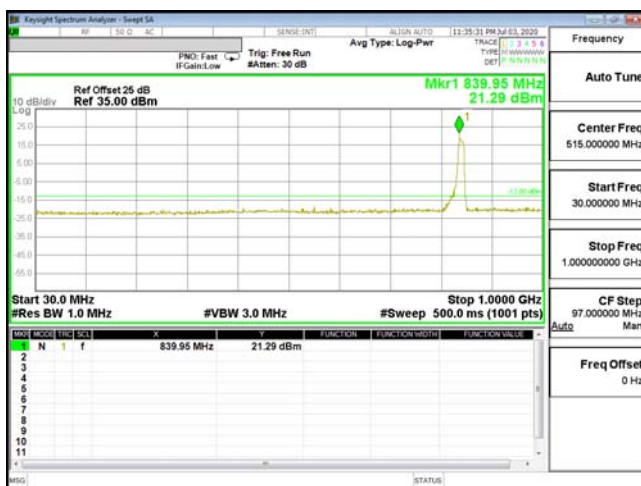
CSE-ENDC_2A_n5-64QAM_10M_CH165800_829(50,2)



CSE-ENDC_2A_n5-64QAM_10M_CH167300_836.5(50,0)



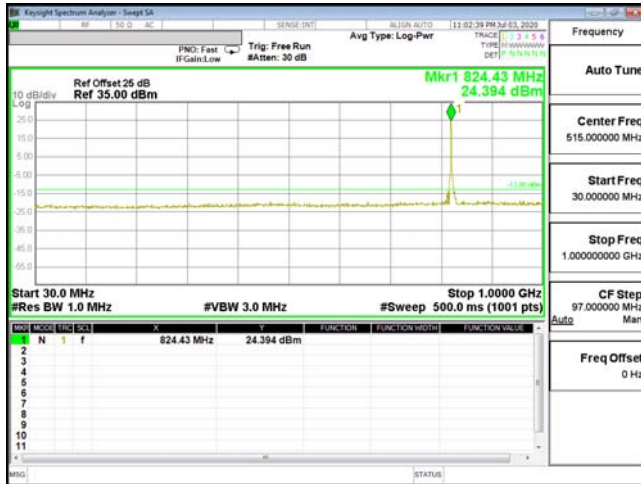
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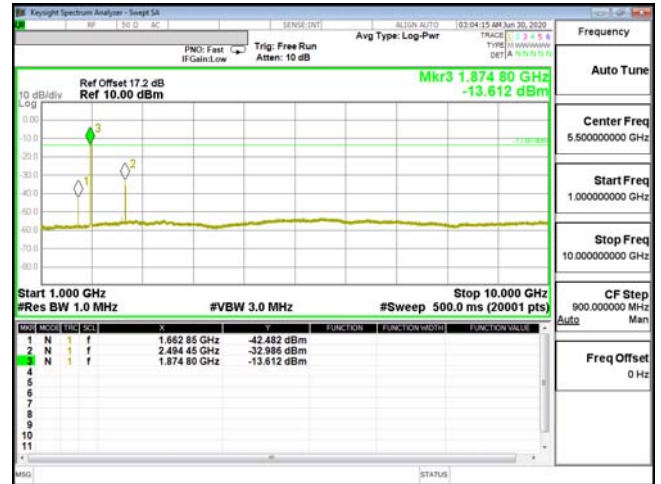
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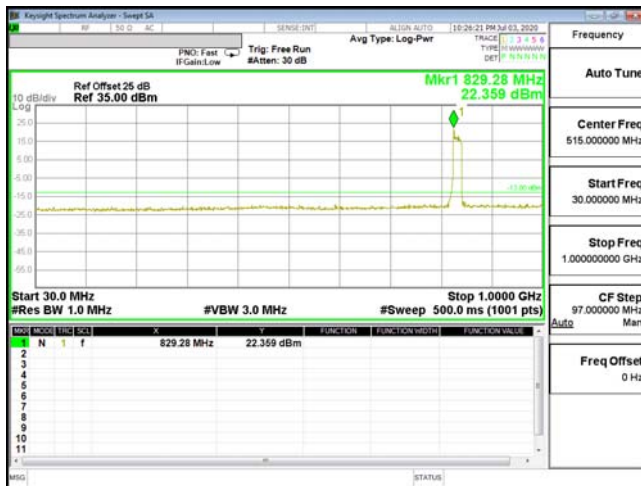
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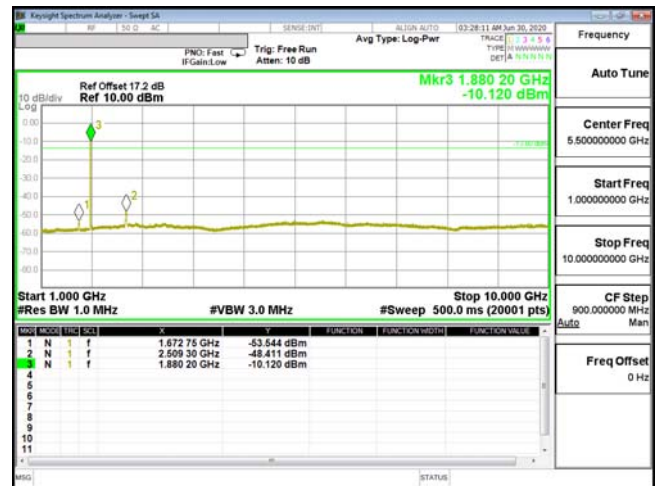
CSE-ENDC_2A_n5-64QAM_15M_CH166300_831.5(1,0)



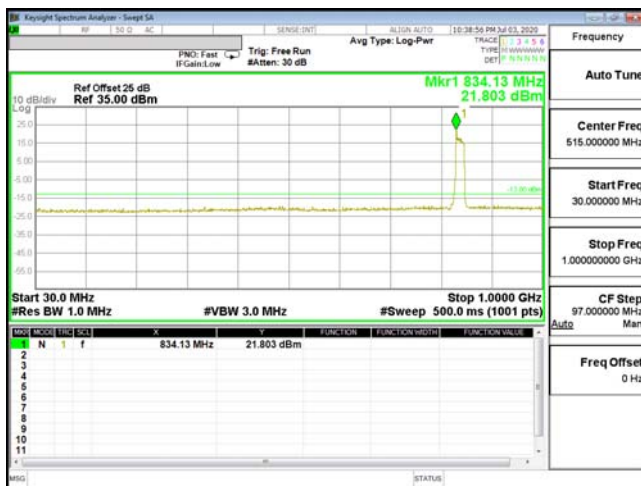
CSE-ENDC_2A_n5-64QAM_15M_CH166300_831.5(1,0)



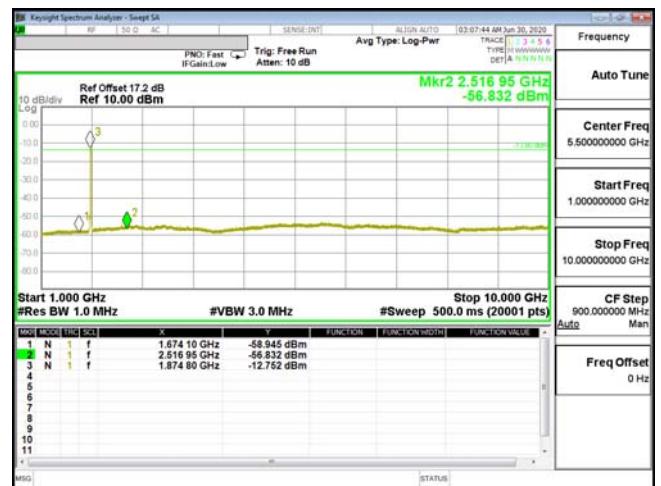
CSE-ENDC_2A_n5-64QAM_15M_CH167300_836.5(75,4)



CSE-ENDC_2A_n5-64QAM_15M_CH167300_836.5(75,4)



CSE-ENDC_2A_n5-64QAM_15M_CH168300_841.5(75,4)



CSE-ENDC_2A_n5-64QAM_15M_CH168300_841.5(75,4)