EXHIBIT 5 BLOCK DIAGRAM

(FOLLOWS THIS PAGE)

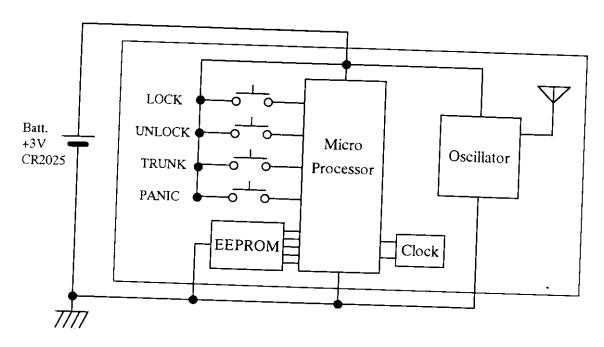
BLOCK DIAGRAM FCC ID: NHVWBU43

EXHIBIT 5

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- 3. Block Diagram.
 - 1) Transmitter



FCC ID: NHVWBU43

EXHIBIT 6

CIRCUIT DESCRIPTION

Encoder IC1 provides a series of pulses to pulse modulate gain and frequency stabilized oscillator TR1.

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EXHIBIT 6