FCC ID: NF260827

Duty Cycle Correction During 100msec:

Each function key sends a different series of characters, but each packet period (32.5 never exceeds a series of 6 long (1.5msec) and 10 short (500ì sec) pulses. Assuming any conbination of short and long pulses may be obtined due encoding the worse case transmit duty cycle would be considered 6x1.5msec+10x500ì sec per 32.5msec = 37.8% duty cycle. Figure A through C show the 43% characterestics of the pulses train for one of these function.

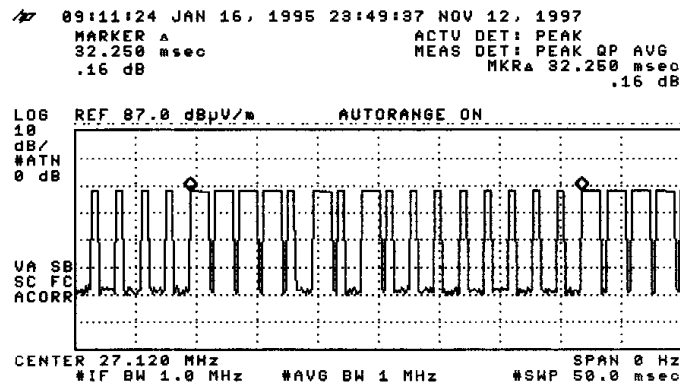Duty Cycle Correction = 20 log (0.43) = -7.3dB.
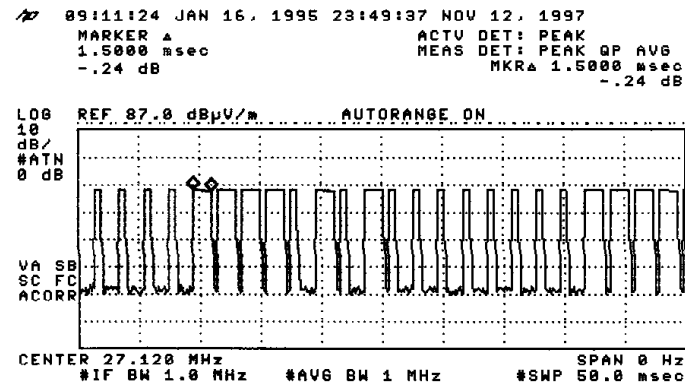
FIGURE A: PULSE TRAIN

FIGURE B: LONG PULSE

FIGURE C: SHORT TRAIN