

Circuit Description

The 2.4GHz Wireless digital Receiver is a low cost and high performance digital wireless camera applications which integrates LQFP-216 codec and controller, which mainly to Encoder with base band processor ,and the using oscillator is 12MHz , 27MHz and 32.768KHz .The portable digital audio solutions SDRAM, the procedure rate is 64M/128M/256M.And the integrates IC U3, which controlled by the 2.4GHz wireless module MCU. when the MCU sent the signal to the integrates Control codec, the SDRAM and the RF module with Power Amplifier.When pressing the key board, the signal will deliver to the GPIO control devicer, and through the GPIO to the integrates M-JPEG codec. The integrates WMB731 codec, the osc is 16MHz, will deal with all the signal and control the command to the other parts of the MCU or module, and sent the final signal to the RF Module with the power Amplifier, and sent the final signal to the Antenna .