

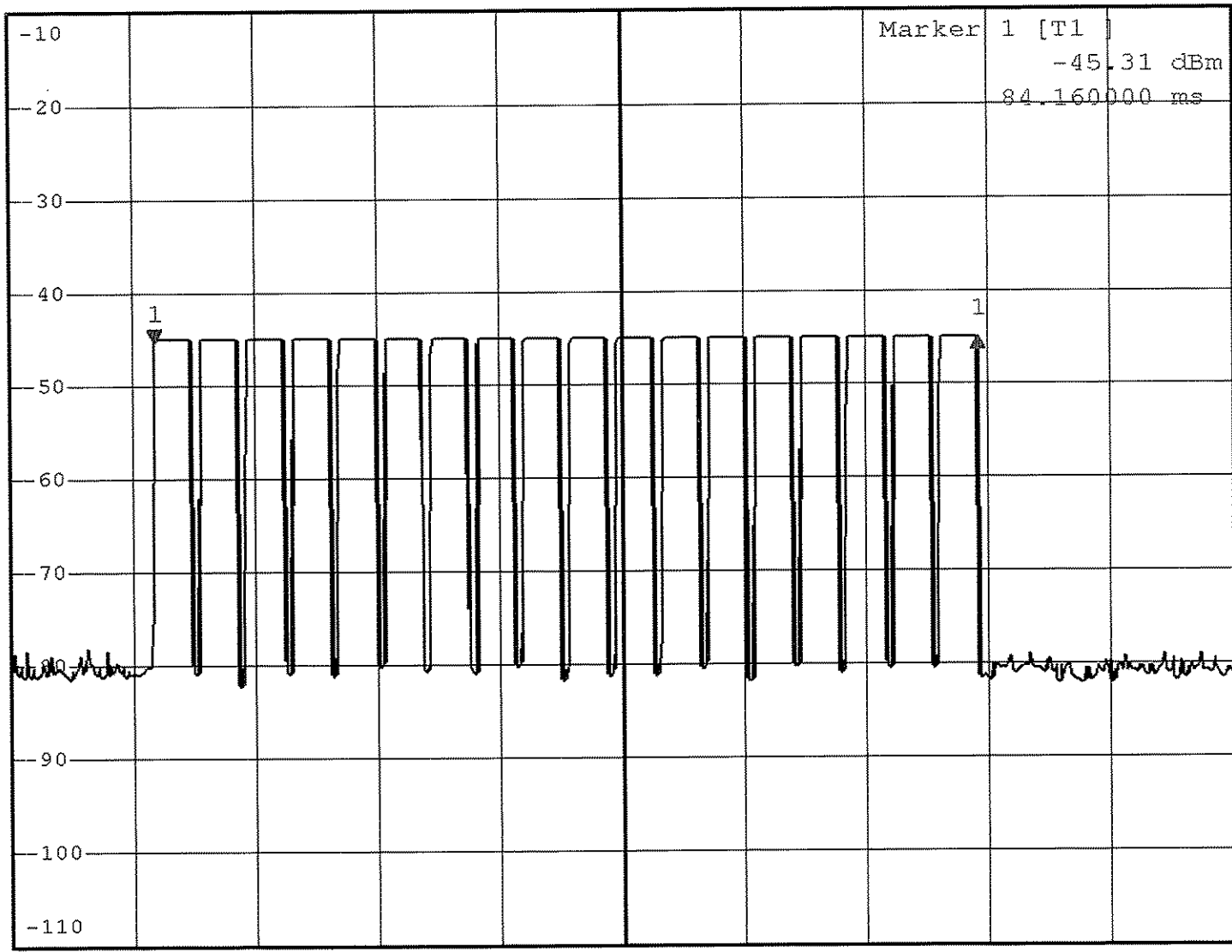


RBW 3 MHz Delta 1 [T1]
 *VBW 3 MHz 0.18 dB
 SWT 120 ms 80.880000 ms

0716642

Ref -10 dBm *Att 0 dB

1 PK *
 CLRWR



SGL

Duty Cycle

$$= \frac{18(3.88)}{100}$$

$$= \frac{69.84}{100}$$

$$= 0.698$$

$$\approx 0.7 //$$

Average Factor

$$= 20 \log 0.7$$

$$= -3.1 \text{ dB} //$$

Center 318.073 MHz

12 ms/



RBW 3 MHz

*VBW 3 MHz

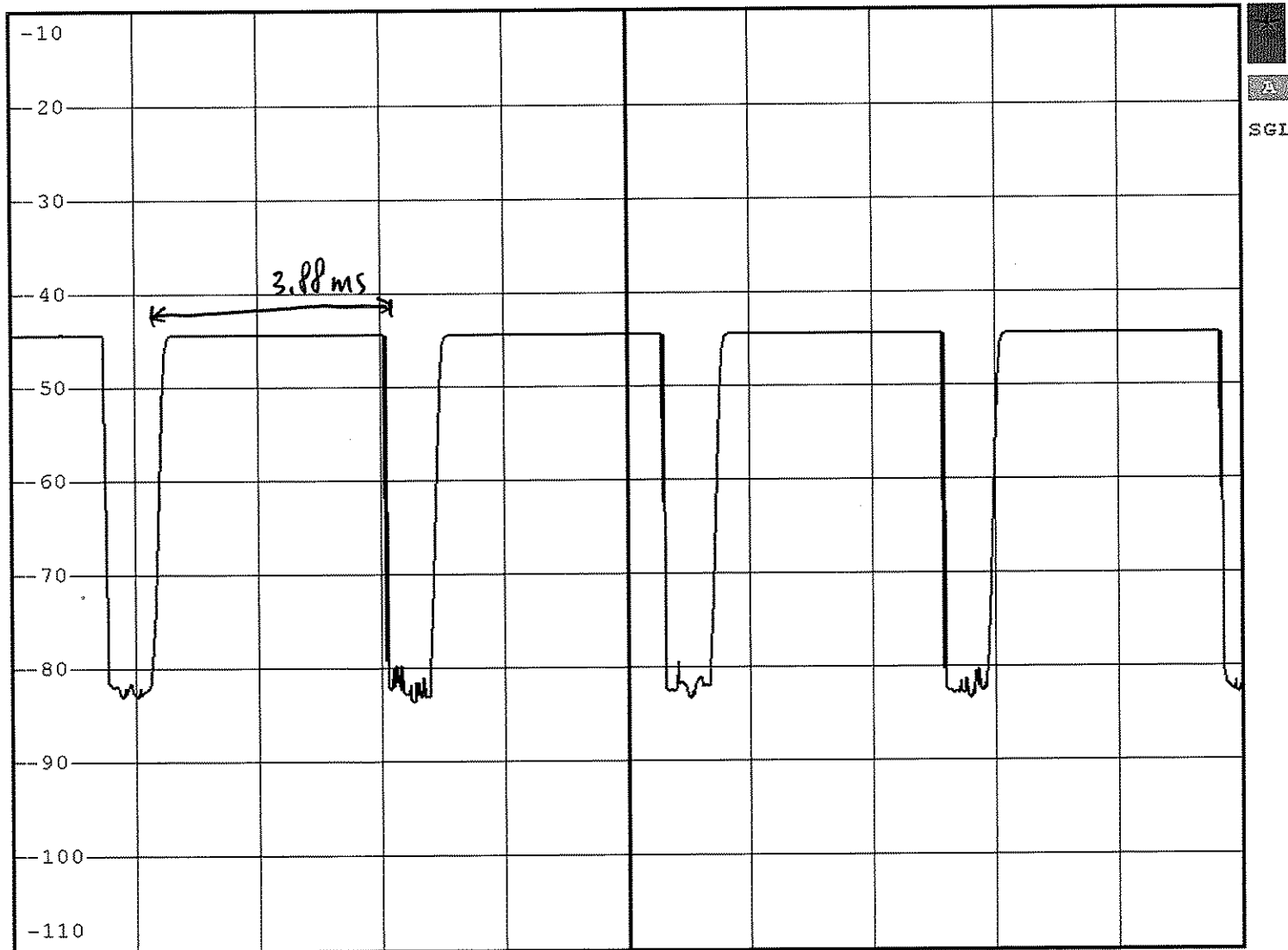
SWT 20 ms

0716642

Ref -10 dBm

*Att 0 dB

1 PK*
CLRWR



Center 318.073 MHz

2 ms/



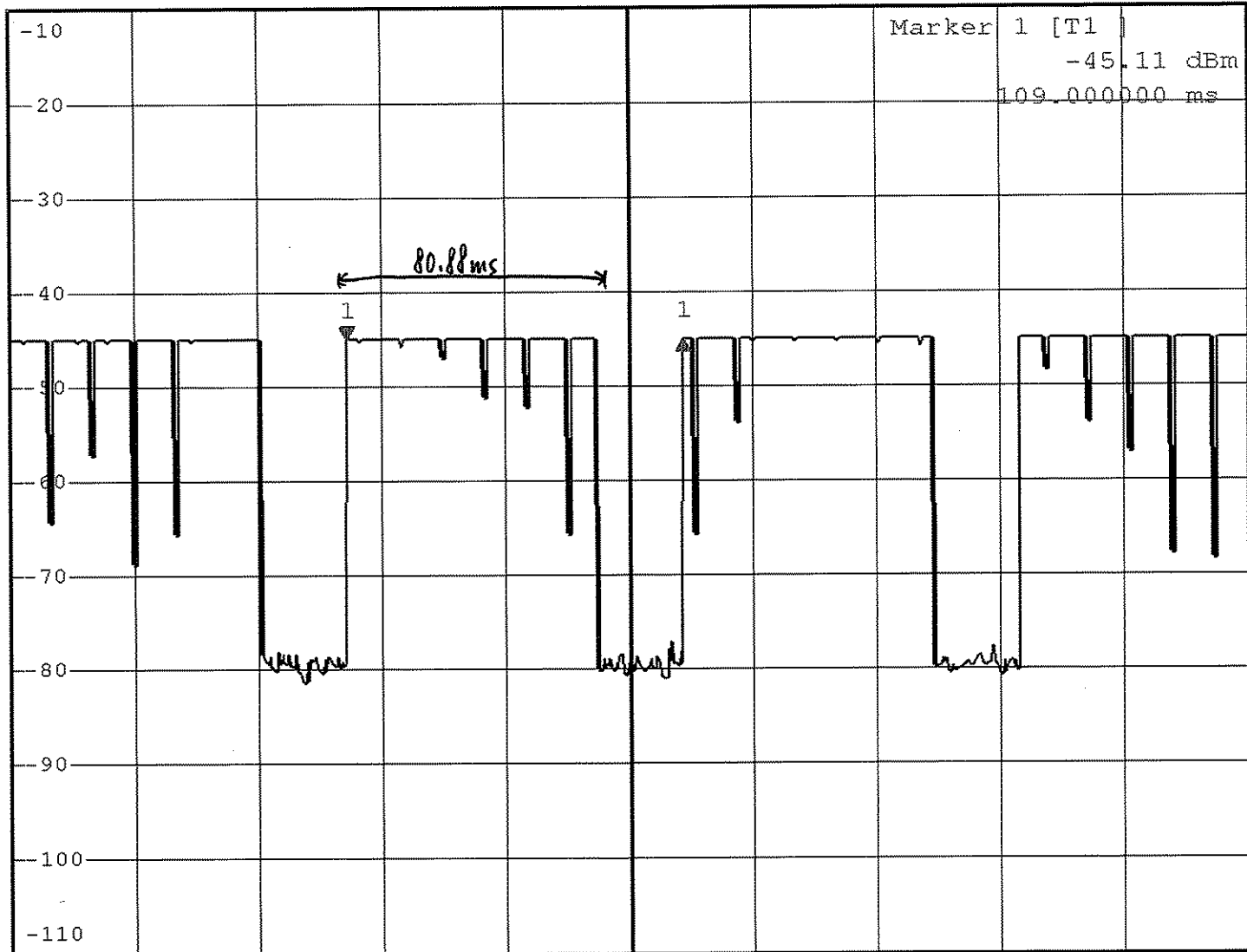
RBW 3 MHz Delta 1 [T1]
*VBW 3 MHz 0.01 dB
SWT 400 ms 109.000000 ms

0716642

Ref -10 dBm

*Att 0 dB

1 PK *
CLRWR



Center 318.073 MHz

40 ms/



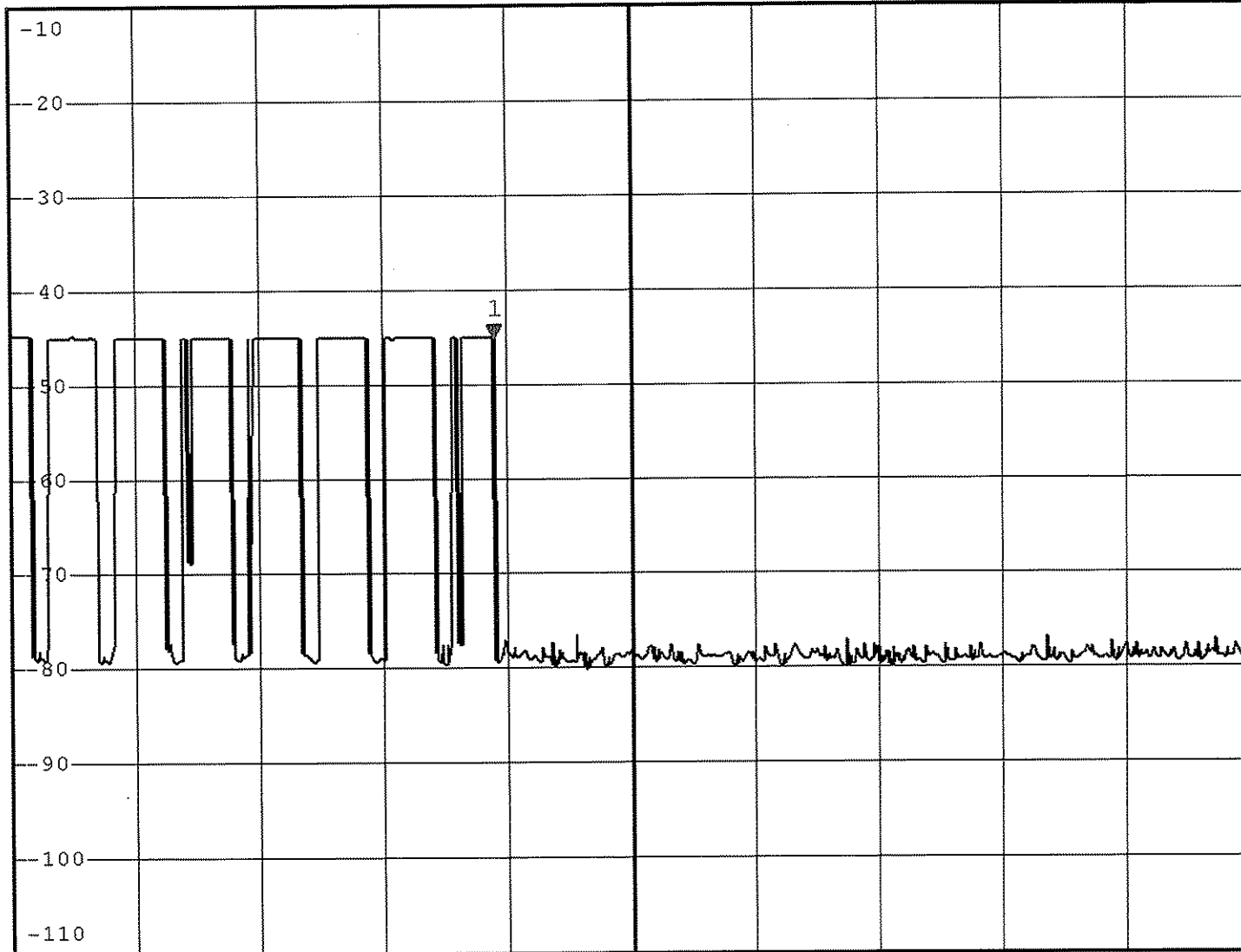
RBW 3 MHz Marker 1 [T1]
*VBW 3 MHz -45.02 dBm
SWT 2 s 778.000000 ms

0716642

Ref -10 dBm

*Att 0 dB

1 PK *
VIEW

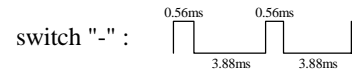
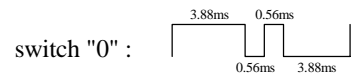
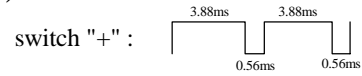


Center 318.073 MHz

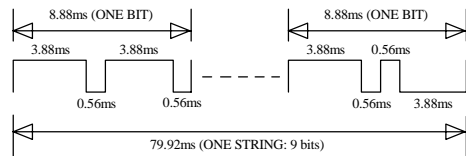
200 ms/

1. DATA for 318MHz:

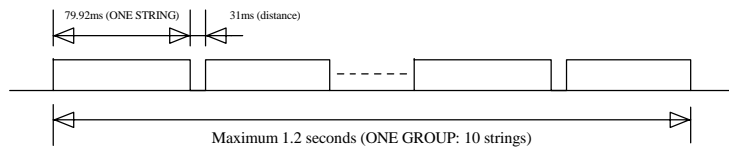
(1) ONE BIT:



(2) ONE STRING:

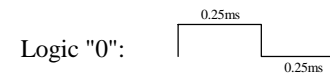
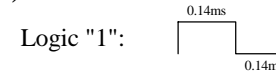


(3) ONE GROUP:

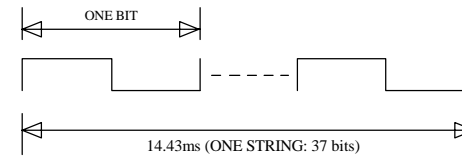


2. DATA for 900MHz:

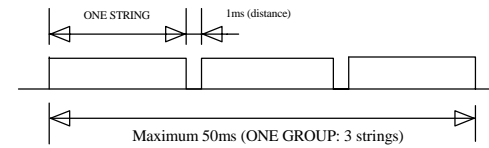
(1) ONE BIT:



(2) ONE STRING:



(3) ONE GROUP:



DOC NAME:	Data Diagram	SMART ELECTRONIC INDUSTRIAL(DONG GUAN)CO.,			
DESIGN:		MODEL:	RF1257O	DOC NO:	
CHECKED BY:		NAME:	Data Diagram	REV:	A
PERMITTED BY:		DATE:		PAGE	1 of 1