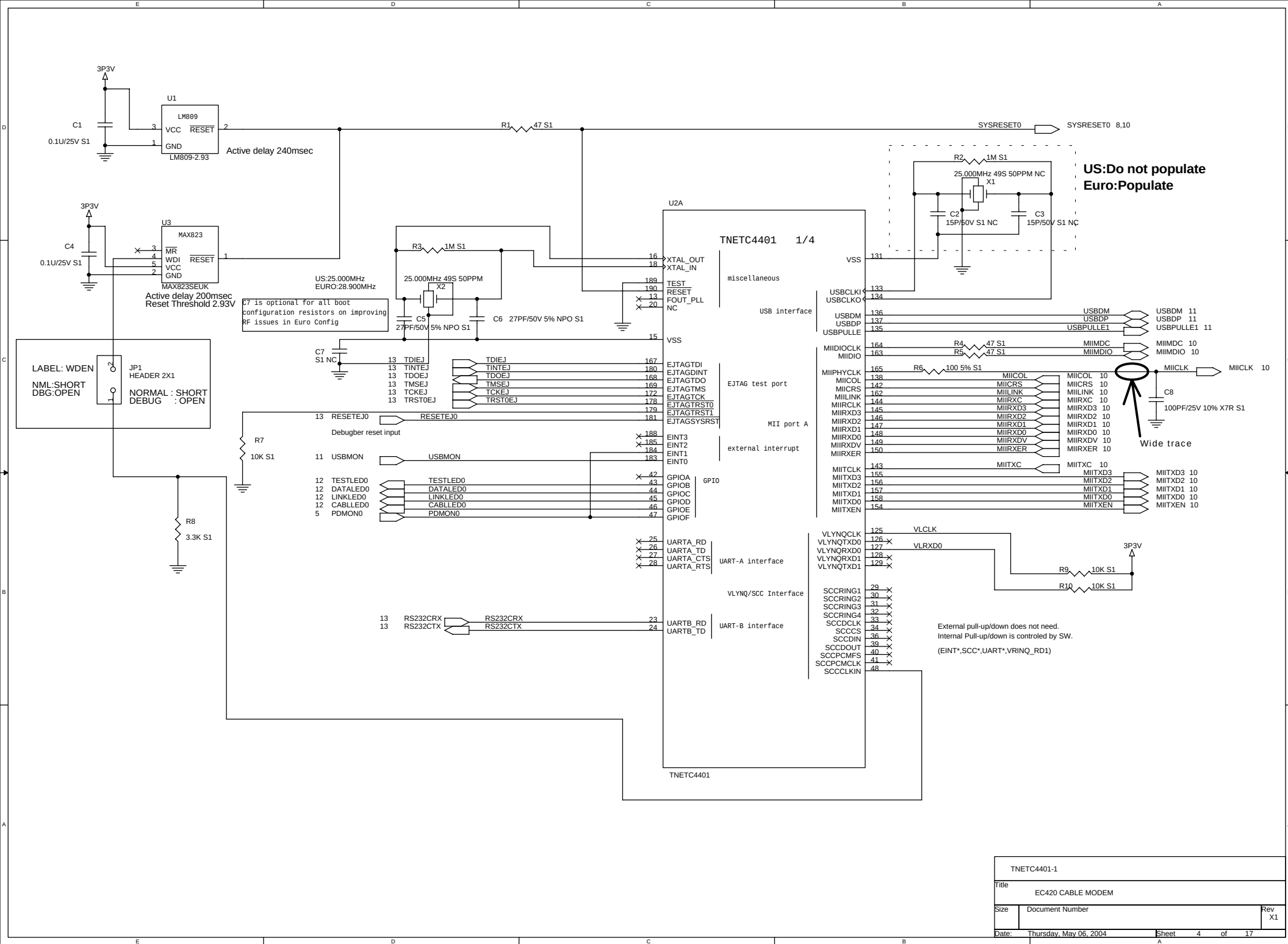
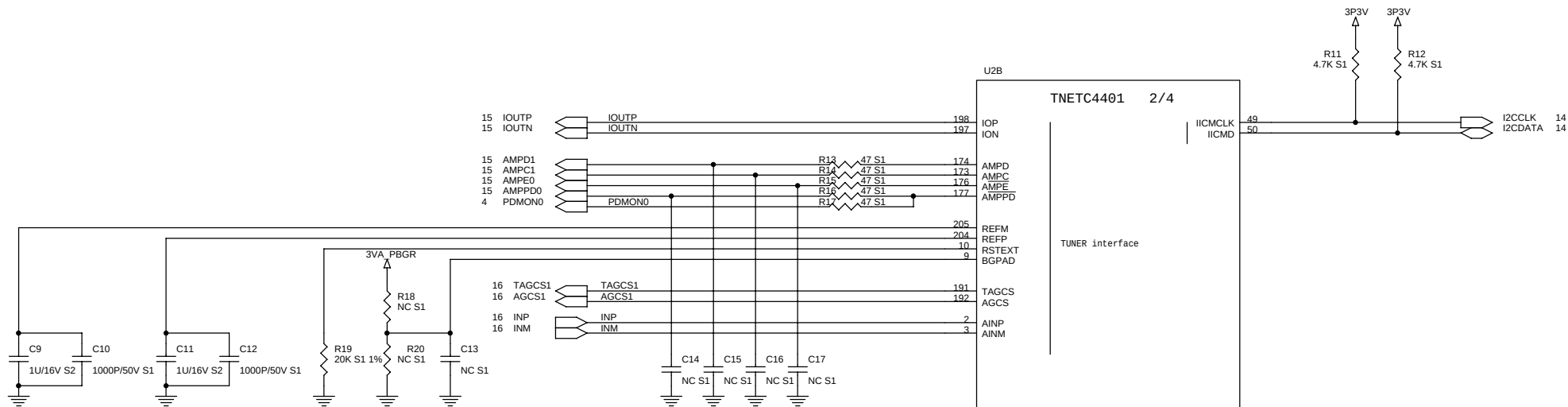


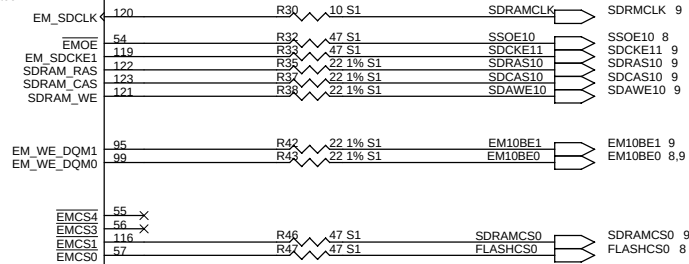
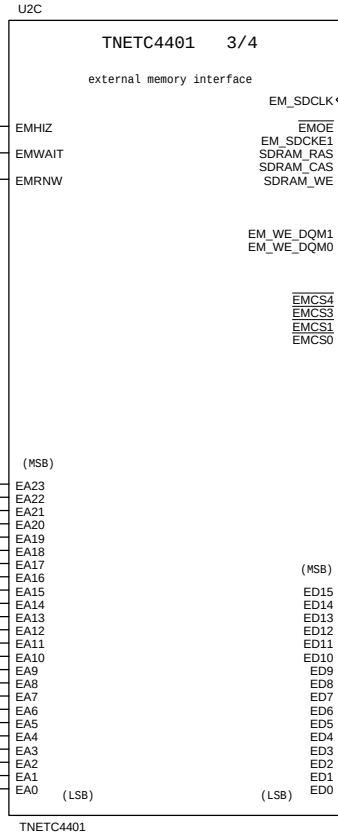
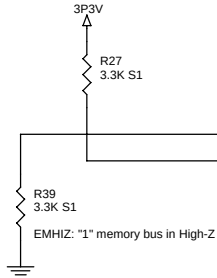
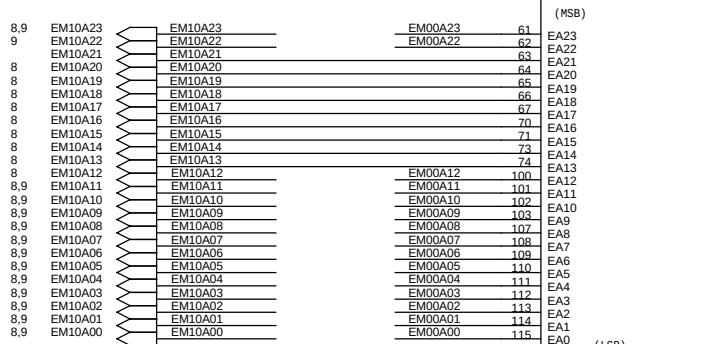
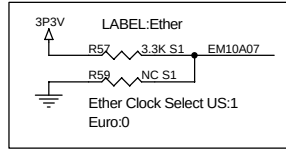
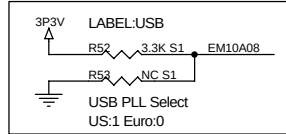
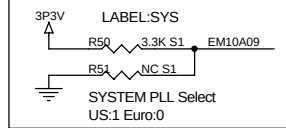
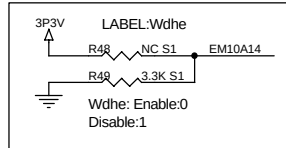
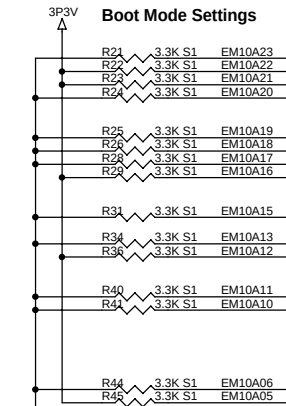




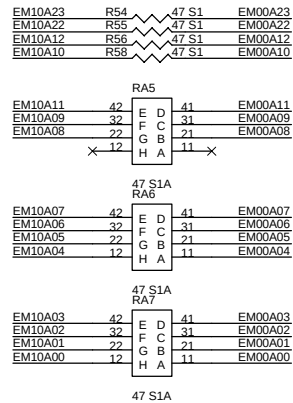
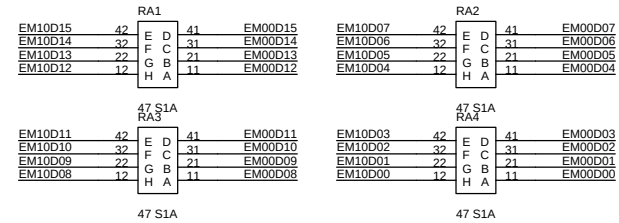
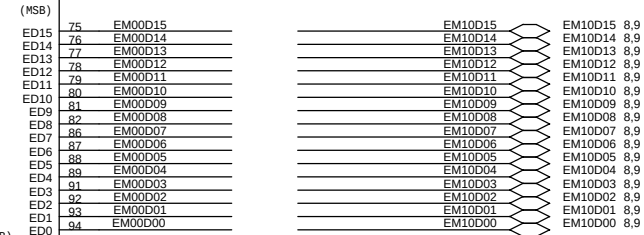
Device Configuration: Boot Mode Settings

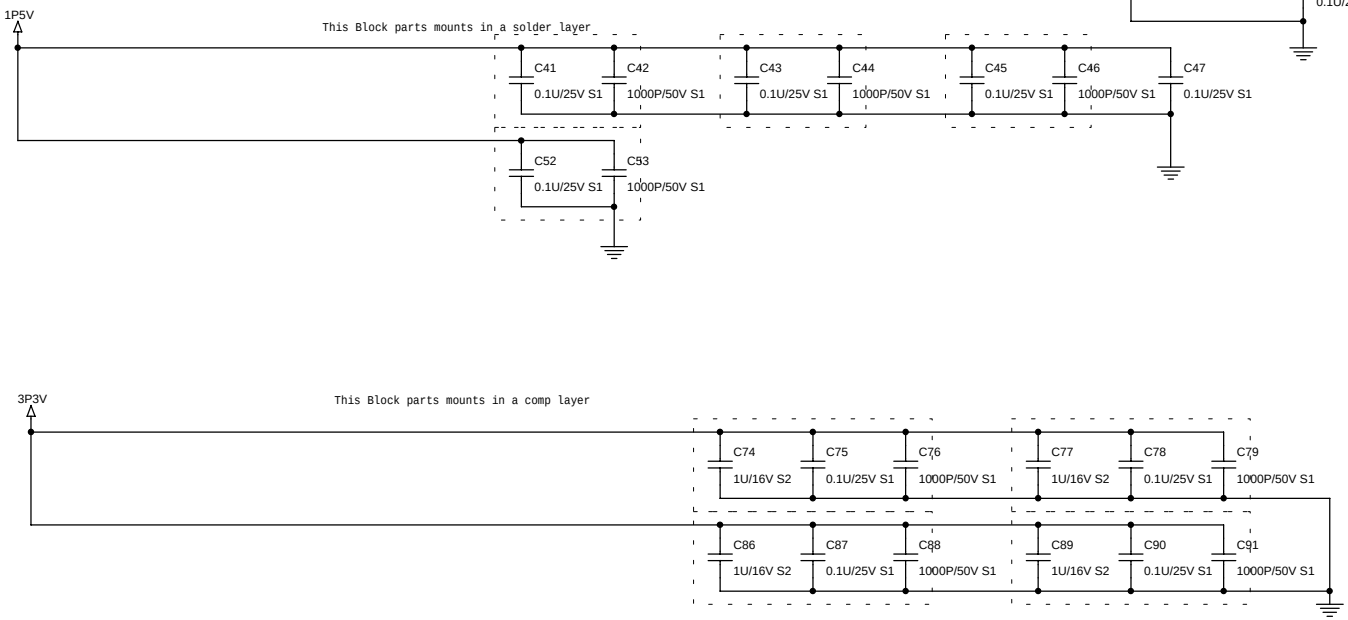
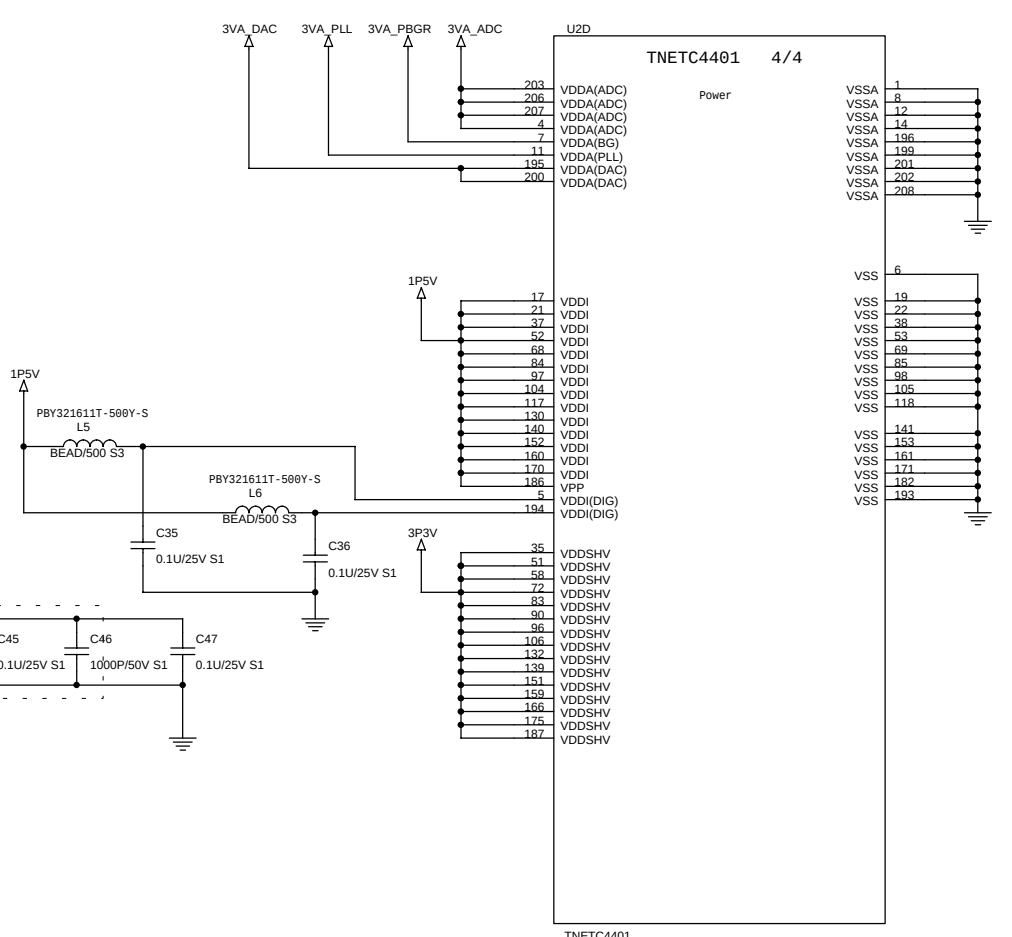
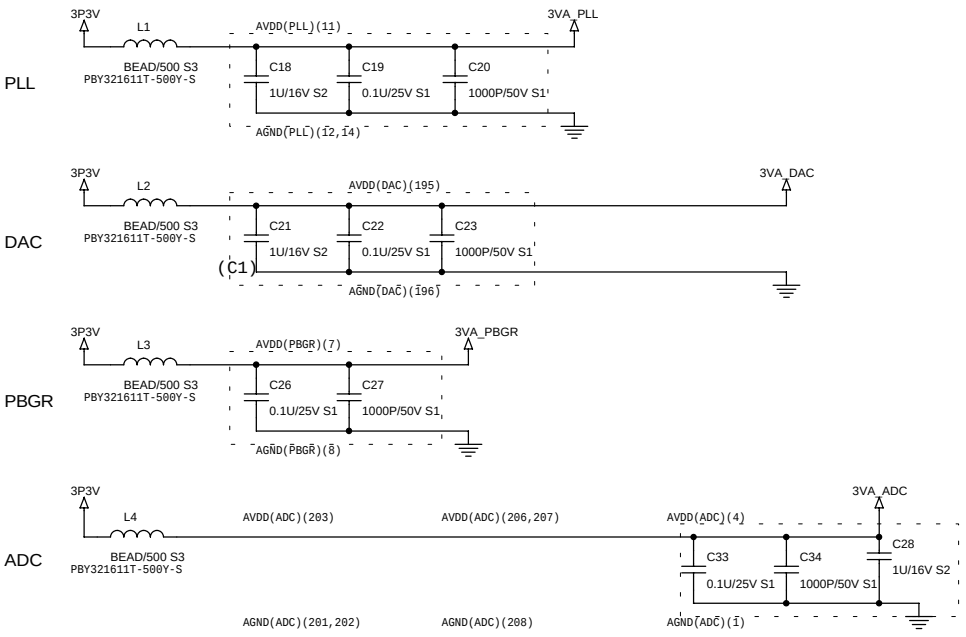
Field	Value	Description	External	PIN	Name
FLSH[1:0]	0	16Bit Flash width	61	EA23	
	1		62	EA22	
ENDIAN	1	Big Endian Select(default)	63	EA21	
VLYNQ CLK	0	VLYNQ CLK is input	64	EA20	
EURO/MANUAL	0	Read external manual setting	65	EA19	
BOOTS	0	Boot Location Select Boot from external Flash	66	EA18	
	0		67	EA17	
	1		70	EA16	
PLLBYPASS	0	PLL Mode	71	EA15	
Wdhe	1(OPT)	1:Watch dog timer disabled after reset	73	EA14	
	0(NML)	0:Watch dog timer enabled after reset			
Wsdp	0	Watch dog timer disable bit is writable	74	EA13	
EMIF_R	1	External memory interface is running at the full speed of the processor	100	EA12	
EMIF_TEST	0	External memory interface is in normal mode of operation	101	EA11	
DOCNOPLL	0	PLL in the DOCSIS is not bypassed	102	EA10	
SYSPLSEL	1(US)	1:System PLL is driven by the XTAL_IN	103	EA9	
	0(EURO)				
USBPLSEL	1(US)	1:USB PLL1 is driven by the XTAL_IN	107	EA8	
	0(EURO)				
ETHCLKSEL	1(US)	1:Ether Clock is driven by the XTAL_IN	108	EA7	
	0(EURO)				
DEFAULT	0	Use the above pin values to determin the boot options	109	EA6	
AV_NOPLL	1	Power down non docsis PLLs.VCO is turned on.	110	EA5	

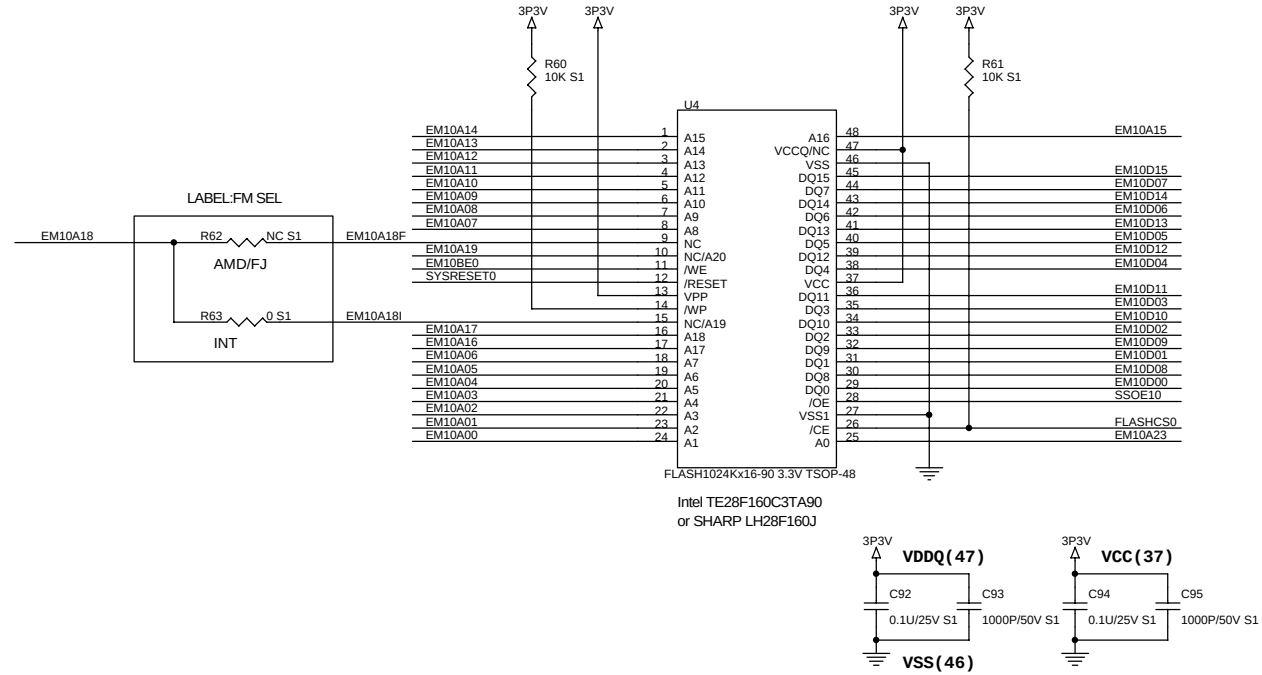
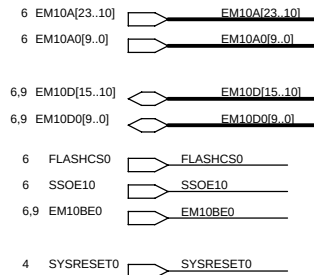
TNETC4401-2			
Title			
EC420 CABLE MODEM			
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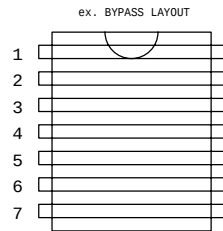
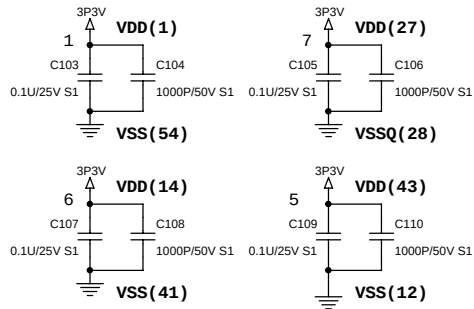
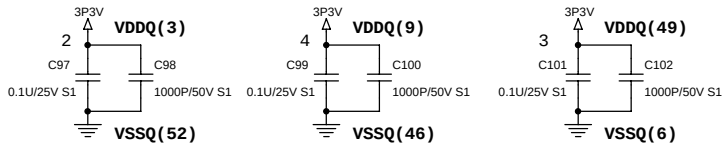
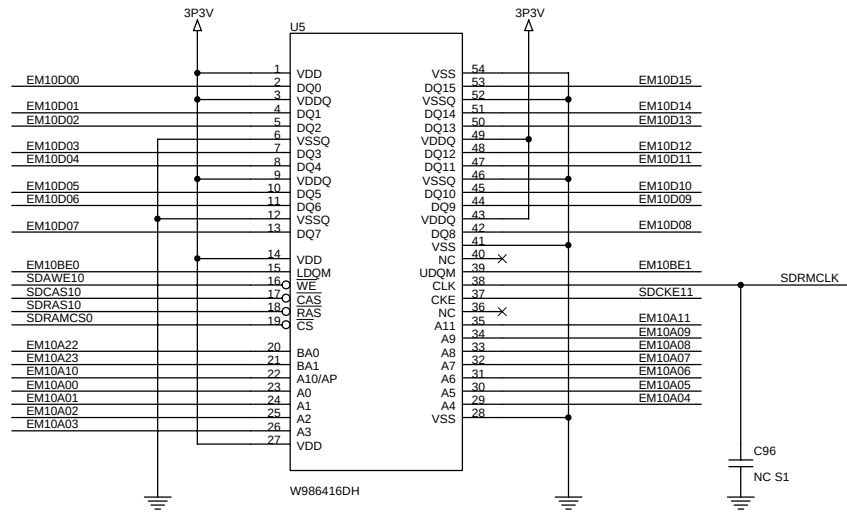
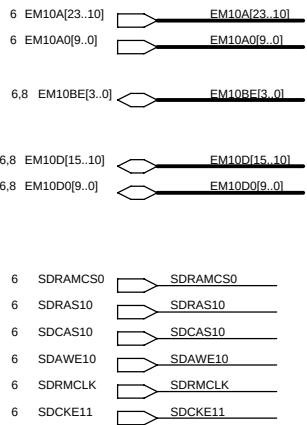
As for the row of data and an address lines, priority is given to a PCB layout.
They are exchangeable if it is easy to a PCB layout.



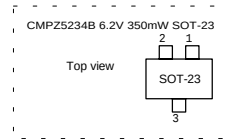




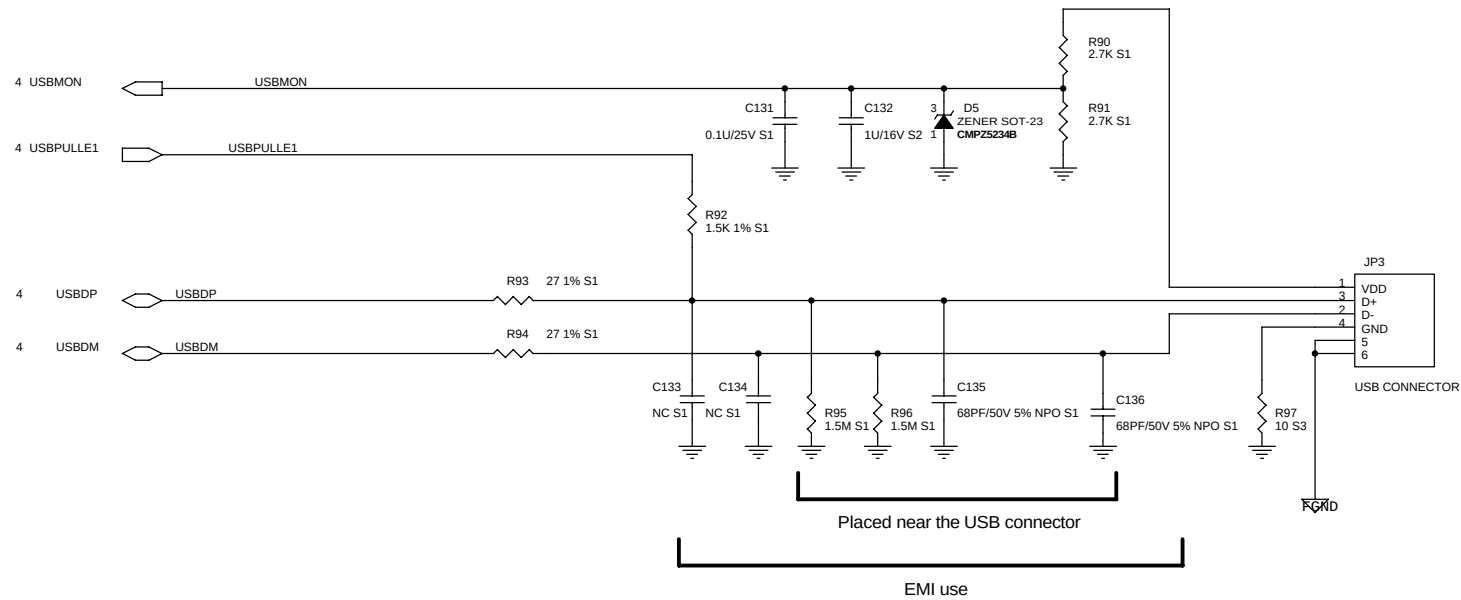
FLASH MEMORY			
Title			
EC420 CABLE MODEM			
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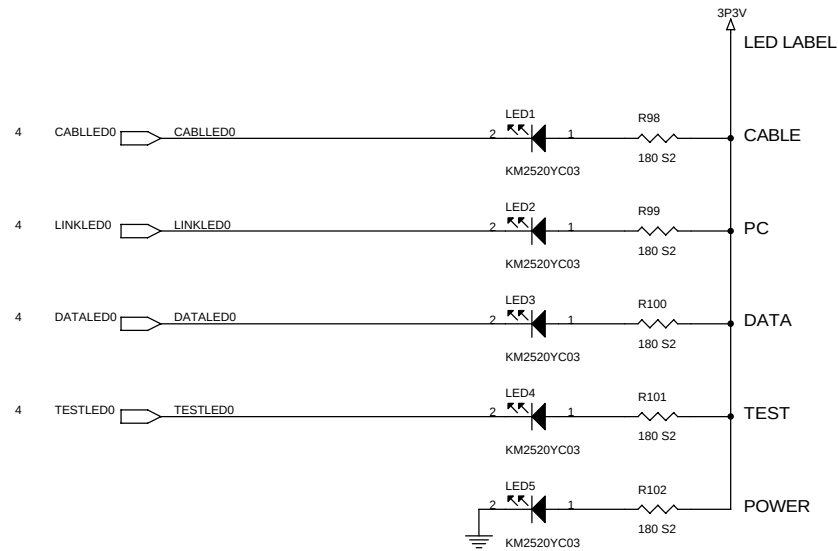
SDRAM			
Title			
EC420 CABLE MODEM			
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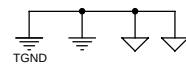
For protection and creat optional LPF in the way to the PUMA



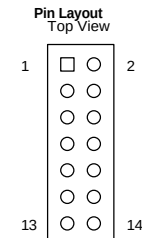
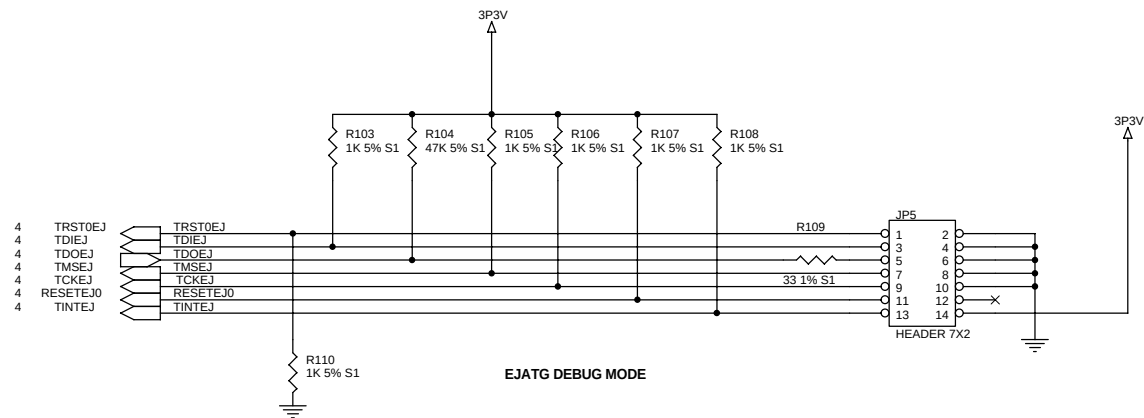
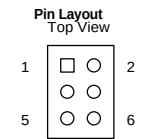
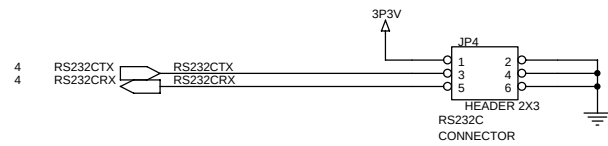
USB Intefcae			
Title			
EC420 CABLE MODEM			
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TNETC4401 IOL = 8mA max



LED Display			
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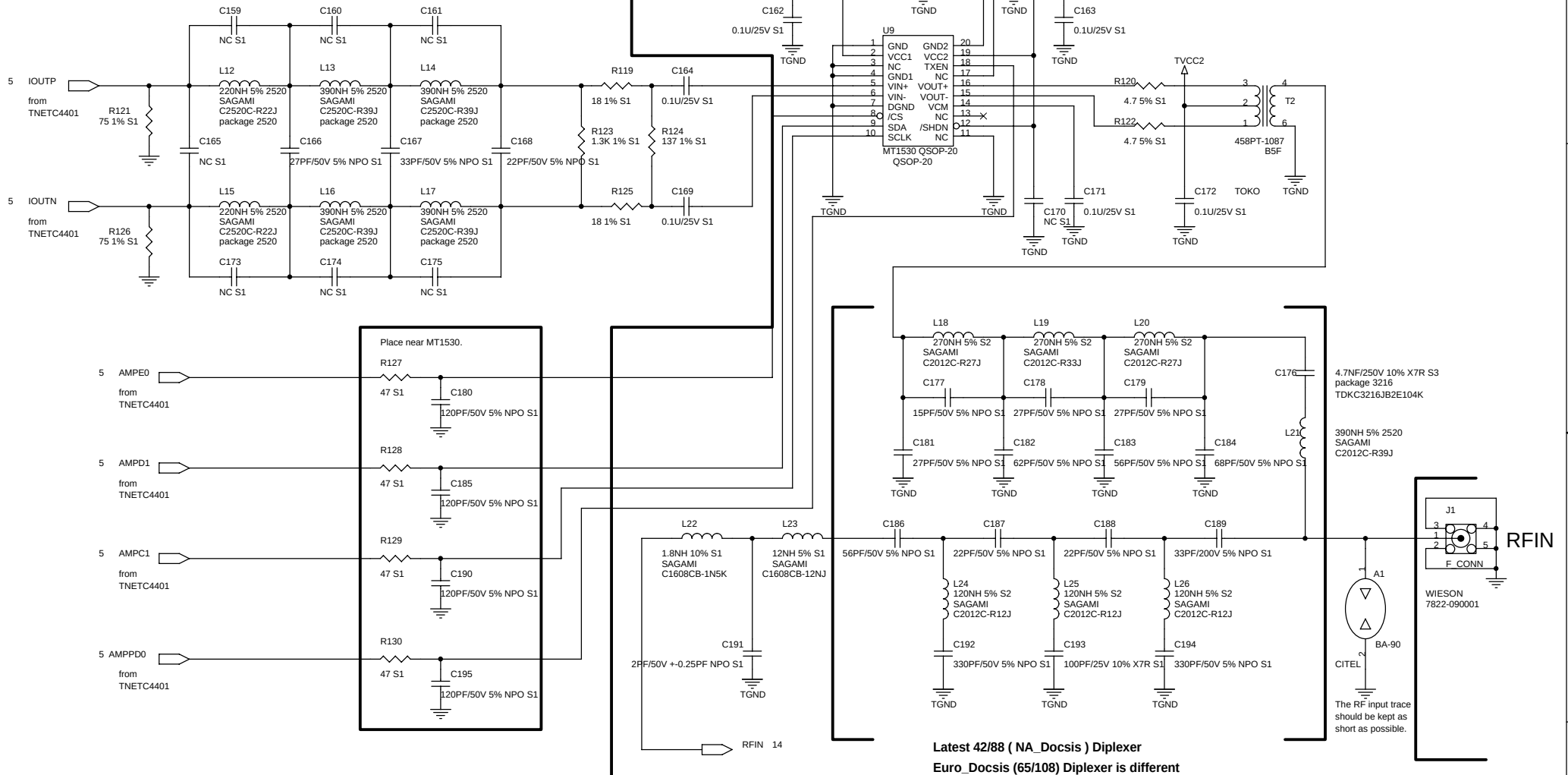


Layout : 4mm around the connector ; No chips allowed

Debug Interface			
Title			
EC420 CABLE MODEM			
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Sheet 13 of 17		A	

These components must be placed outside the tuner RF shield.

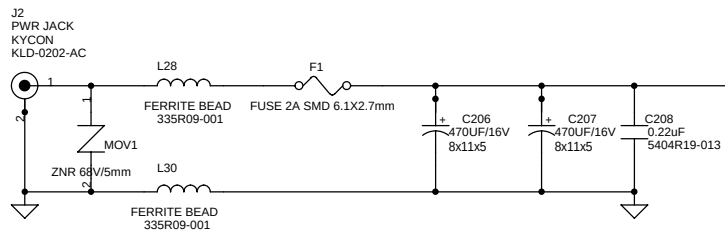
NA Docsis



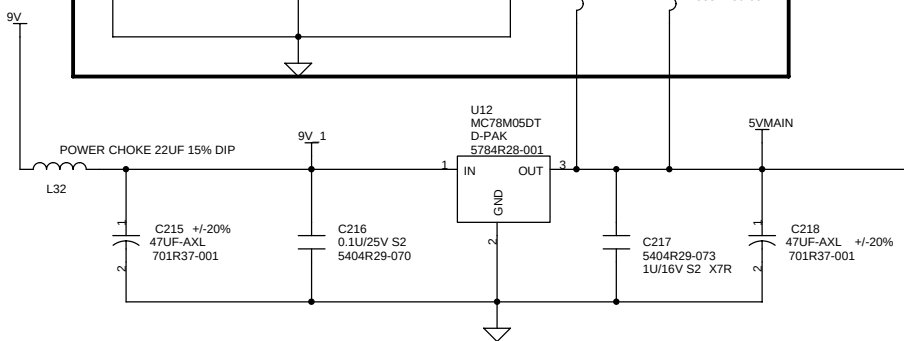
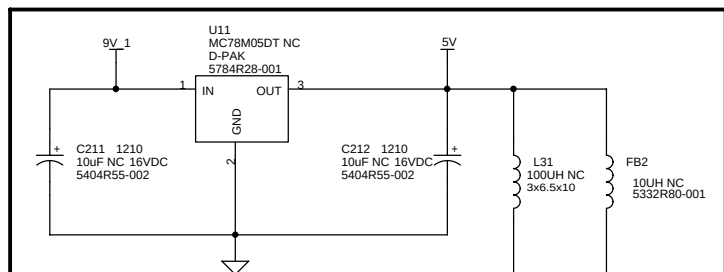
Latest 42/88 (NA_Docsis) Diplexer
Euro_Docsis (65/108) Diplexer is different

These components must be placed outside the tuner RF shield.

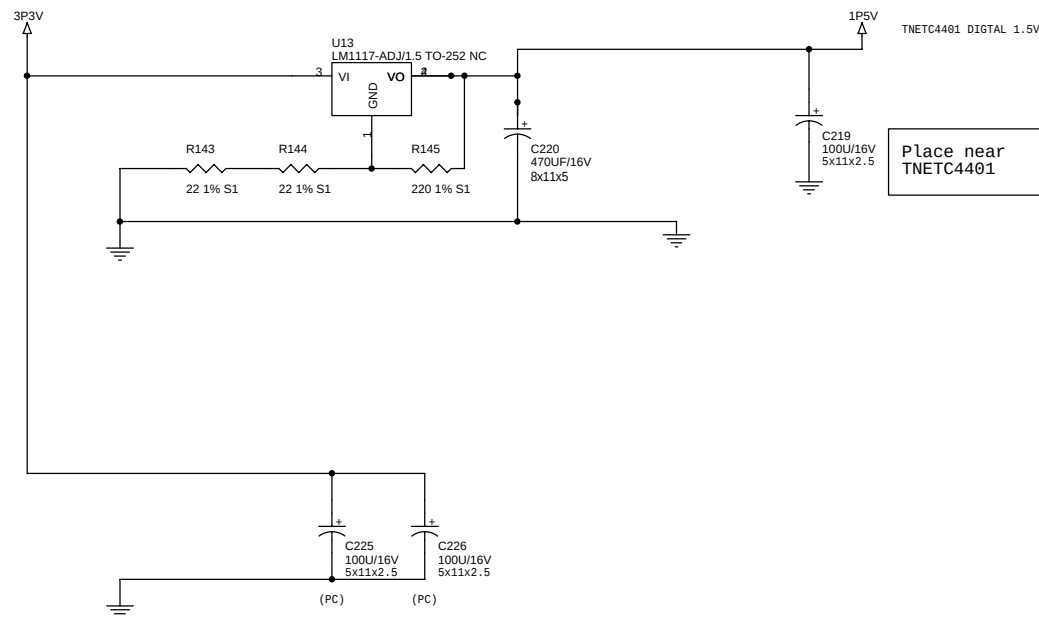
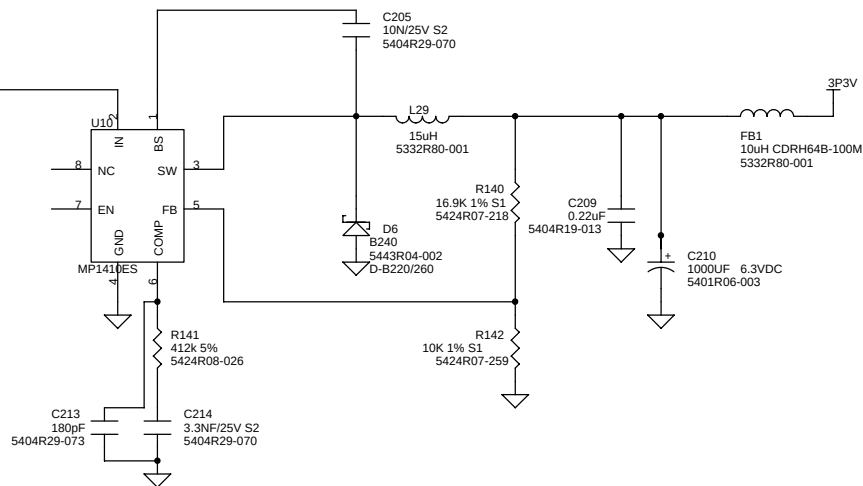
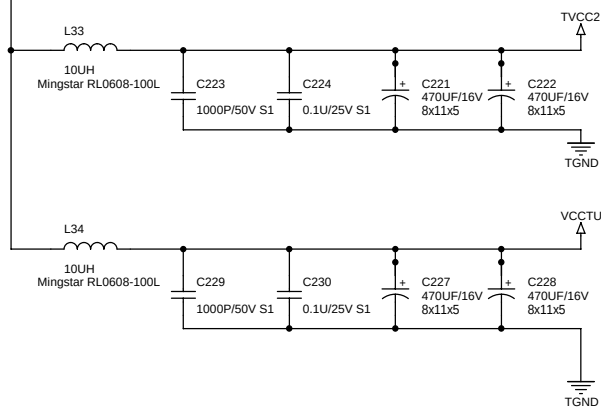
RF Upstream			
Title			
EC420 CABLE MODEM			
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Do not install and Layout



DCR = 0.56ohm max
Rate Current 1.0A max



Place near
TNETC4401

POWER Control		
Title		
EC420 CABLE MODEM		
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