

Netatmo

73 rue de Sevres 92100 Boulogne-Billancourt France

Date: 2022-03-25

Federal Communications Commission
Authorization and Evaluation Division
7435 Oakland Mills Road
Columbia, MD 21046

FCC ID: N3A-NAS01

To whom it may concern,
Pursuant to the provisions of the Commission's rules Title 47 Sections §0.457 and §0.459, we are requesting the Commission to withhold the following attachment(s) as confidential documents from public disclosure indefinitely.

- | | |
|---|---|
| <input checked="" type="checkbox"/> Block Diagram | <input type="checkbox"/> Tune-Up Information |
| <input checked="" type="checkbox"/> Operational Description | <input type="checkbox"/> Security Information |
| <input checked="" type="checkbox"/> Schematics | <input type="checkbox"/> Software Defined Radio (SDR) Information |
| <input type="checkbox"/> Parts List | |

The above materials contain trade secrets and proprietary information not customarily released to the public. The public disclosure of these materials may be harmful to the applicant and provide unjustified benefits to its competitors.

In addition to above mentioned documents, in order to comply with the marketing regulations in Title 47 CFR §2.803 and the importation rules in Title 47 CFR §2.1204, while ensuring that business sensitive information remains confidential until the actual marketing of newly authorized devices, we request Short Term Confidentiality of the following attachment(s);

- | | |
|---|---|
| <input checked="" type="checkbox"/> External Photos | <input checked="" type="checkbox"/> Test Setup Photos |
| <input checked="" type="checkbox"/> Internal Photos | <input checked="" type="checkbox"/> User Manual |

☐ For 45 days, pursuant to Public Notice DA 04-1705.

or

☒ For 180 days pursuant to KDB 726920 D01.

The applicant understands that pursuant to Section 0.457 of the Rules, disclosure of this application and all accompanying documentation will not be made before the date of the Grant for this application.

Sincerely,

Netatmo



Romain Cazalis/Head Hardware Design