

S898 Circuit Description

1. Introduction

The model S898 is a 25 channel (43*49MHz) cordless telephone.

The whole unit is divided into two main parts as follows:

One remote handset.

One base unit.

2. Functional Blocks of the Remote Handset

- Keyboard matrix and function LED
- MCU and MCU interface
- Antenna and Duplexer
- Receiver amplifier
- Demodulator
- Audio amplifier
- Compander
- Transmitter and receiver PLL frequency synthesizer
- Datashaper
- Transmitter amplifier
- Transmitter oscillator and modulator
- PLL loop filter
- Charge detector
- Buzzer amplifier

3. Circuit Block Description

3.1 Keyboard matrix and Function LED

Pin 3 to Pin 6, Pin 8 to Pin11 and Pin 25 of the MCU form a keyboard and the talk LED KLED1 function is controlled by the Pin 7 of the MCU U2.

3.2 MCU and MCU interface

The heart of the handset is the MCU 87C405-HM communicates with the PLL of TB31224 via pin 16, 17 and 18. These three pins also control the audio path of the incoming and outgoing audio signal.

Transmitter DC power is controlled by pin 14 of U2.

Data communication between Handset and Base is via the pin 12 and pin 24 by the RF link.

3.3 Antenna and Duplexer

ANT is the transmit and receive signal antenna.

DUP2 is a duplexer which rejects the transmitter signal to interfere the receiver.

3.4 Receiver amplifier

Q7 (9018H) and IFT3 is a tune RF amplifier which amplifies the 43 to 46 MHz incoming RF signal and rejects the unwanted signal at the other frequencies.

3.5 Demodulator

U1 (TB31224F) is a combo chip include a demodulator which performs double conversion in order to produce two IF signals and demodulate the FM signal into baseband audio. In addition, it has noise detector to monitor the receiving carrier for the changed condition of channels.

3.6 Transmitter amplifier

Q1, L2, C24, C22 and Q2 form transmitter amplifier which amplifies the outgoing 48*49MHz RF signal.

3.7 Transmitter oscillator and modulator

Q3, IFT4, C33, C32, D13, C28 form a PLL tune oscillator which oscillates at the FCC assigned channel frequencies. D14, C27, C29, C30 performs as a FM modulator.

3.8 PLL loop filter

C20, C73, R2, R1 and R37, C48, R38, C78, C79 are the receive and transmit

PLL loop filters respectively.

3.9 Charge detector

ZD1, D5, R44, R43 and C50 form a charge detector to direct the charging signal to the MCU:

3.10 Buzzer amplifier

Q5 is a buzzer amplifier driven directly by the MCU pin 23. Pin 22 is used to lower the Buzzer volume when press the key.

3.11 Audio Amplifier

For U1 TB31224 - Pin15, Pin16, Pin 19, Pin20 the receive audio amplifier respectively

For U1 TB31224 - Pin13, Pin14, Pin 9, Pin10 the transmit audio amplifier respectively

3.12 Comander and Low battery detect

U1 TB31224 also include a comander IC which performs compress at transmitted signal via Pin12 and expand the received signal via pin17.

Low battery detect output is from U1's Pin21.

3.13 Datashaper

U1's Pin 23 is a datashaper which send the information from BASE to the MCU Pin24(RXDAT).

4. Functional Blocks of the Base unit

- Power supply
- MCU and MCU interface
- Antenna and Duplexer
- Receiver amplifier
- Demodulator
- Audio amplifier
- Compander
- Transmitter and receiver PLL frequency synthesizer
- PLL loop filter
- Data shaper and noise detector
- Transmitter amplifier
- Transmitter oscillator and modulator
- Charge detector
- Line audio interface
- Ring detector
- LED function board
- Power fail detector

5. Circuit Block Description

5.1 Power supply

BU3 78L05J regulate the input DC 9V which provides 5VDC power to every part of the circuit.

5.2 MCU and MCU interface

The heart of the base is the MCU 87C405-BM communicates with the PLL of Combo Chip TB31224 via pin 18, 19, and 20.

Transmitter DC is controlled by pin 22.

Pin 4 to Pin9 consist of the DTMF generator. The communication between Handset and Base is via the pin 28 and pin 25 through the RF link.

5.3 Antenna and Duplexer

ANT is the transmit and receive signal antenna. BDUP2 is a duplexer which rejects the transmitter signal to interfere the receiver.

5.4 Receiver amplifier

BQ1 and BIFT1 is RF amplifier which amplifies the 48 to 49 MHz incoming RF signal and rejects the unwanted signal at the other frequencies.

5.5 Demodulator

BU5 (TB31224F) include a demodulator IC which performs double conversion to produce two IF signals and demodulate the FM signal into baseband audio. In addition, it has noise detector to monitor the receiving carrier for the changed condition of channels. BU5 pin5 provide the logic signal to indicate the noisy condition to BU1 from pin 21.

5.6 Transmitter

BQ3, BL3, BC30, BC33 and BQ5 form a RF transmitter amplifier which amplifies the outgoing 43 to 46MHz RF signal. BQ4 and BQ6 are band switch

5.7 Transmitter oscillator and modulator

BQ7, BC47, BC40, BVD2, BIFT6 and BQ6 form a PLL tuned oscillator which oscillates at the FCC assigned Channel frequencies. BVD1 performs as a FM modulator.

5.8 PLL loop filter

BR38, BR21, BC24, BC25 BC44 and BR15, BR16, BC16, BC17 BC18 are the transmit and receive PLL loop filters respectively.

5.9 Charge detector

BQ12 is a charge detector to direct the charging signal to the MCU pin 24.

5.10 Audio amplifier and compander

For BU5 TB31224 - Pin15, Pin16, Pin 19, Pin20 the receive audio amplifier respectively

For BU5 TB31224 - Pin13, Pin14, Pin 9, Pin10 the transmit audio amplifier respectively

BU5 TB31224 also include a compander IC which performs compress at transmitted signal via Pin12 and expand the received signal via pin17.

5.11 Line audio interface

BQ14, BR99, BR98, BR103, BR100, BR101, BC75, BR106, BR104 and BT1 line transformer are the audio interface to the telephone line. The transformer is also for telephone line isolation.

5.12 Datashaper and noise detector

BU5's Pin 23 is a datashaper which send the information from HANDSET to the MCU Pin12(RXDAT).

BU5 PIN 25,26 form a noise detector. It can detect the high frequency noise and the signal is given out from BU5's pin 5 to BU1 pin 21.

5.13 Ring detector

BR104, BC76, BZ1, BZ2, BU4(TLP*521*1), BR106, BDd8, form a ring detector to control the pin23 of MCU BU1.

5.14 Power fail detector

BZD3, BR60, BQ11, BR71 form a power fail detector. The power failure signal is sent to pin 13 of BU1.

5.15 LED function board

BLED2 is for power, BLED1 is for charge and in use.