

COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2481

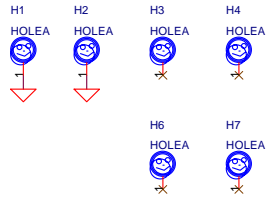
PCB Revision : 0.2

SMT build : PVT

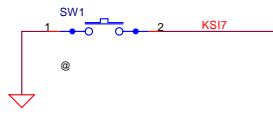
EDX20 Sub_Board Schematics Document

BTN Board

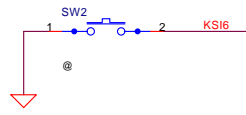
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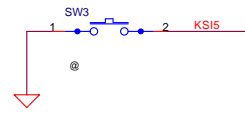
USER BTN1



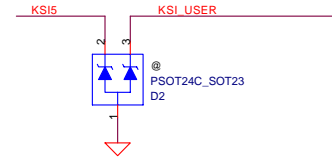
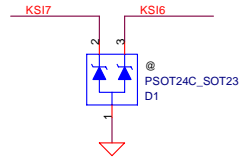
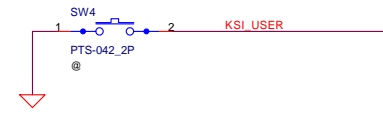
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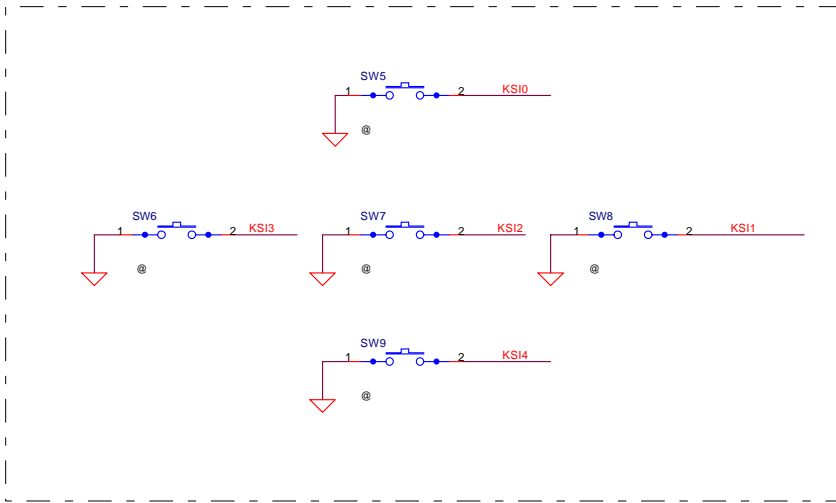
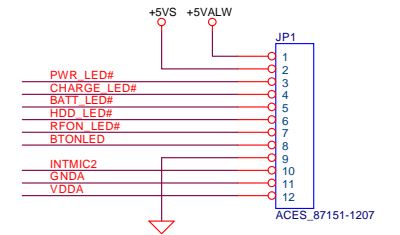
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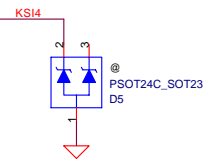
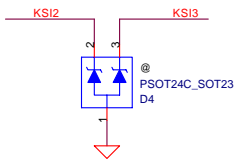
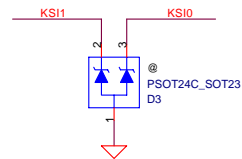
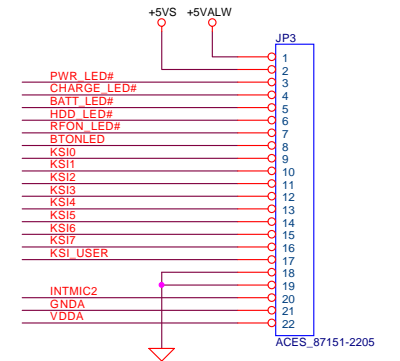
USER BTN4



To LED Board Connector



From M/B Side Connector



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Compal Electronics, Inc.		
Title: BTN BOARD		
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2482

PCB Revision : 0.3

SMT build : PVT

EDX20 Sub_Board Schematics Document

LED Board

Title		
COVER PAGE		
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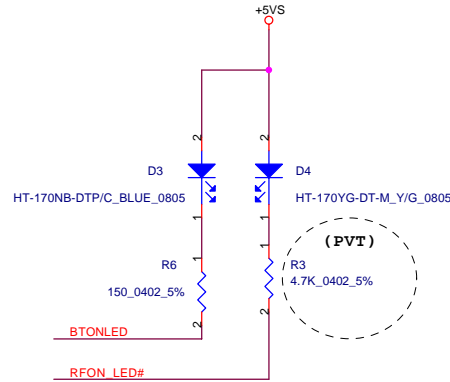
Compal Confidential

Model Name : EDX20

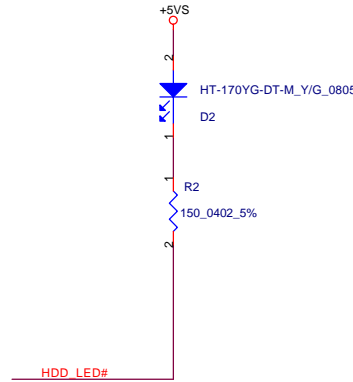
File Name : LS-2482

LED Board

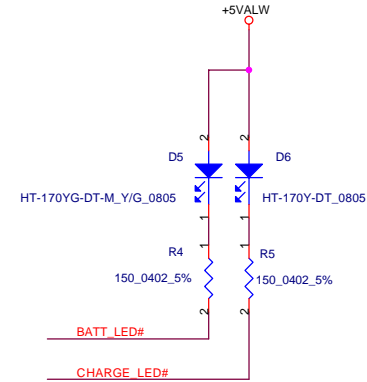
Wireless LAN/BT LED



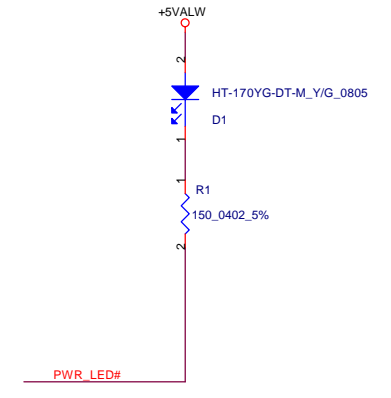
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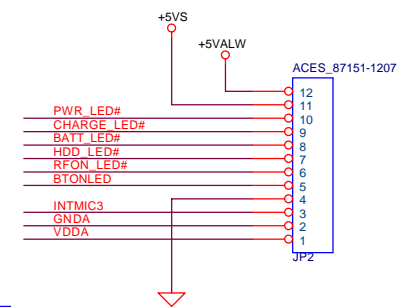
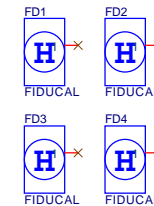
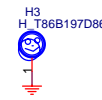
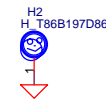
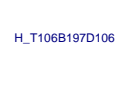
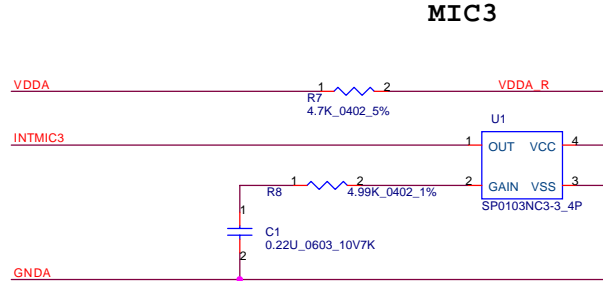
BATTERY/CHARGE LED



POWER LED



MIC3



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Compal Electronics, Inc.			
Title	<Title> LED & MIC3 Board		
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2483

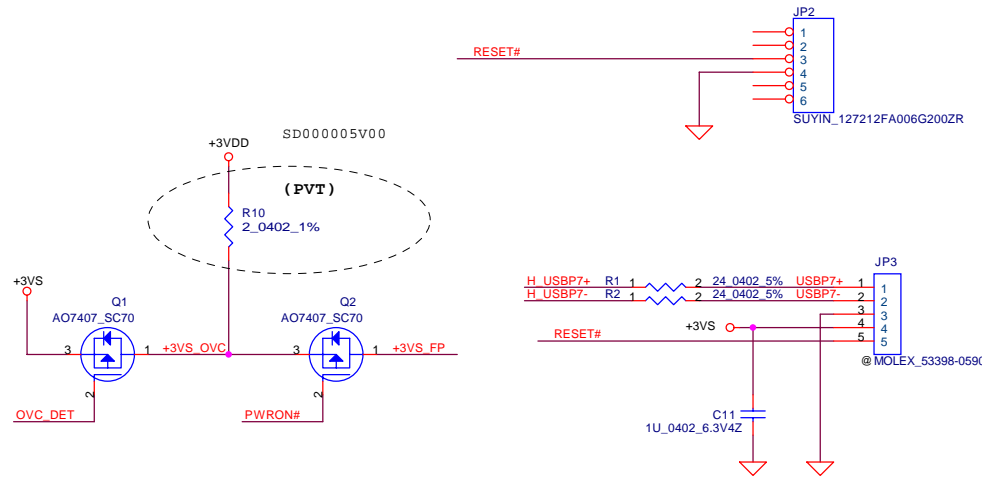
PCB Revision : 0.3

SMT build : PVT

EDX20 Sub_Board Schematics Document

FingerPrint Board

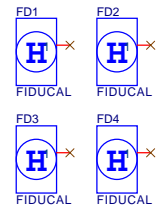
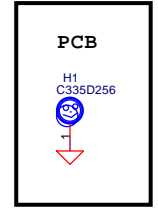
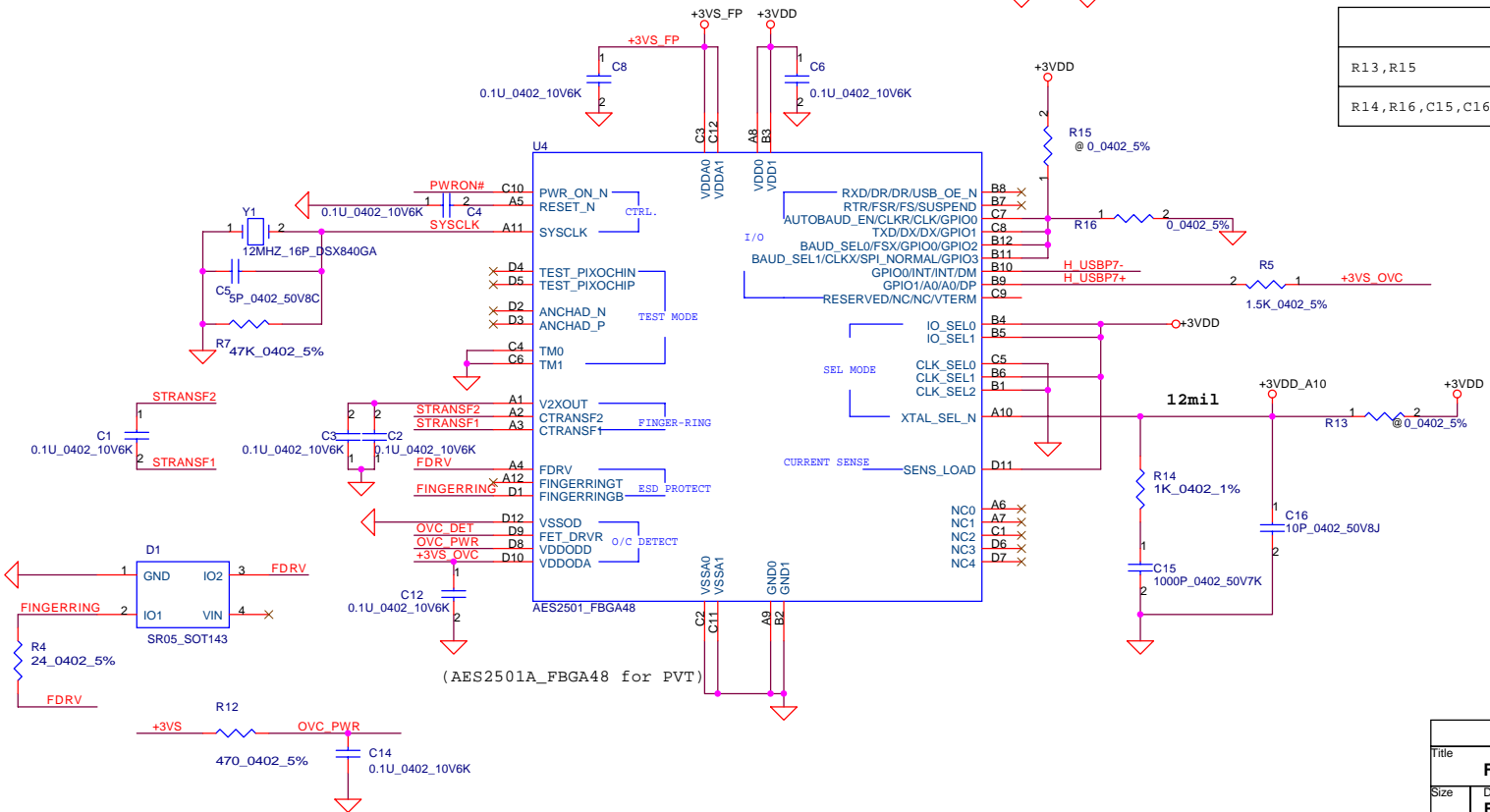
Title		
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B7, B8, D2, D3, D4, D5 AND D9 MUST KEEP VIA

CLK_SEL[0..2]	010	USB I/F 12MHz
IO_SEL[0,1]	11	USB 2.0 FULL SPEED

	AES2501	AES2501A
R13,R15	POP	DE_POP
R14,R16,C15,C16	DE_POP	POP



Title		Fingerprint
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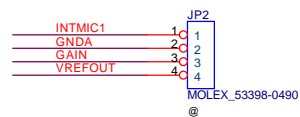
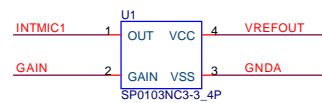
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MODEL NAME : EDX20
PCB No : LS 2484
PCB Revision : 0.3
SMT build : PVT

EDX20 Sub_Board Schematics Document

MIC Board

Title		
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Compal Electronics, Inc.		
Title		
MIC		
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	EDX20 LS2484	0.3
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2485

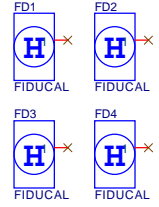
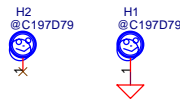
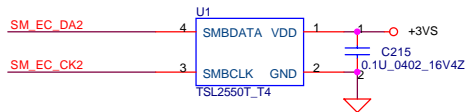
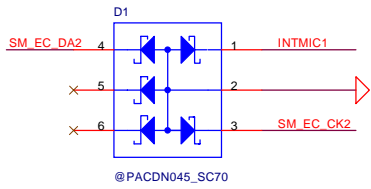
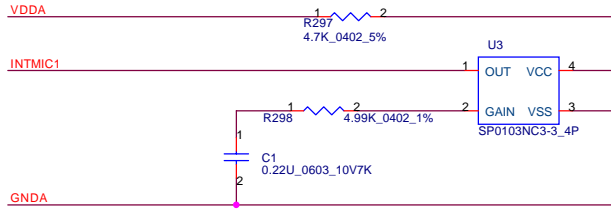
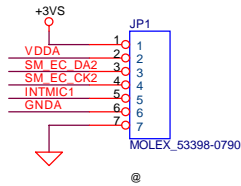
PCB Revision : 0.2

SMT build : PVT

EDX20 Sub_Board Schematics Document

ALS_MIC Board

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ALS/MIC		
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2486

PCB Revision : 0.2

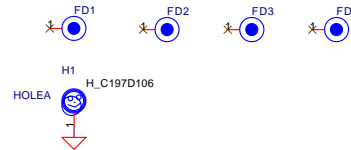
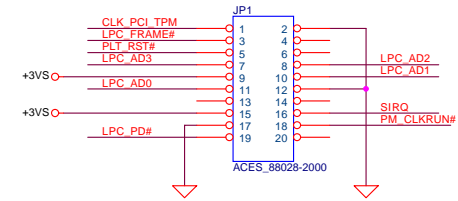
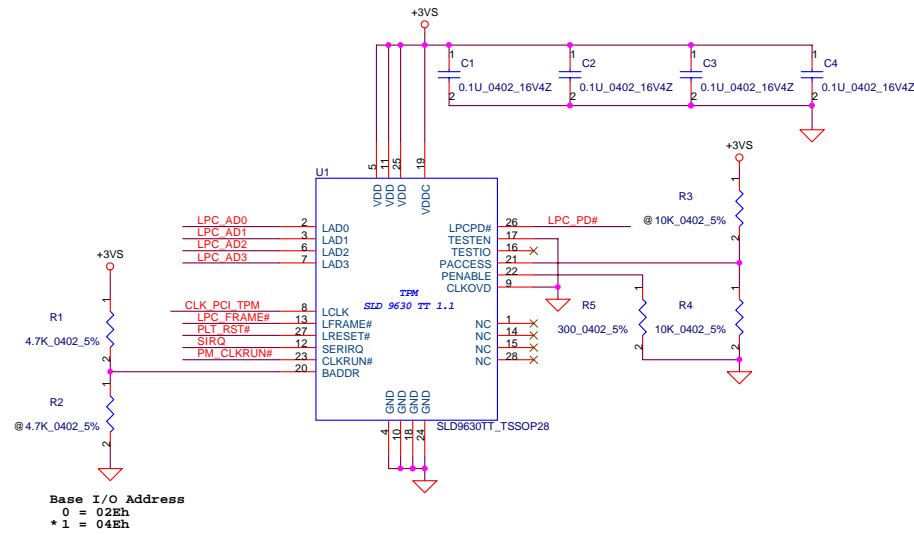
SMT build : PVT

EDX20 Sub_Board Schematics Document

TPM Board

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TPM



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Compal Electronics, Inc. EDX20 TPM		
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2487

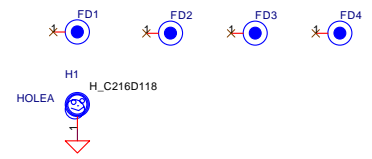
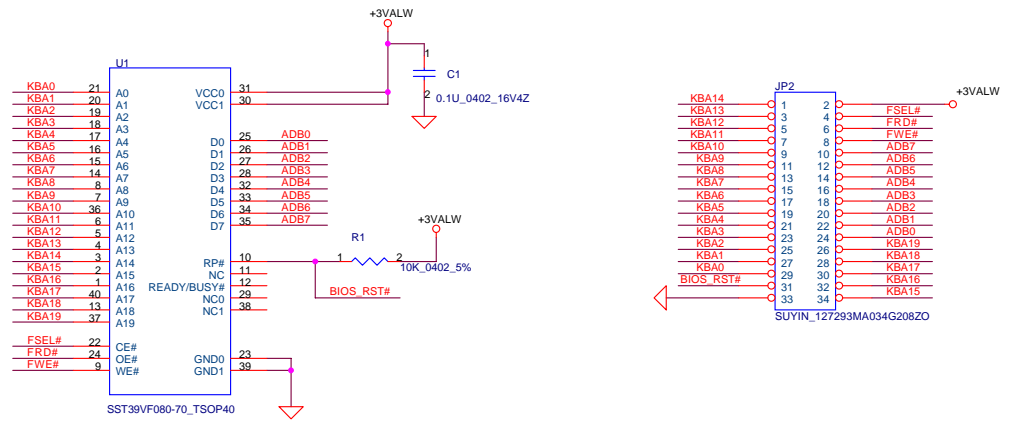
PCB Revision : 0.2

SMT build : PVT

EDX20 Sub_Board Schematics Document

BIOS Board

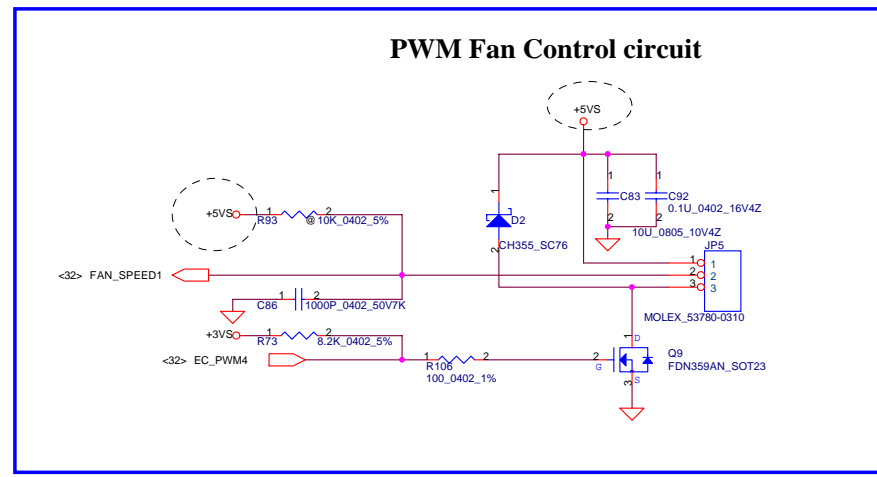
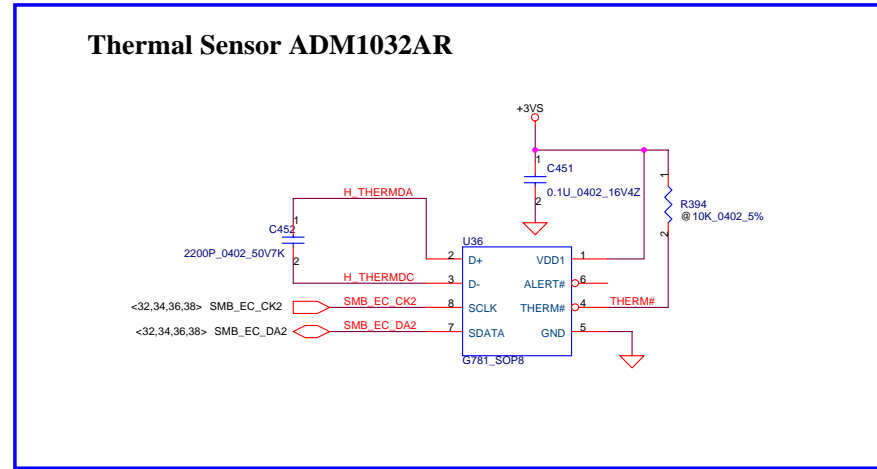
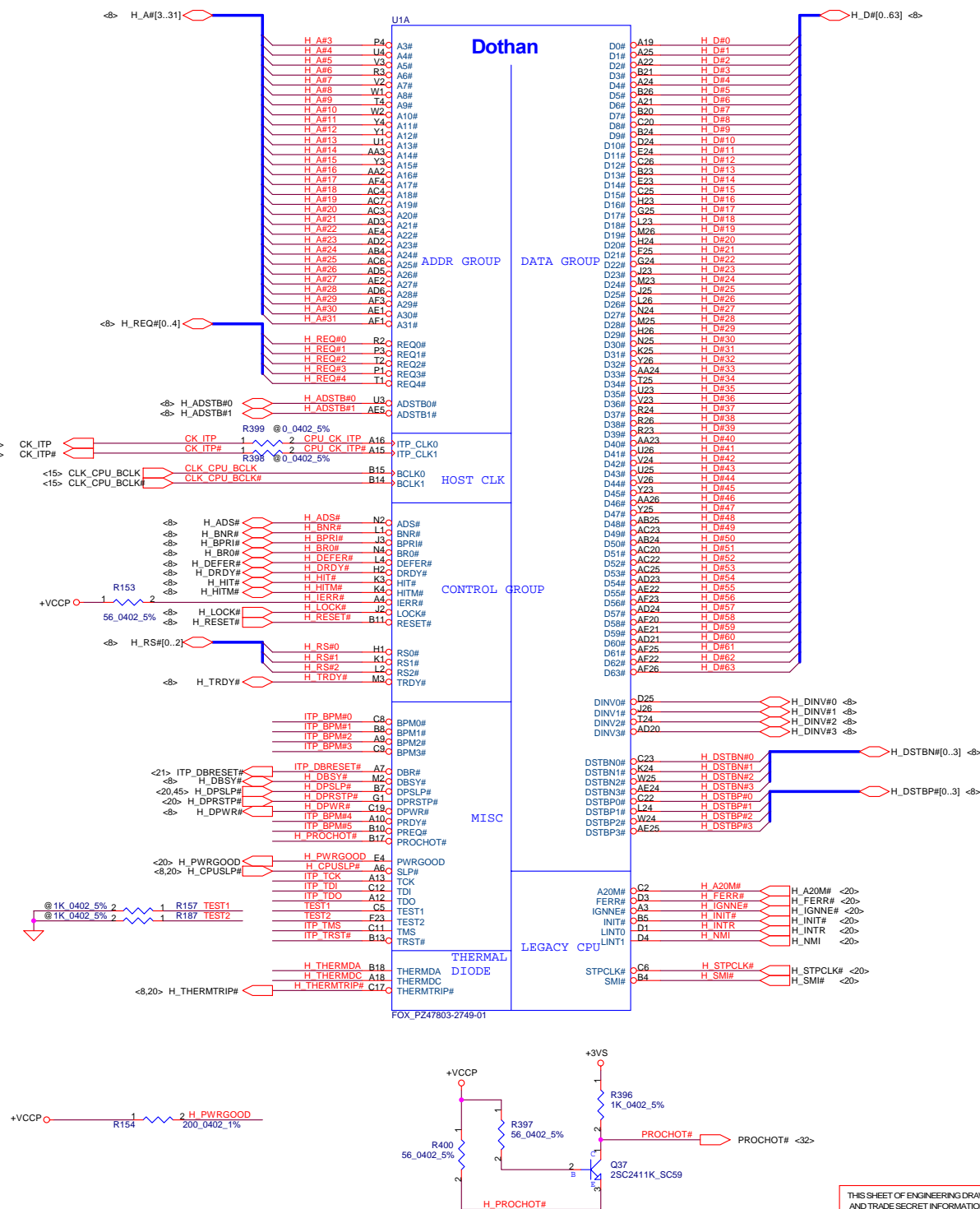
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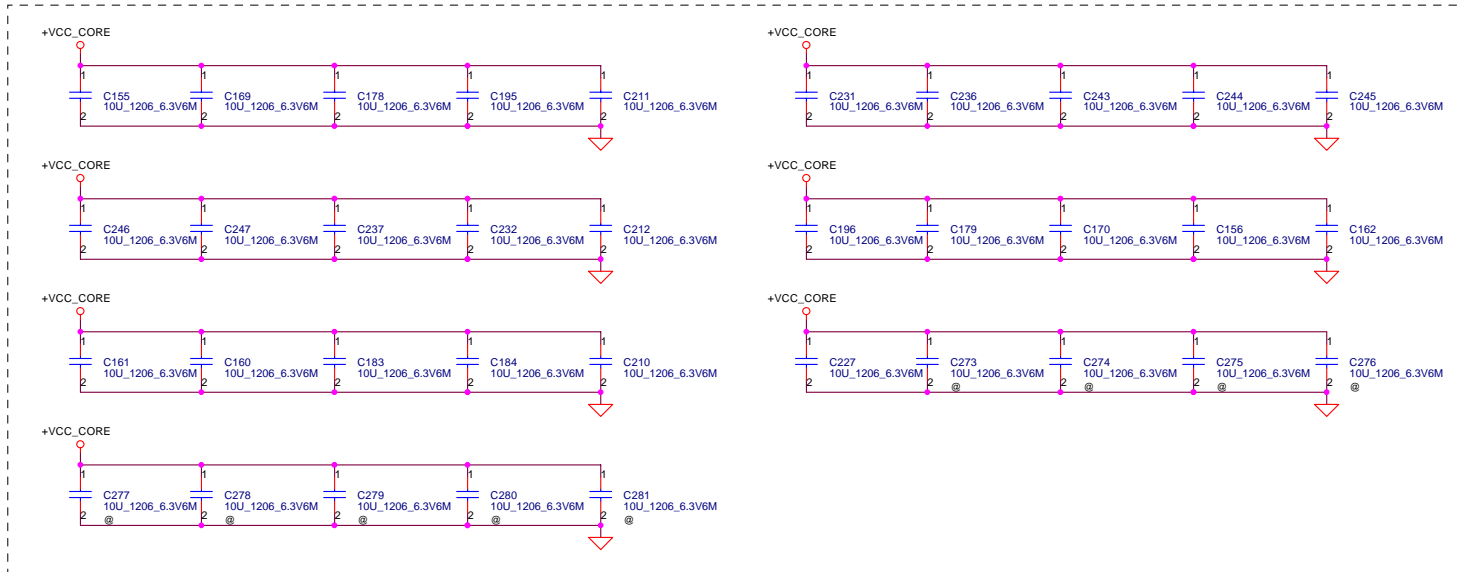
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Compal Electronics, Inc.		
Title		
BIOS Board		
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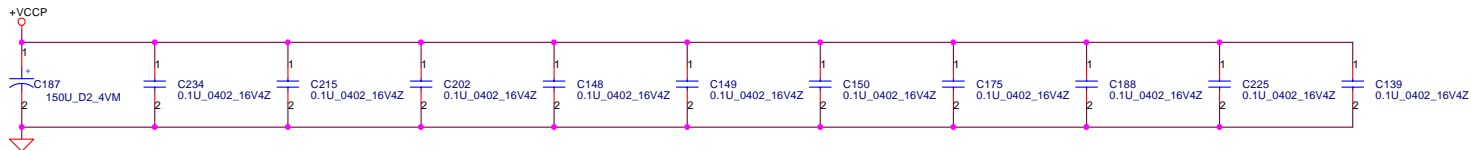
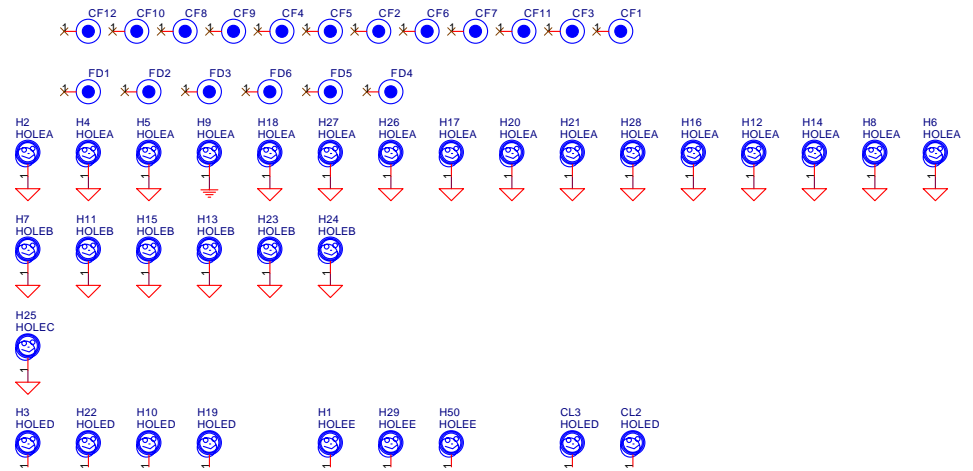
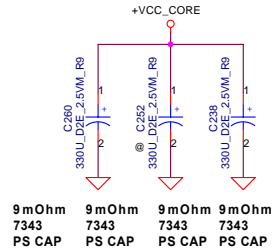
CHANGE TO CPU PN



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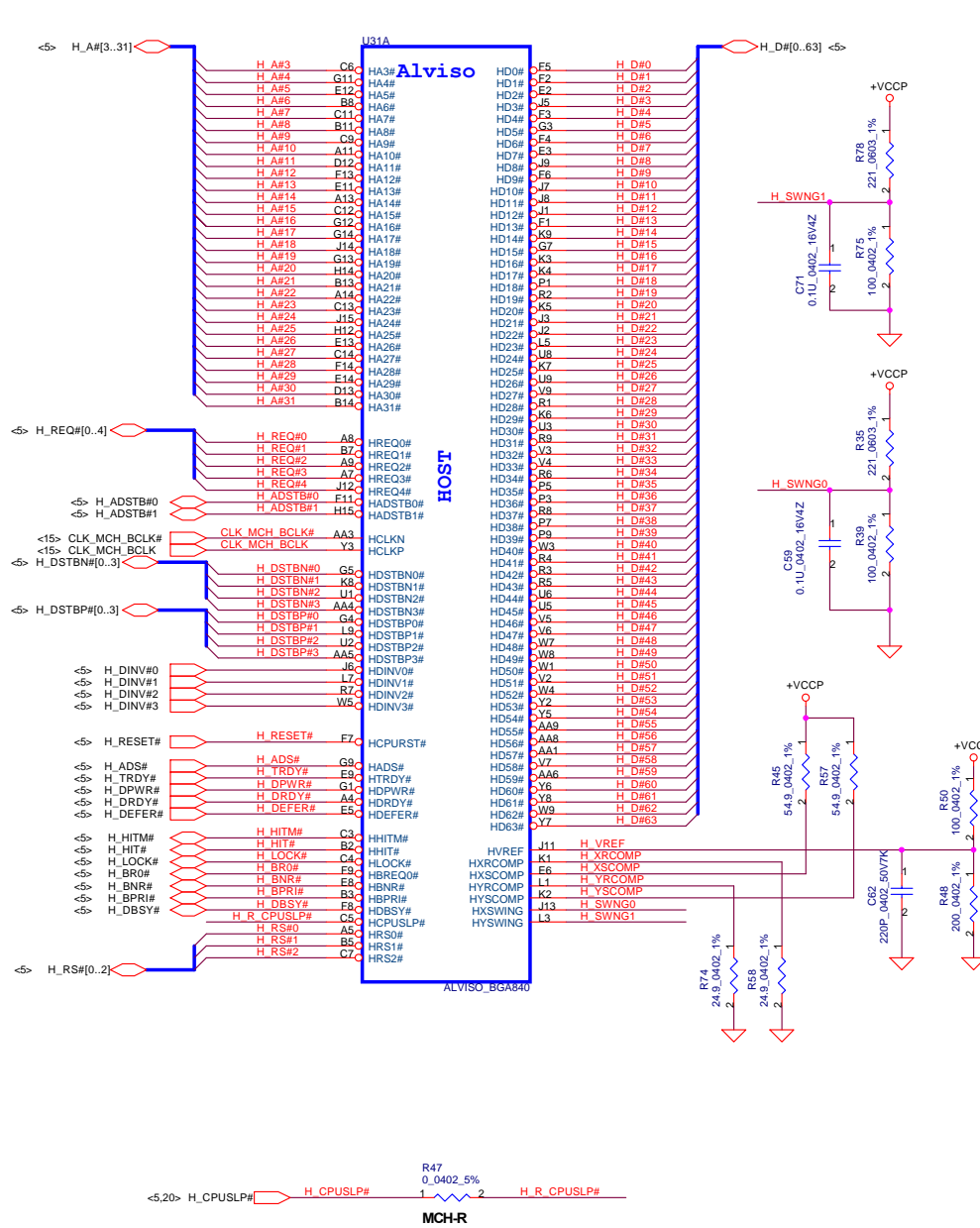


Near VCORE regulator.



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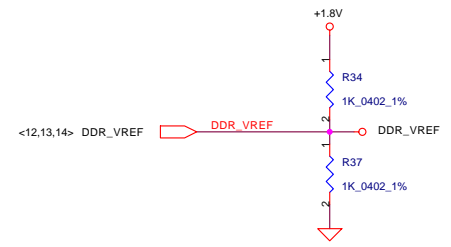
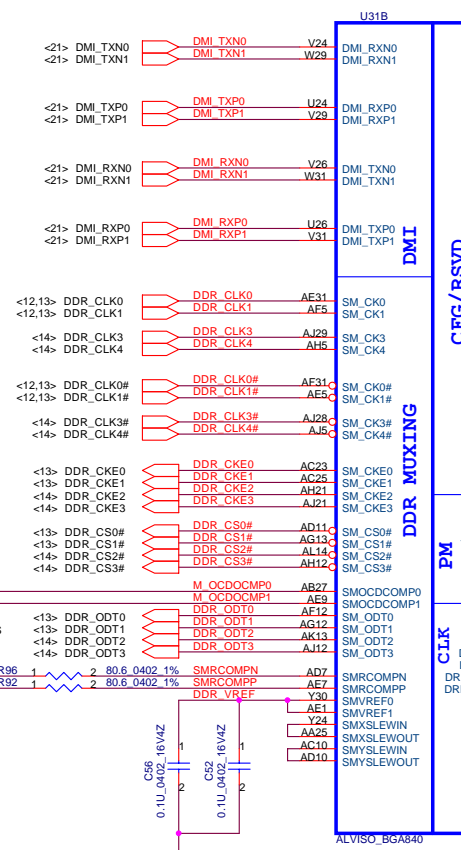
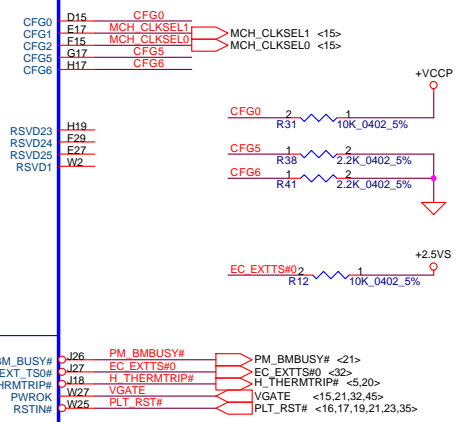
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Layout Note:
Route as short as possible

Note:
"Do not install MCH-R for Dothan-A,
Install MCH-R for Dothan-B"

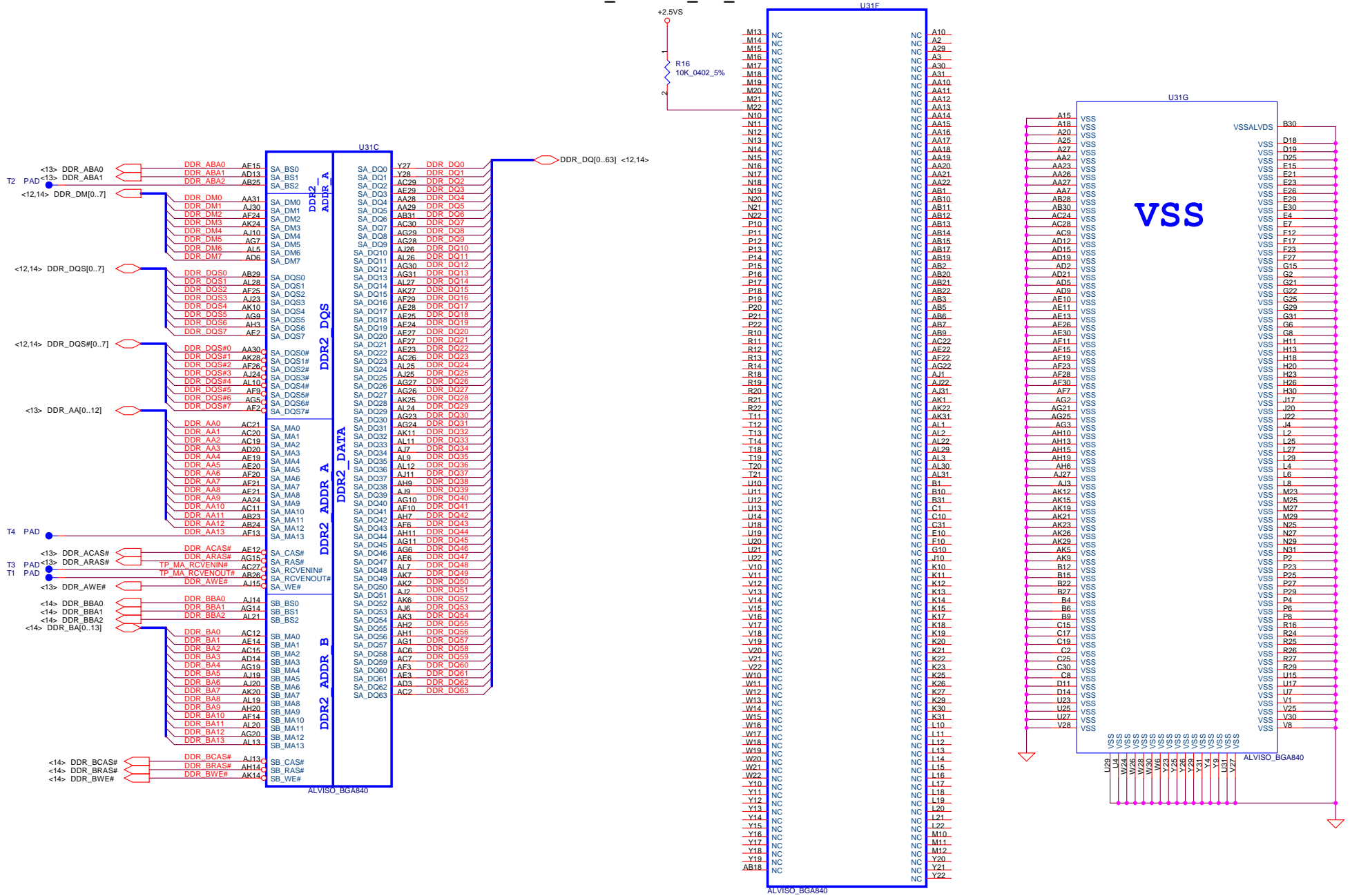
CFG2[0]	Refer to page 15 for FSB frequency select
CFG5	Low = DMI x 2 * High = DMI x 4
CFG6	Low = DDR-I * High = DDR-I



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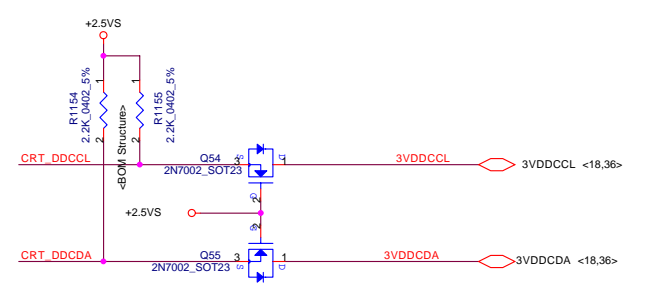
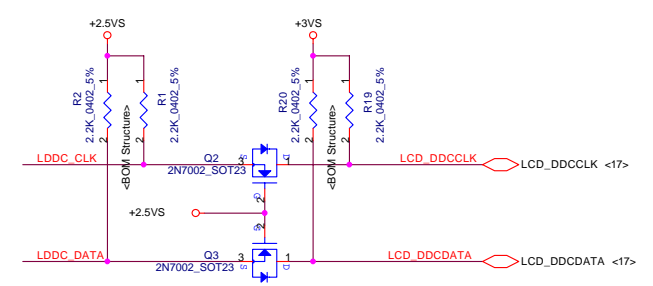
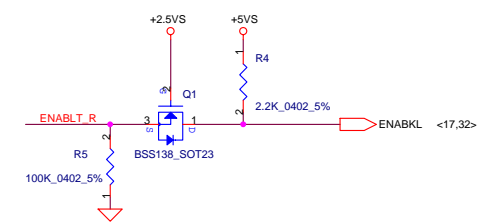
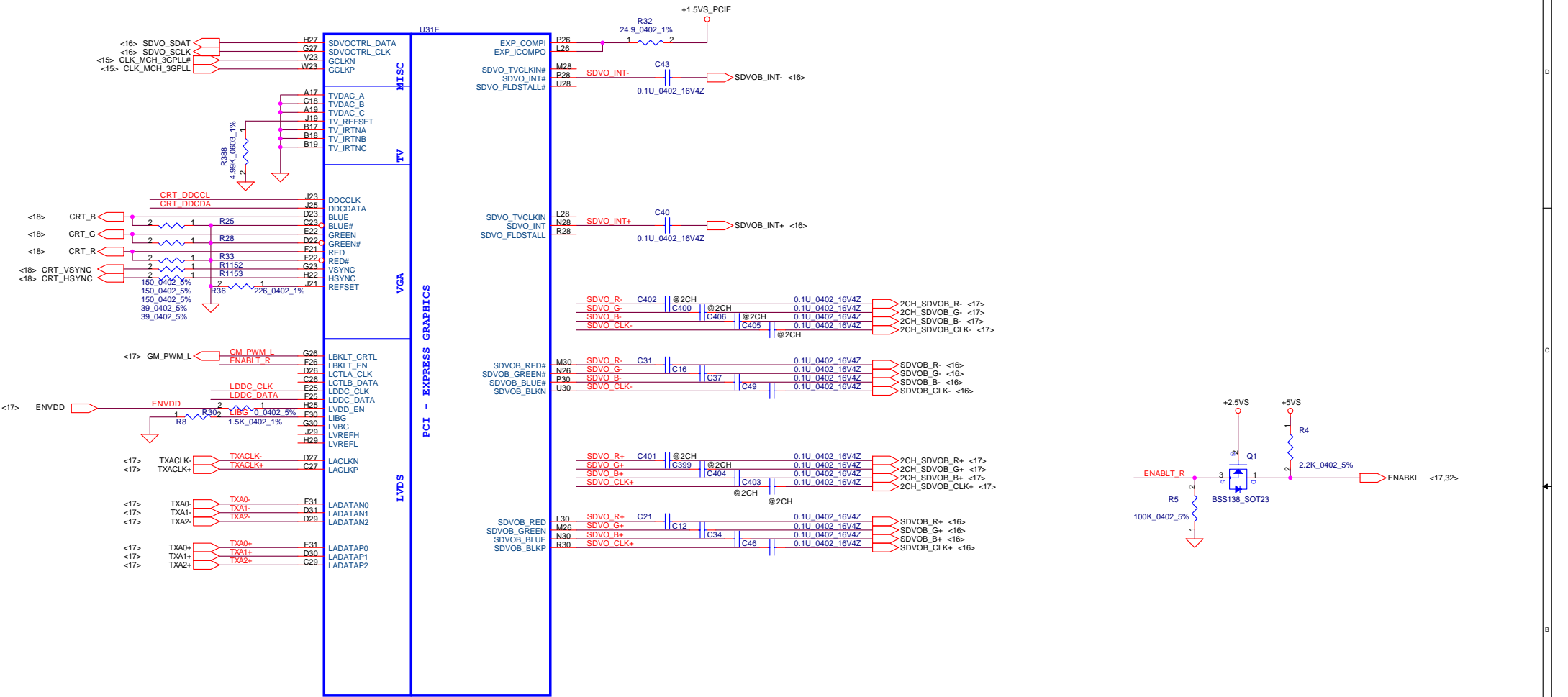
Compal Electronics, Inc.			
Alviso (1 of 4)			
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Sonoma_Platform_MOW_04WW25



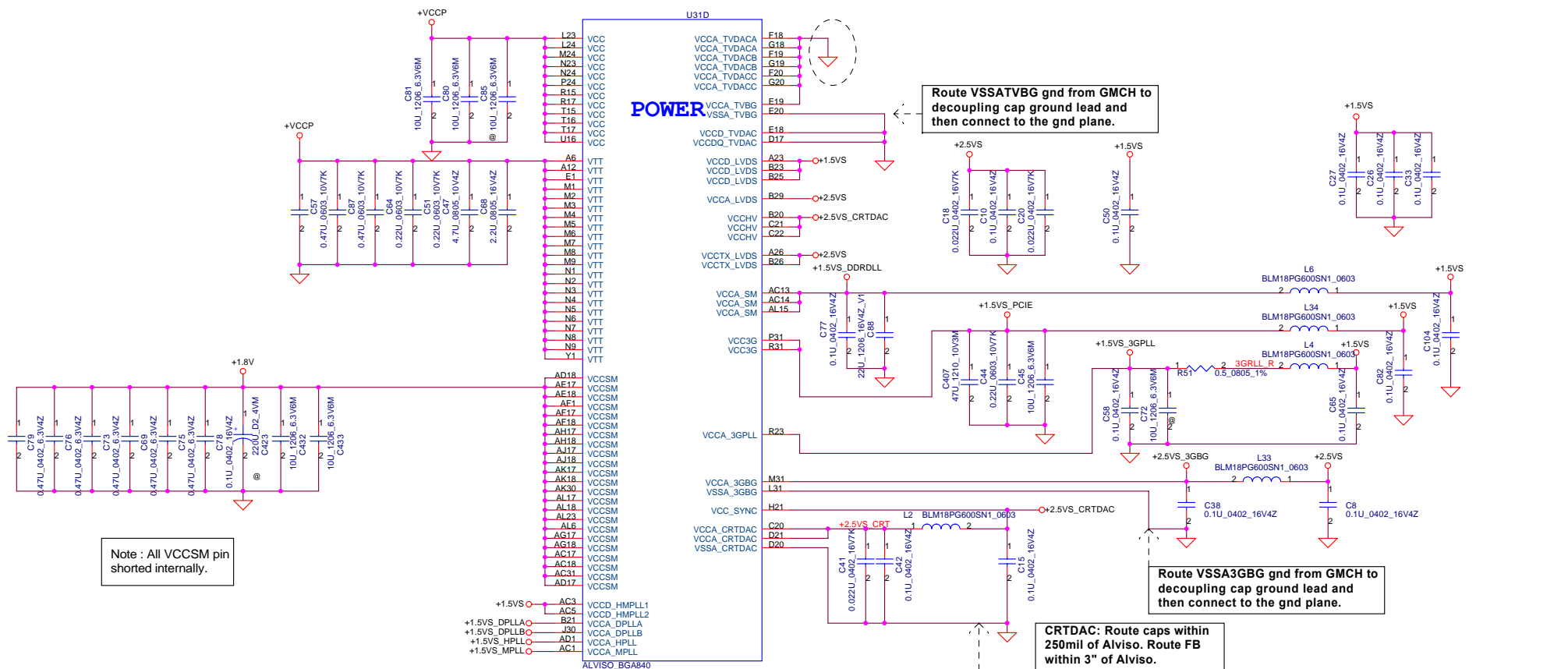
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Compal Electronics, Inc.			
Alviso (2 of 4)			
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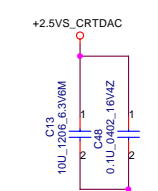
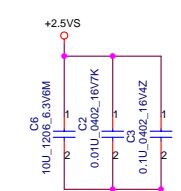
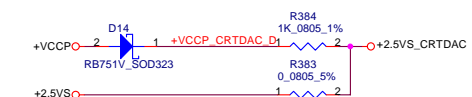
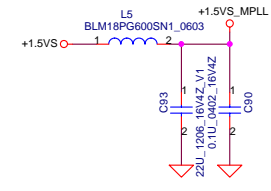
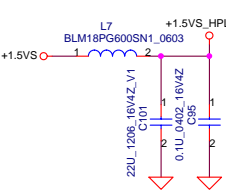
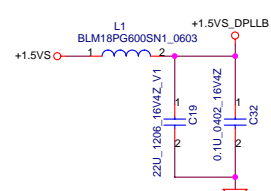
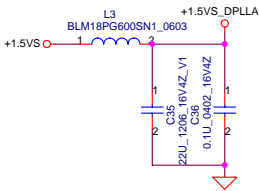


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Compal Electronics, Inc.			
Alviso(3 of 4)			
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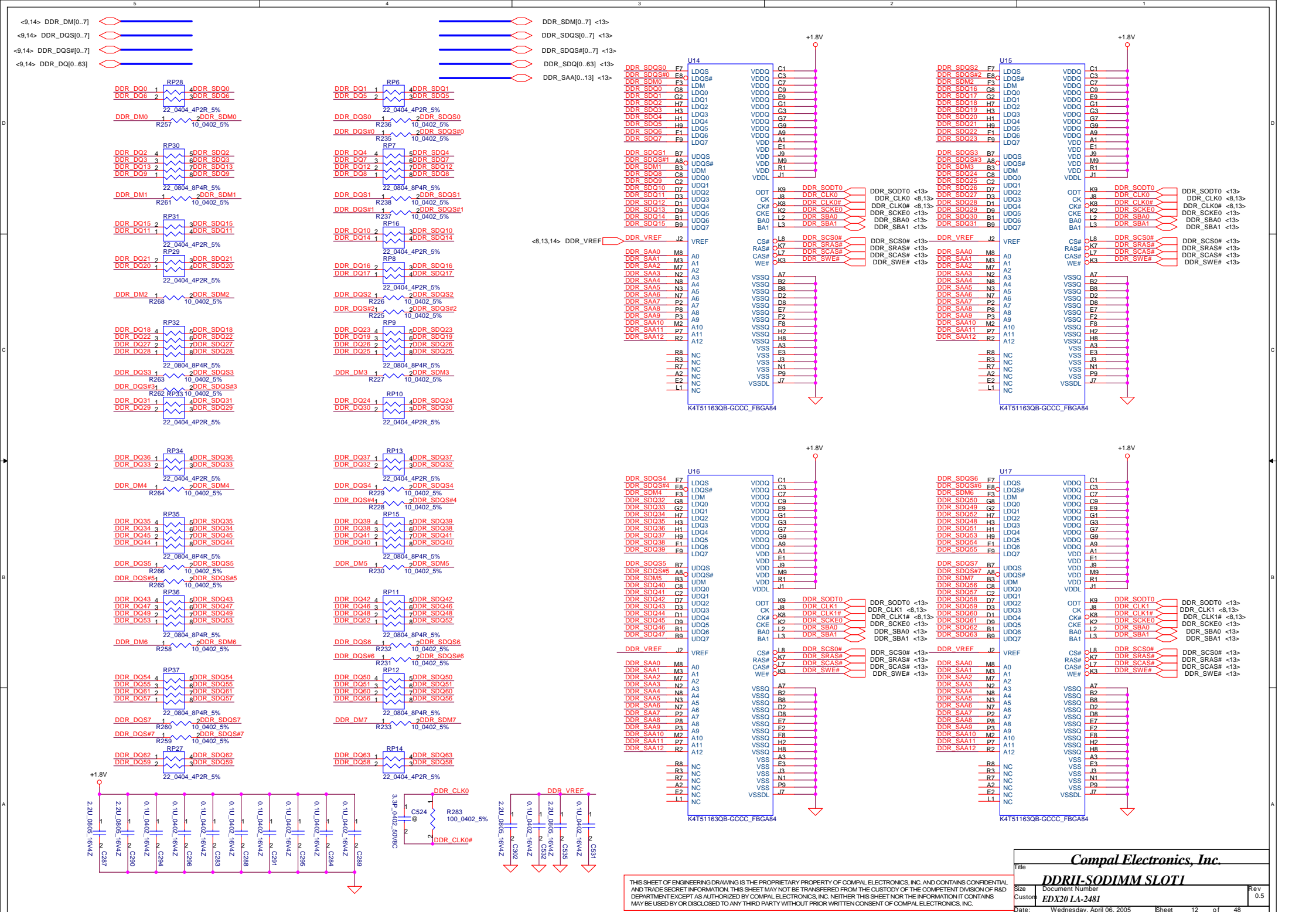


Note : All VCCSM pin shorted internally.



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Compal Electronics, Inc.		
Alviso (4 of 4)		
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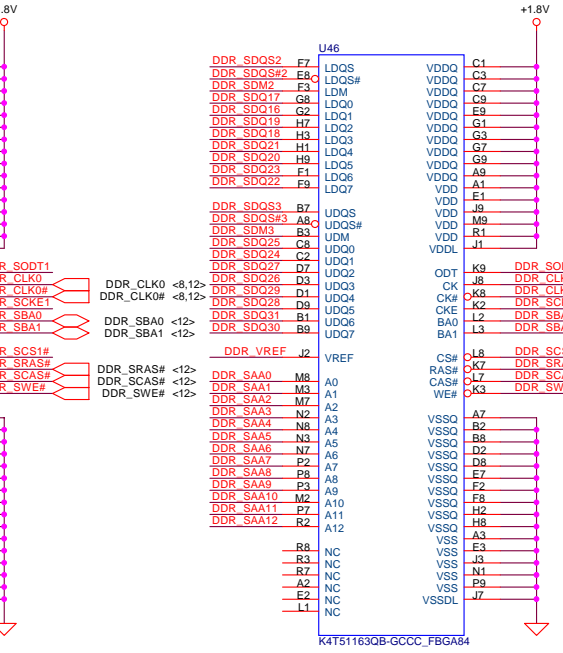
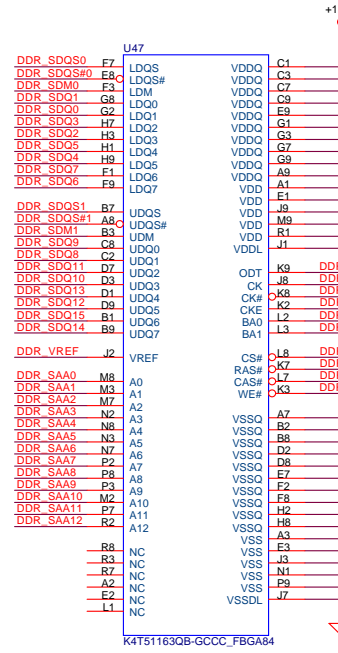
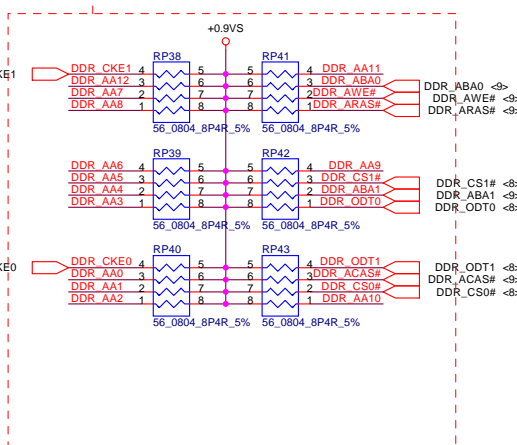
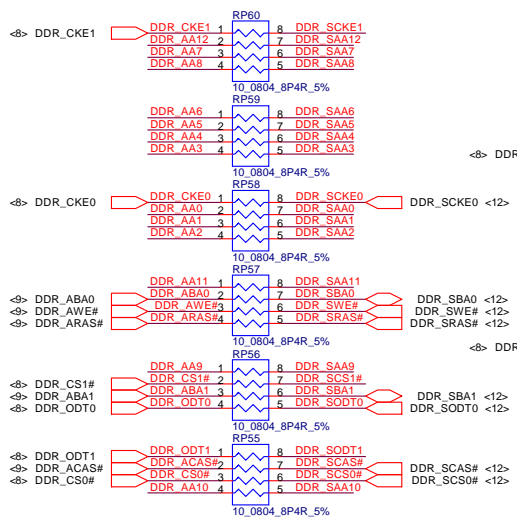
Compal Electronics, Inc.

DDR1-SODIMM SLOT1






File: _____
 Size: _____
 Customer: **EDX20 LA-2481**
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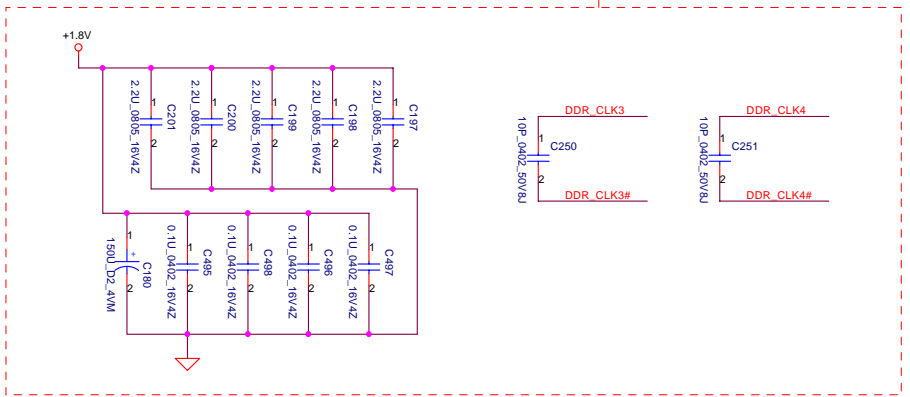
Layout Note:
Place these resistor
closely JP33,all
trace length<750 mil



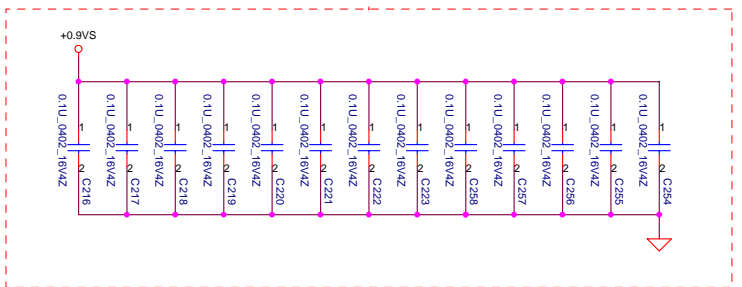
Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9V_DDR_V

<9,12> DDR_DQS#[0..7] 
 <9,12> DDR_DQ[0..63] 
 <9,12> DDR_DM[0..7] 
 <9,12> DDR_DQS#[0..7] 
 <9,12> DDR_BA[0..13] 

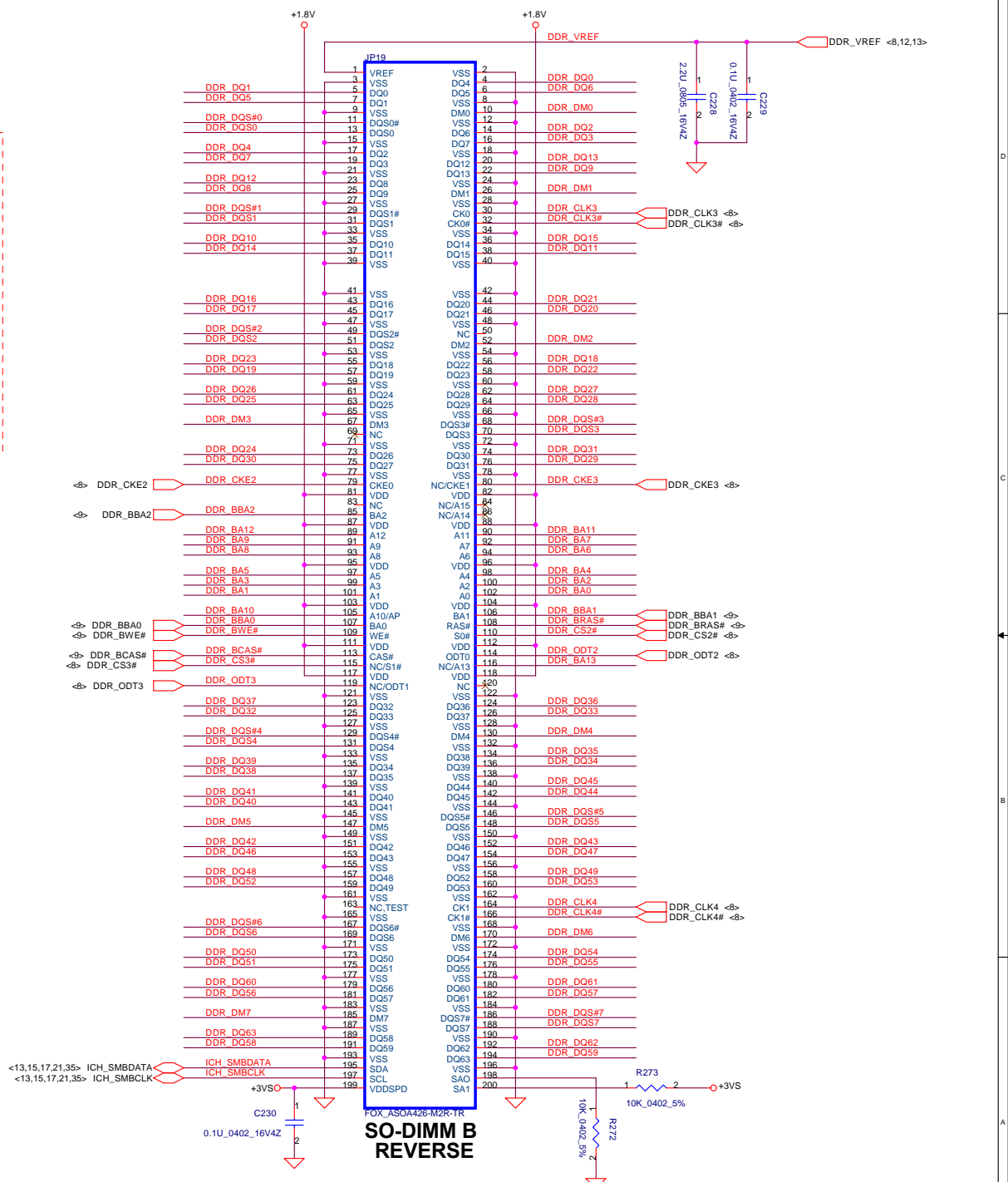
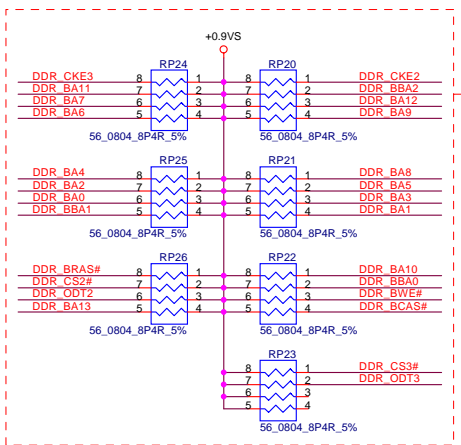
Layout Note:
Place near JP33



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V_DDR_VTT



Layout Note:
Place these resistor closely JP33, all trace length <750 mil



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Compal Electronics, Inc.
DDR1-SODIMM SLOT2

Title		
Size	Document Number	Rev
Customer	EDX20 LA-2481	0.5
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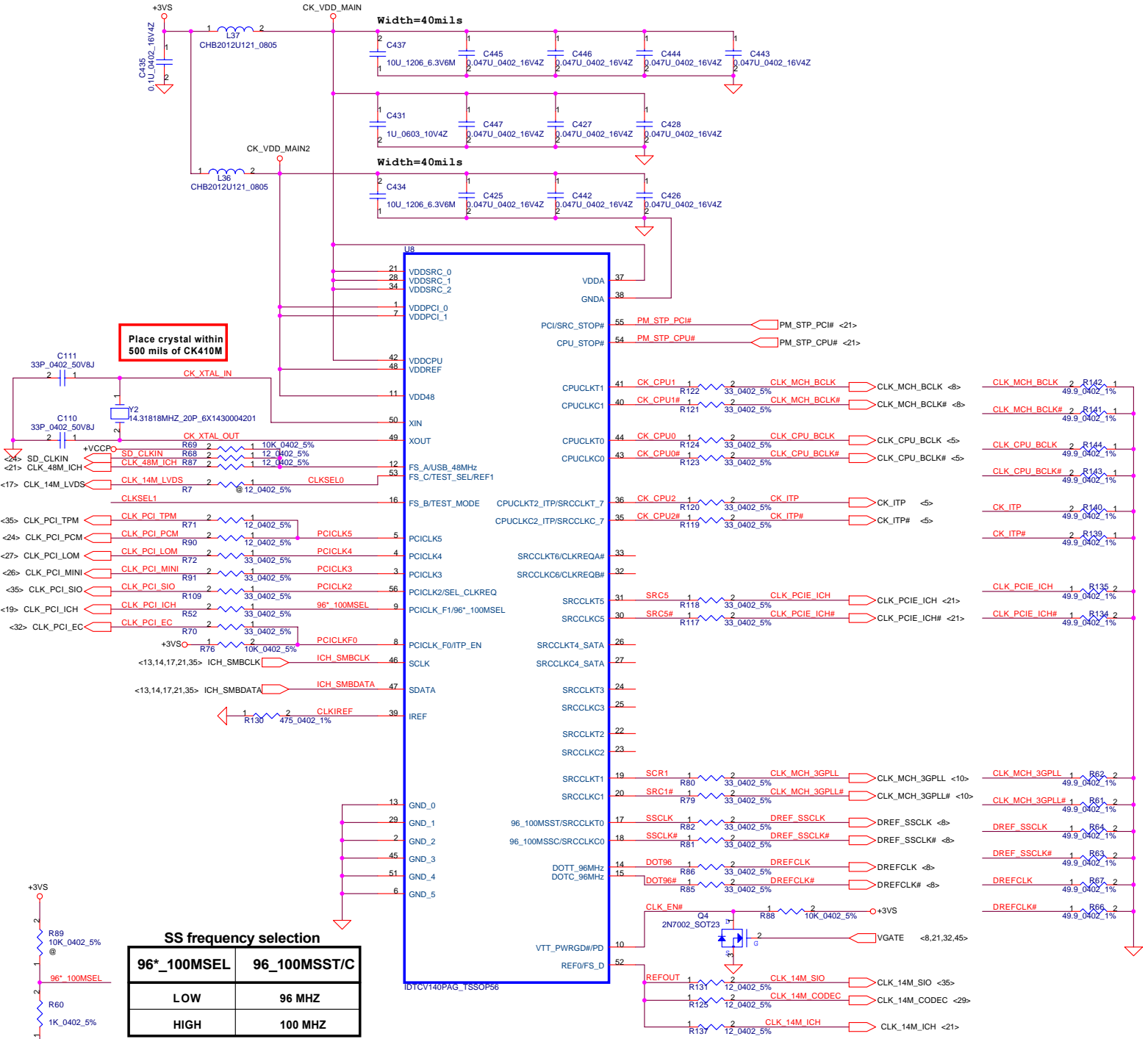
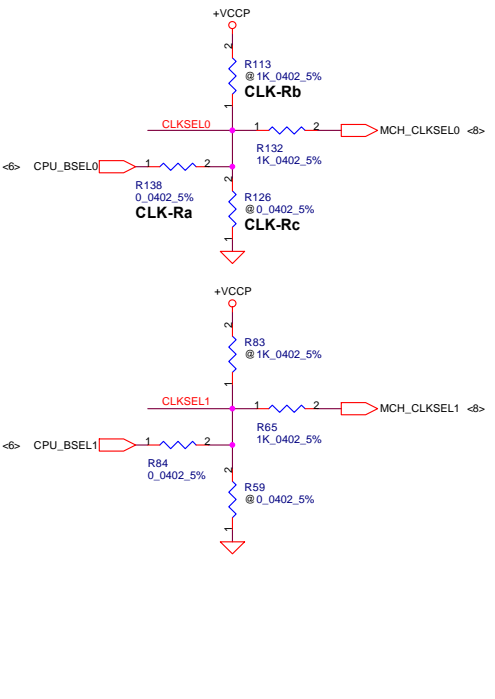
Dothan A step

FSC	FSB	FSA	CPU	SRC	PCI
CLKSEL0	CLKSEL1	CLKSEL2	MHz	MHz	MHz
1	0	1	133	100	33.3
0	0	1	100	100	33.3

Dothan B step

FSC	FSB	FSA	CPU	SRC	PCI
CLKSEL0	CLKSEL1	CLKSEL2	MHz	MHz	MHz
0	0	1	133	100	33.3
1	0	1	100	100	33.3

CPU Type	CLK-Ra	CLK-Rb	CLK-Rc
Dothan-A PSB400	OPEN	OPEN	0 Ohm
Dothan-A PSB533	OPEN	1K Ohm	OPEN
Dothan-B	0 Ohm	OPEN	OPEN



SS frequency selection

96*_100MSEL	96_100MSST/C
LOW	96 MHZ
HIGH	100 MHZ

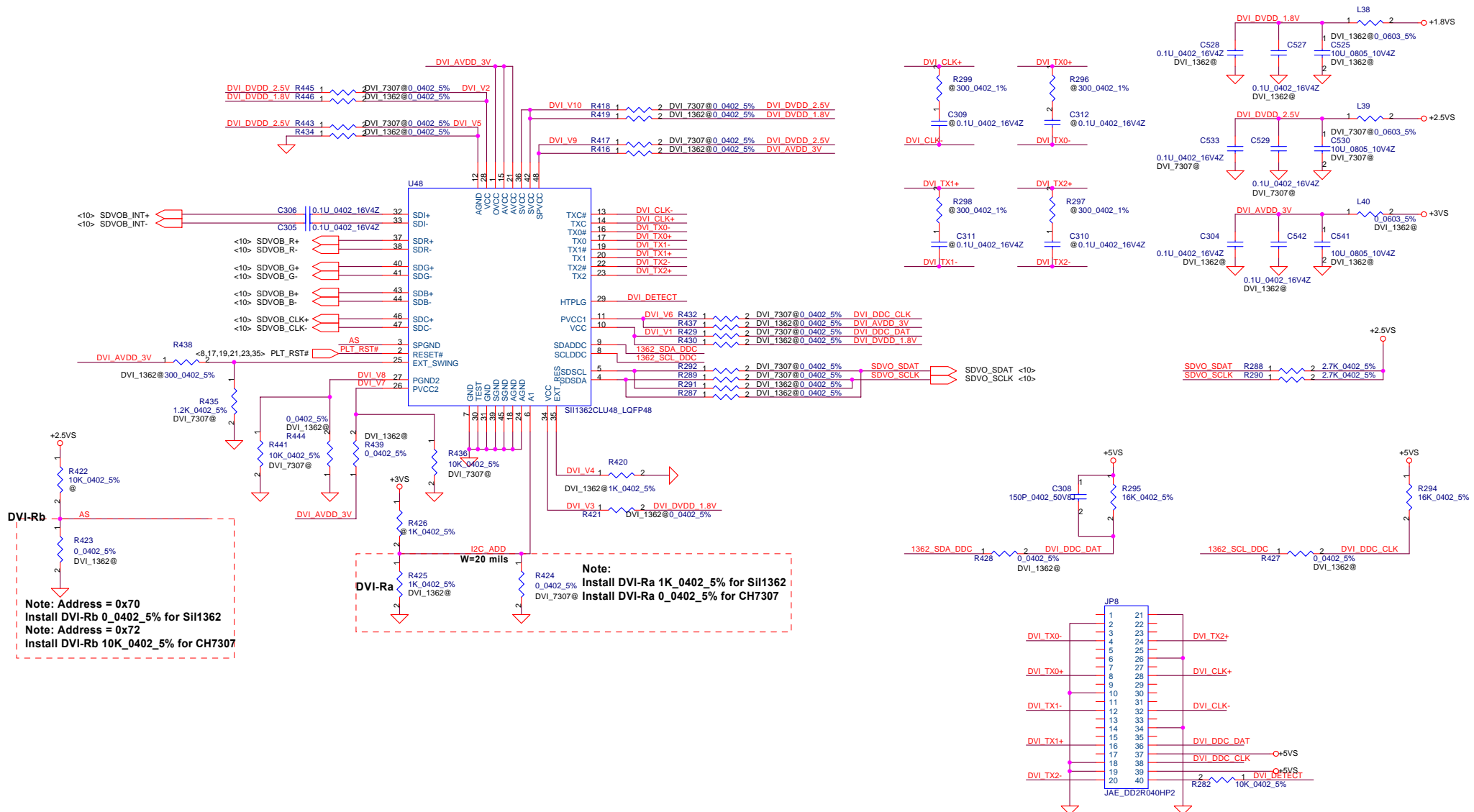
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Compal Electronics, Inc.

Clock Generator

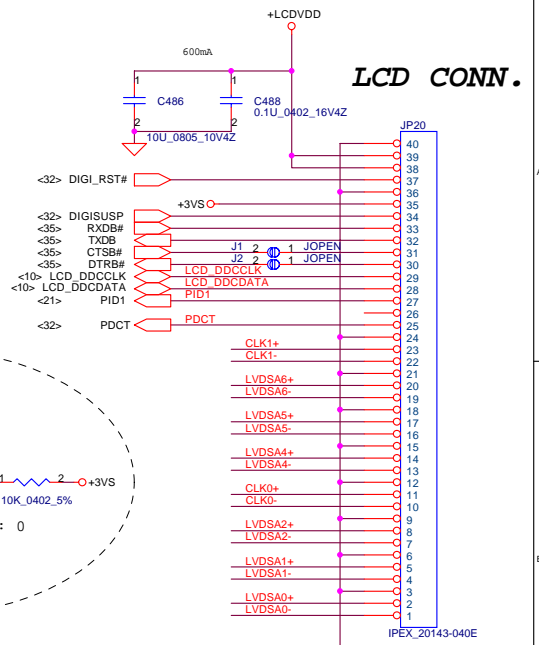
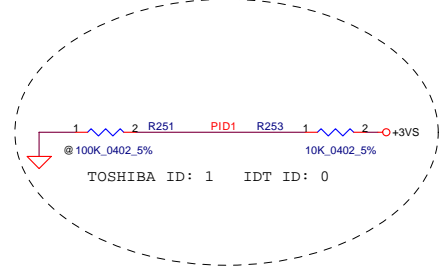
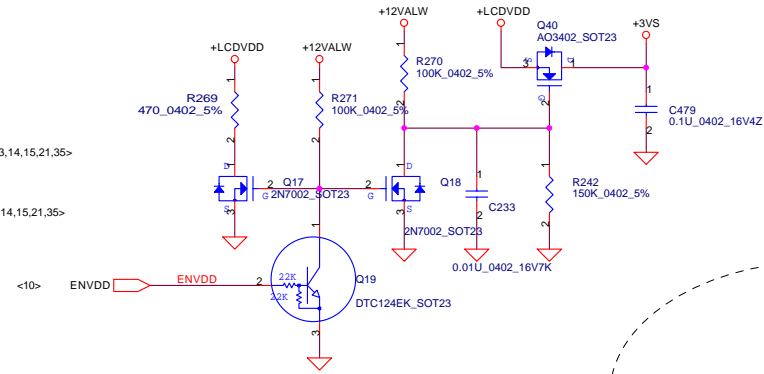
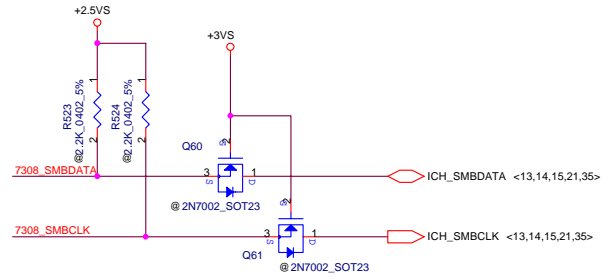
Title		
Size	Document Number	Rev
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DVI CONTROLLER

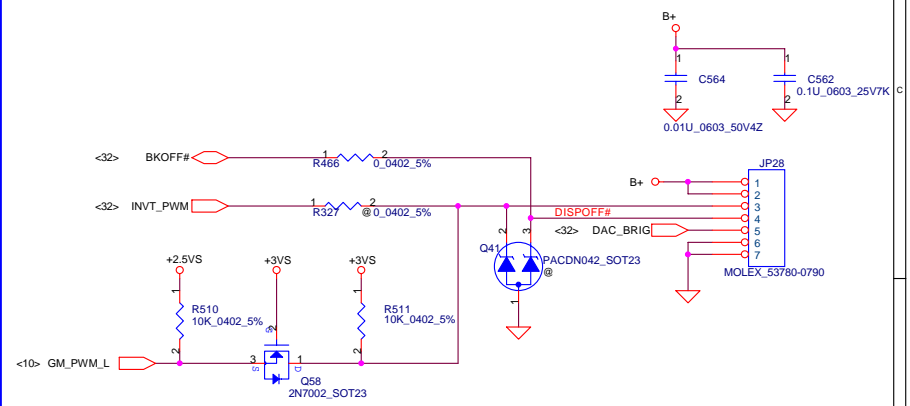
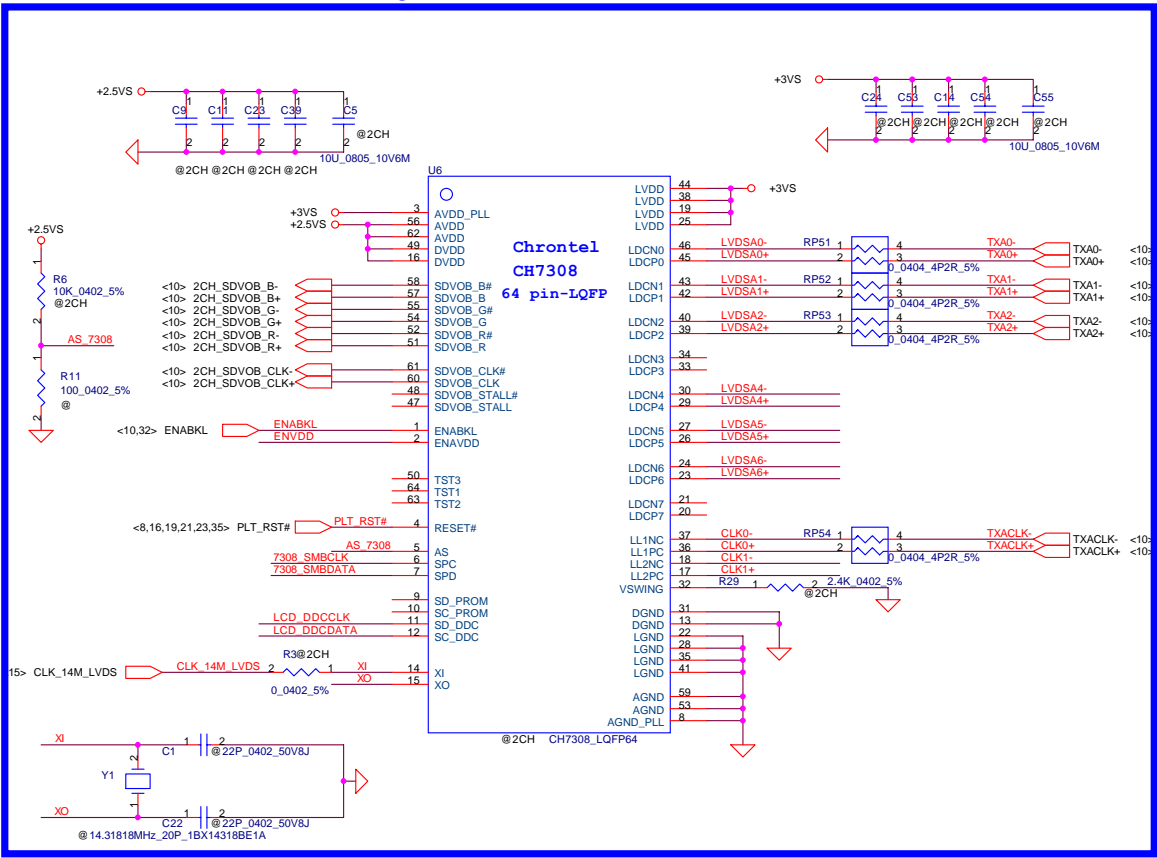


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Compal Electronics, Inc.		
DVI CH7307/SII1362		
Title	Document Number	Rev
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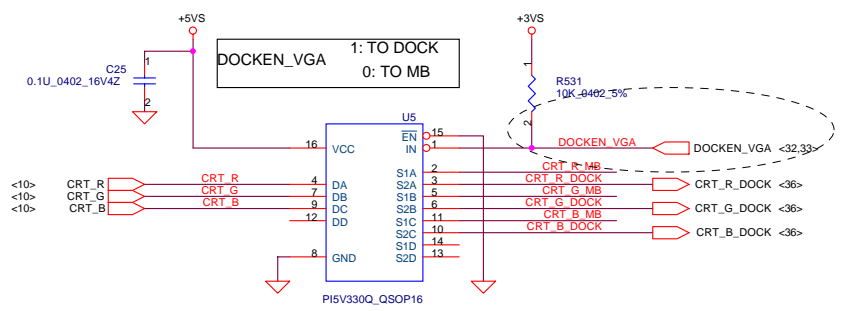
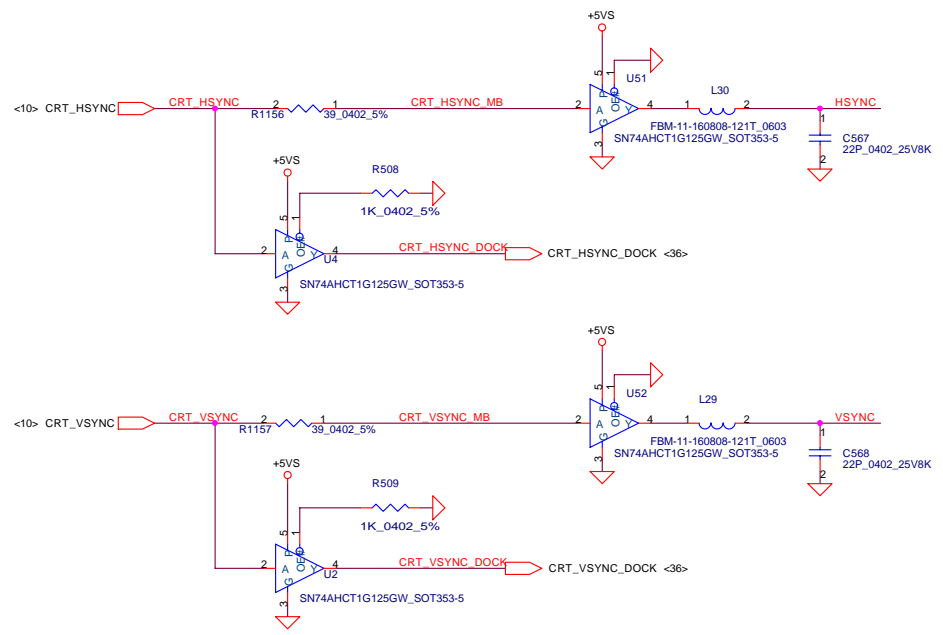
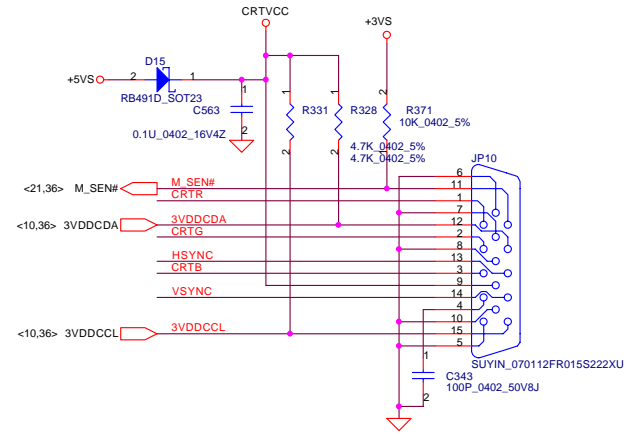
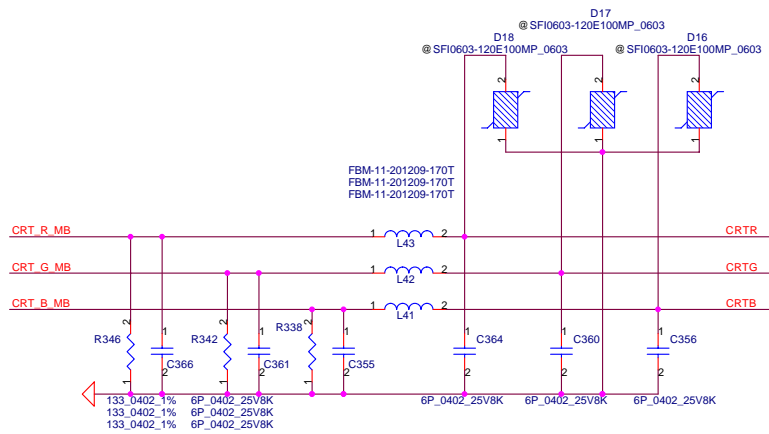


@2CH



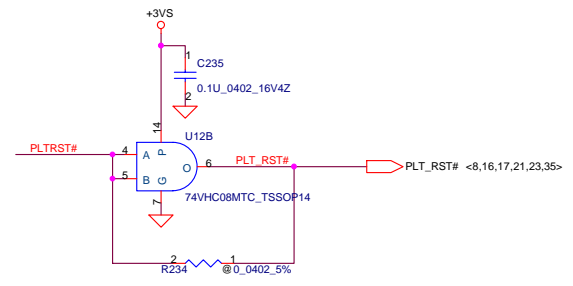
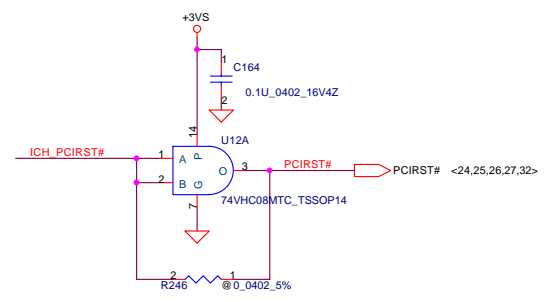
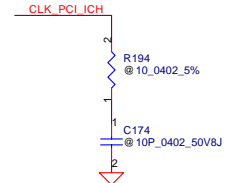
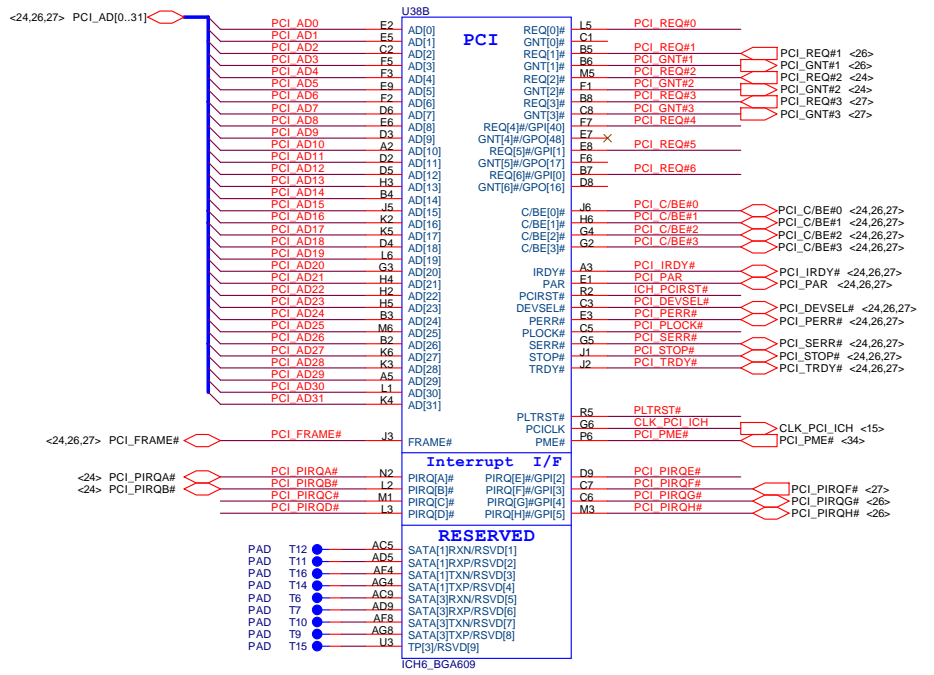
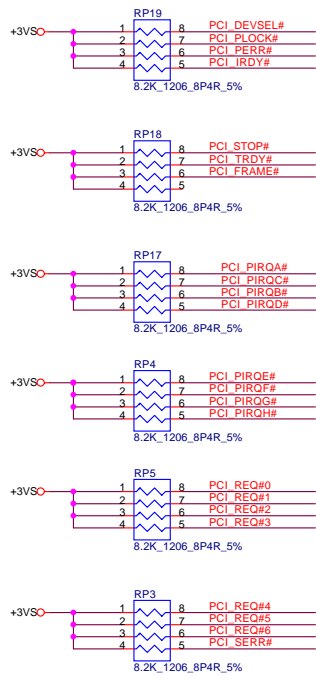
Compal Electronics, Inc.		
Title	LCD Conn&Inverter	
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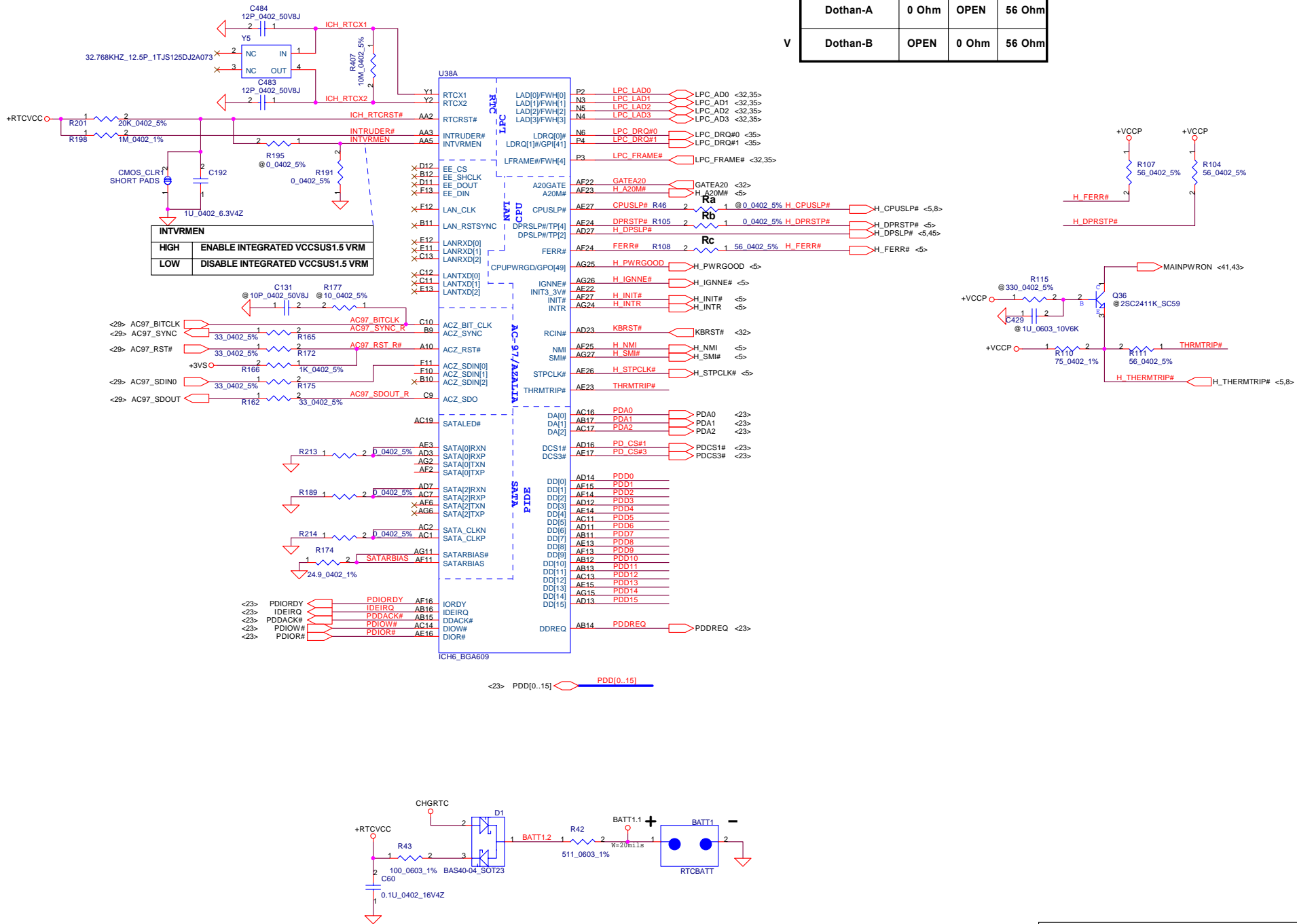
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Compal Electronics, Inc. Title LCD Connector & CRT		
Size	Document Number	Rev
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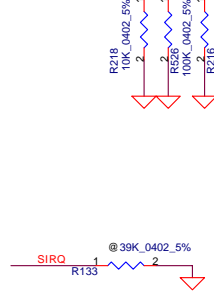
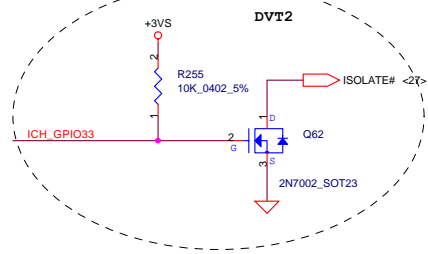
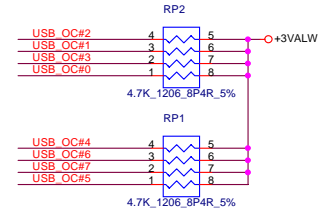
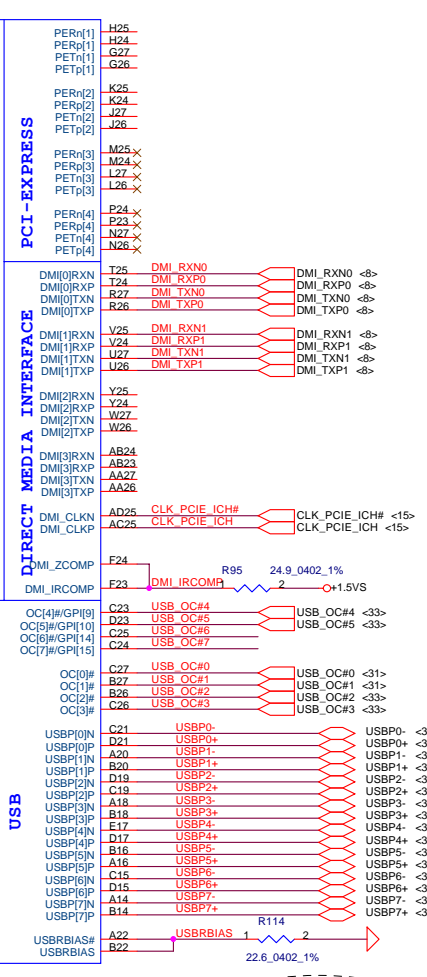
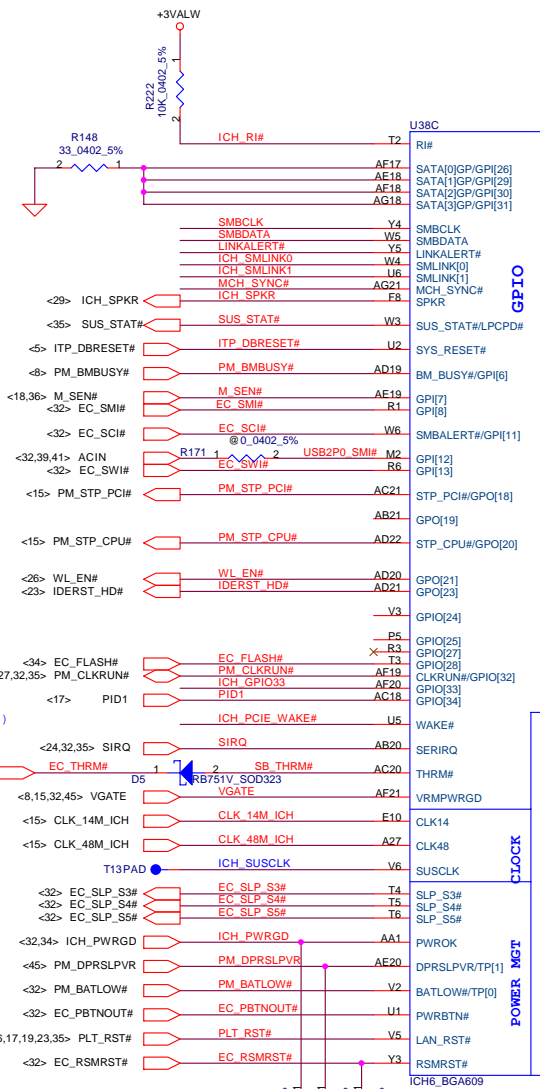
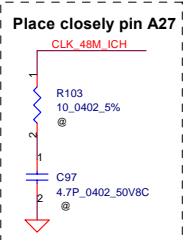
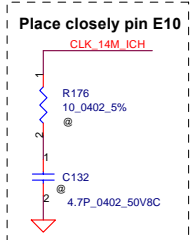
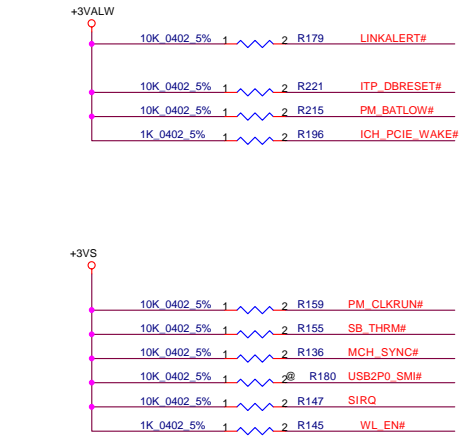
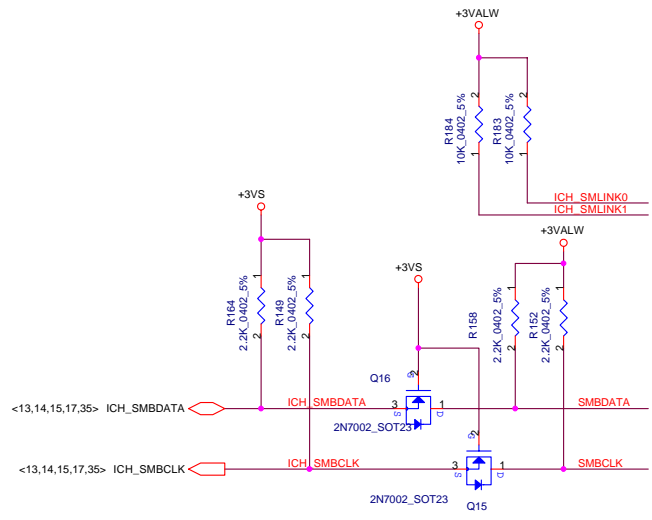


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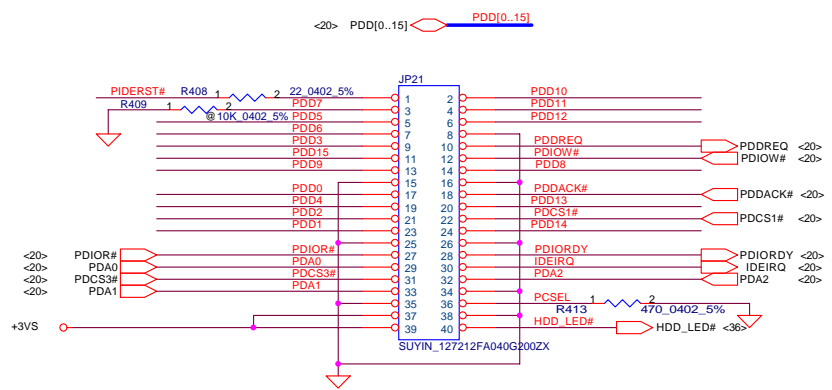
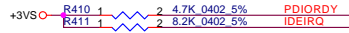
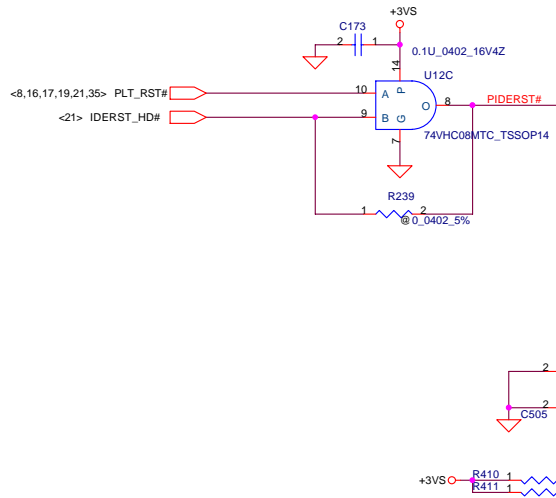
CPU Type	Ra	Rb	Rc
Dothan-A	0 Ohm	OPEN	56 Ohm
Dothan-B	OPEN	0 Ohm	56 Ohm



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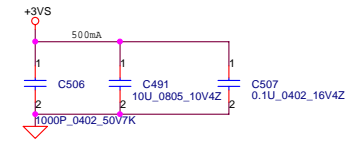


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Place caps. near HDD CONN.

Layout Note: +VPHDD trace width 60 mil



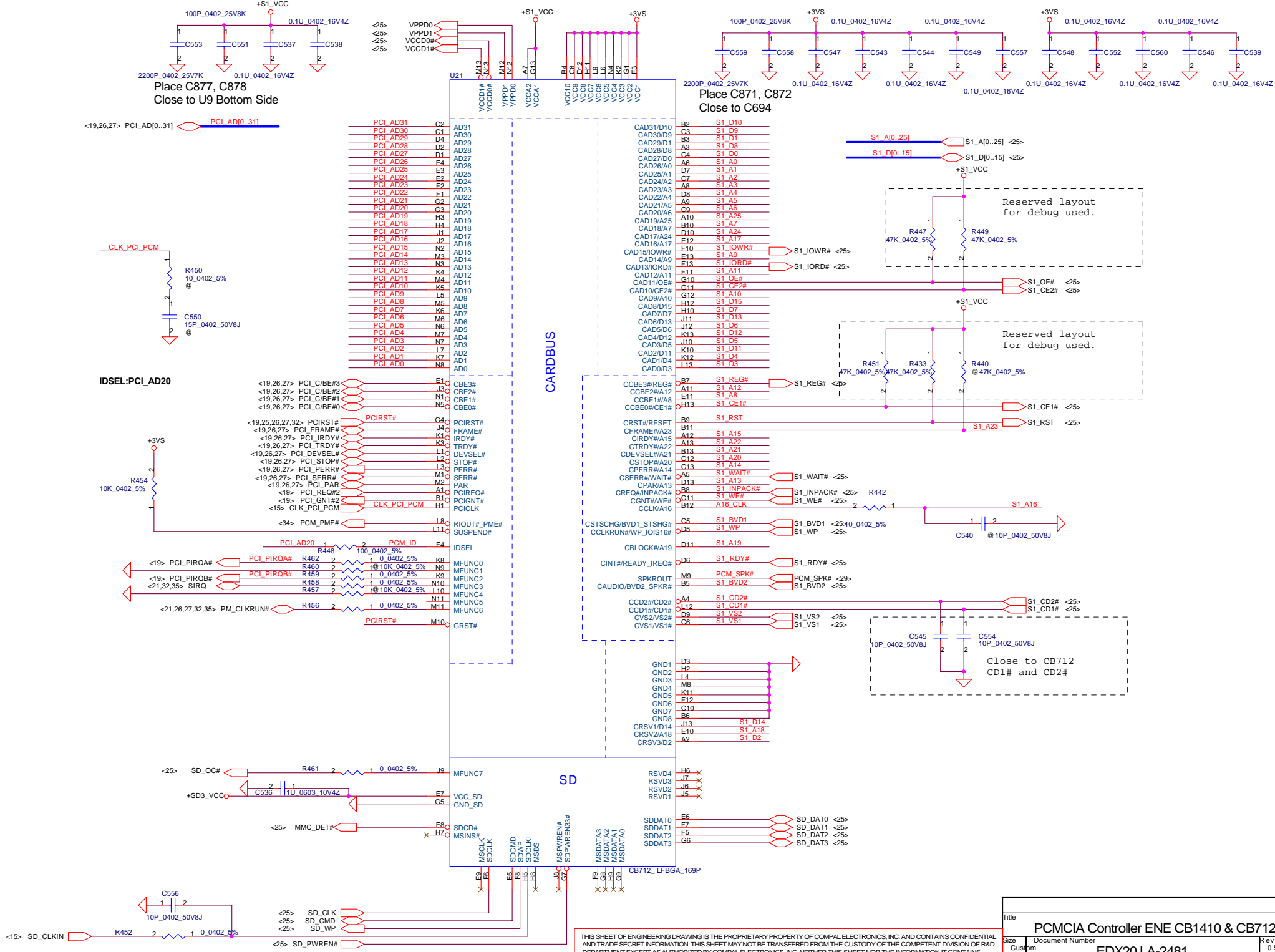
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Compal Electronics, Inc.		
Title ICH4-M PIDE&ICH Pull-up		
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Place C877, C878
Close to U9 Bottom Side

Place C871, C872
Close to C694

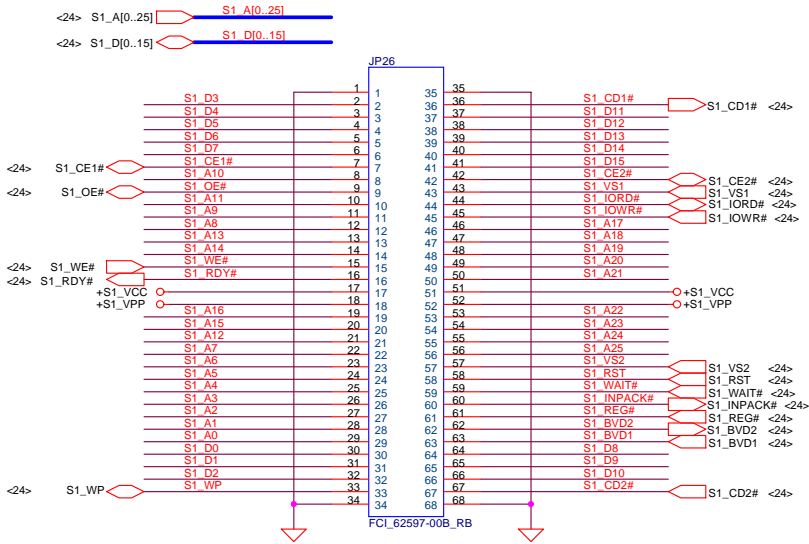
Close to CB712
CD1# and CD2#



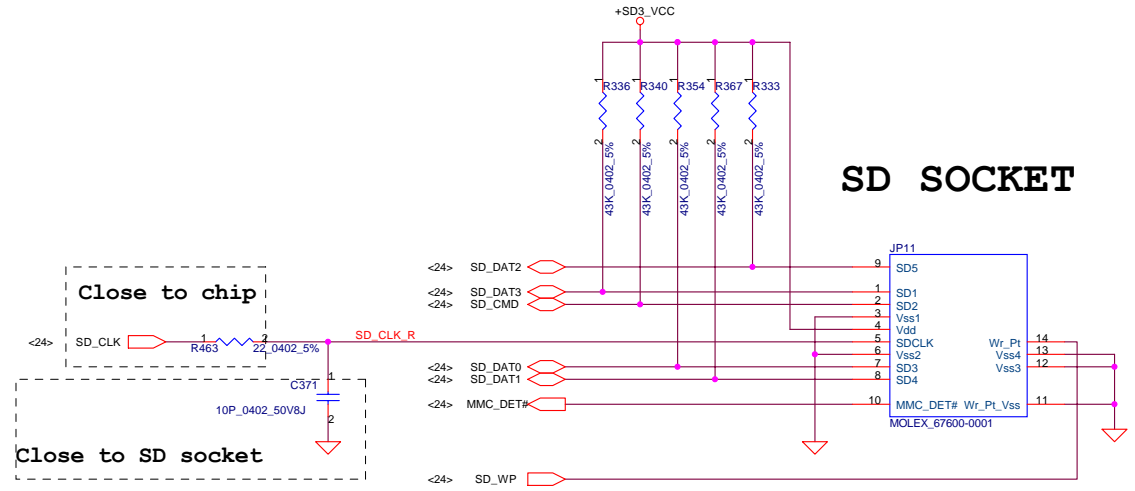
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Title		PCMCIA Controller ENE CB1410 & CB712	
Size	Document Number	EDX20 LA-2481	
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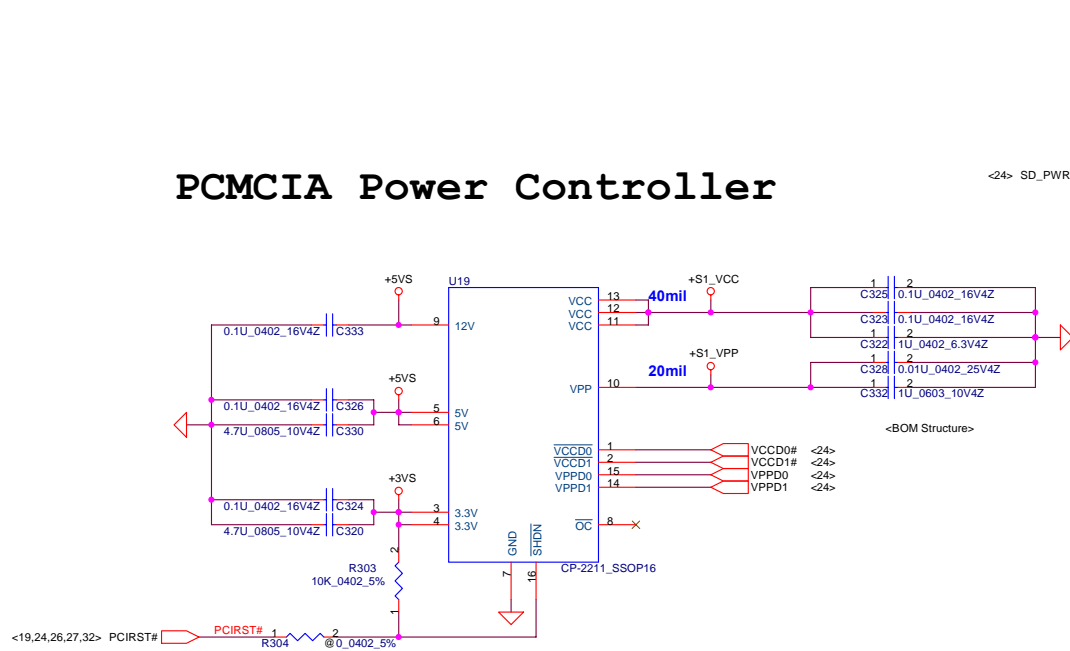
CARDBUS SOCKET



SD SOCKET



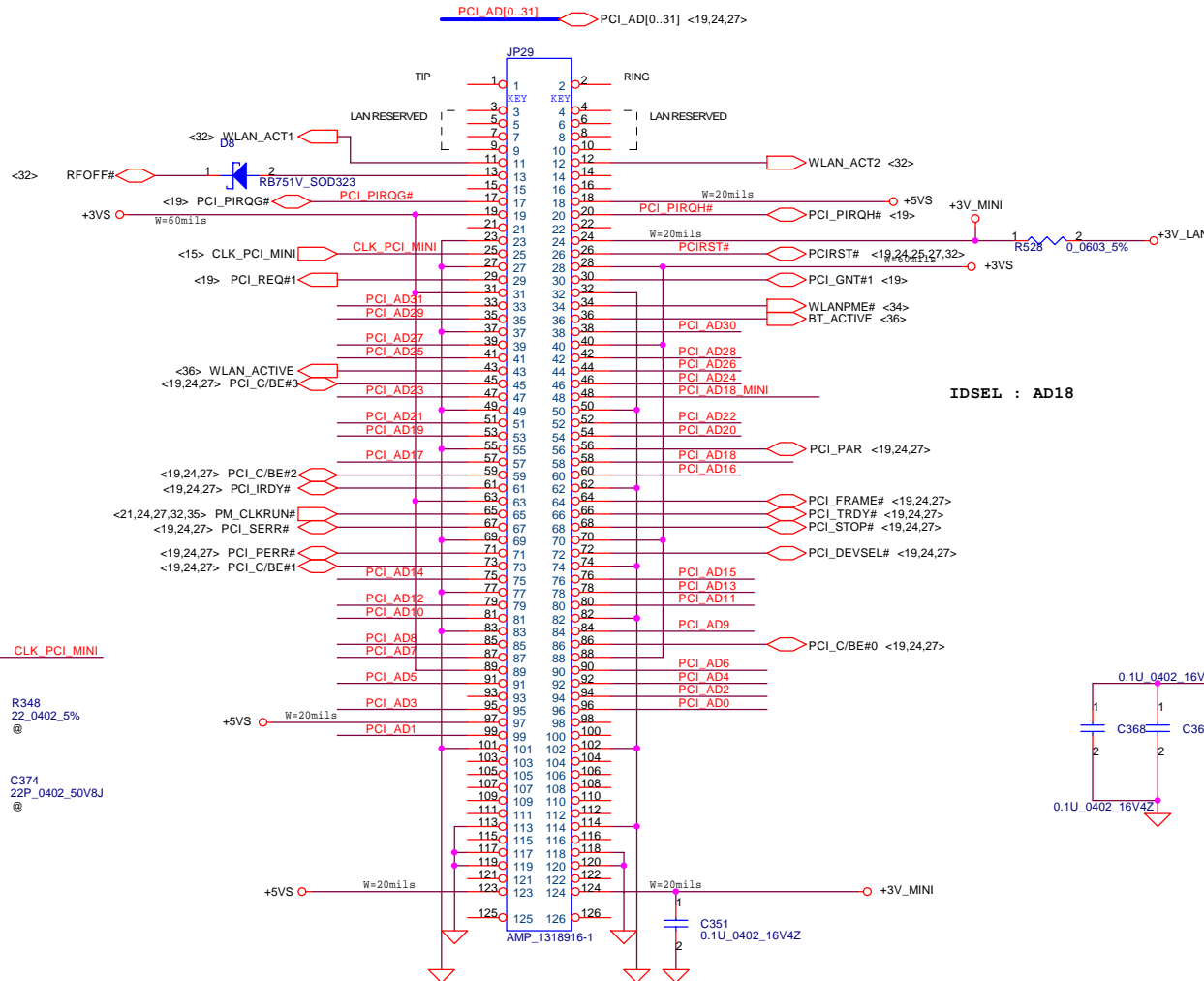
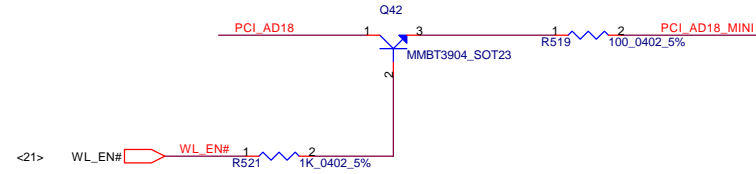
PCMCIA Power Controller



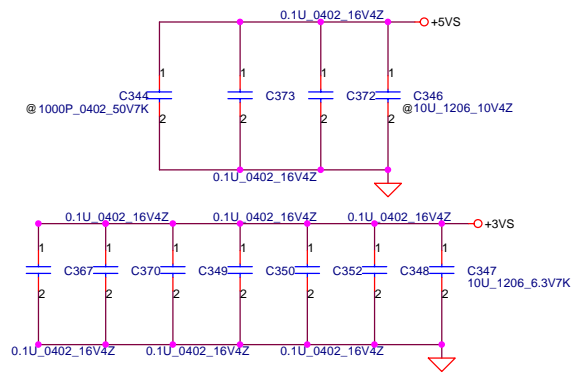
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Compal Electronics, Inc.		
Title: PCMCIA/SD SOCKET		
Size: EDX20 LA-2481	Document Number: EDX20 LA-2481	Rev: 0.5
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-+5VS limited to 100mA
 -+3.3VAUX--Normal:
 375mA, D3: 200mA,
 -+3VS_MINIPCI: 500mA
 for wireless LAN



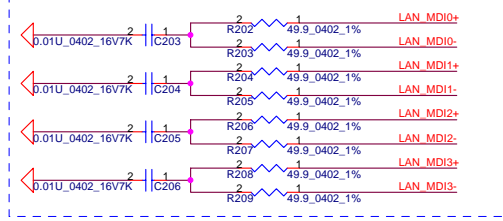
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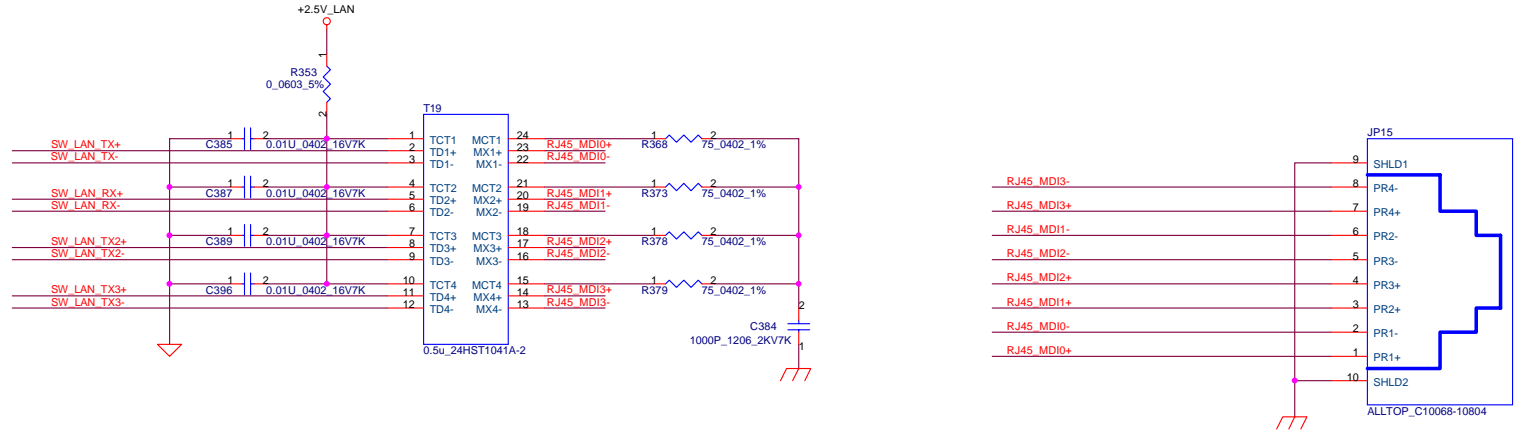
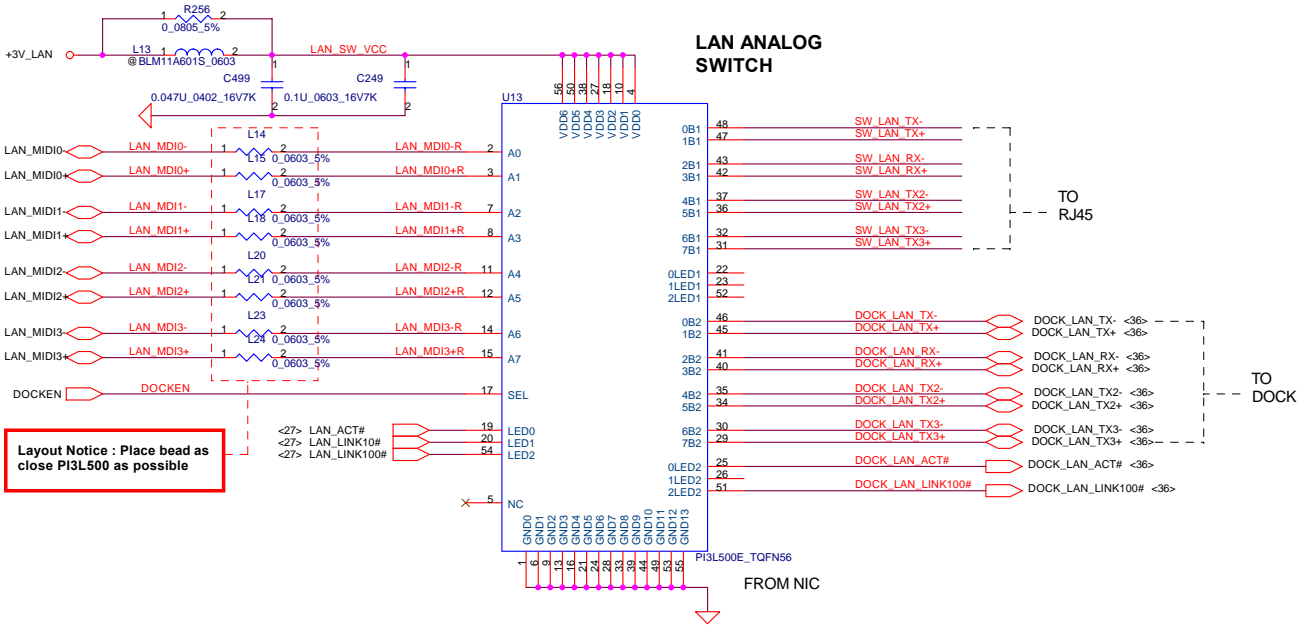
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Compal Electronics, Inc.		
Title MiniPCI TYPEIII Slot		
Size	Document Number EDX20 LA-2481	Rev 0.5
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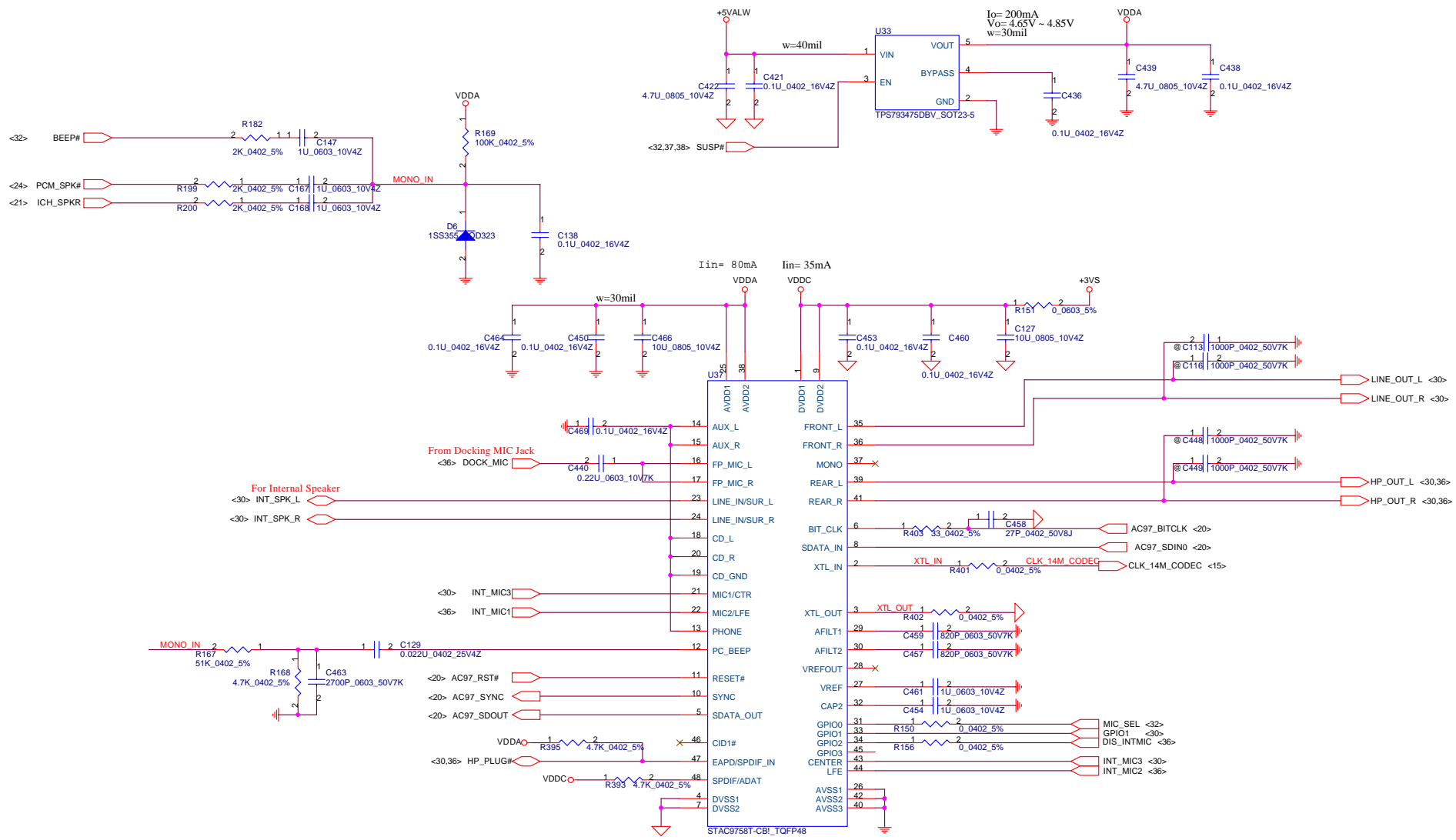
Close to Chip side



DOCKEN 1: TO DOCK
0: TO RJ45



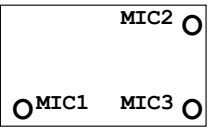
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DIS_INTMIC (PIN 34)	INT_MIC1 INT_MIC2 INT_MIC3	DOCK_MIC (PIN 16)
H	DISABLE	ENABLE
L	ENABLE	DISABLE

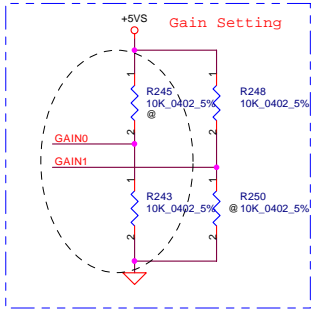
EAPD (PIN 47)	LINE_IN_L PIN23 LINE_IN_R PIN24
H	ENABLE
L	DISABLE

MIC_SEL (PIN 31)	INT_MIC1 (PIN 22)	INT_MIC2 (PIN 44)	INT_MIC3 (PIN 21,43)
L (Landscape)	ENABLE	DISABLE	ENABLE
H (Portrait)	DISABLE	ENABLE	ENABLE

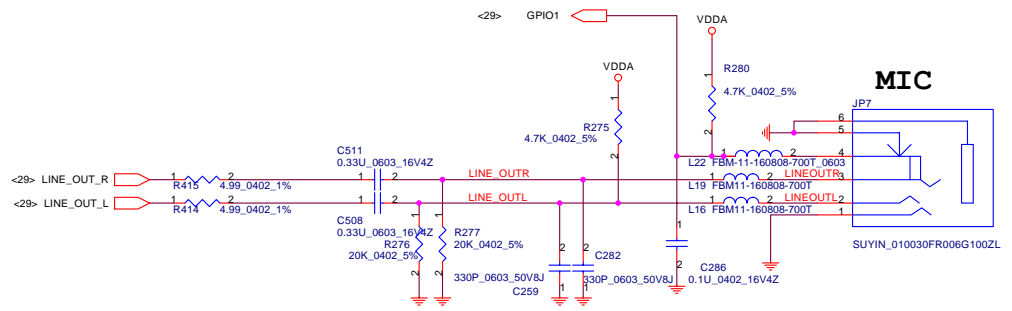
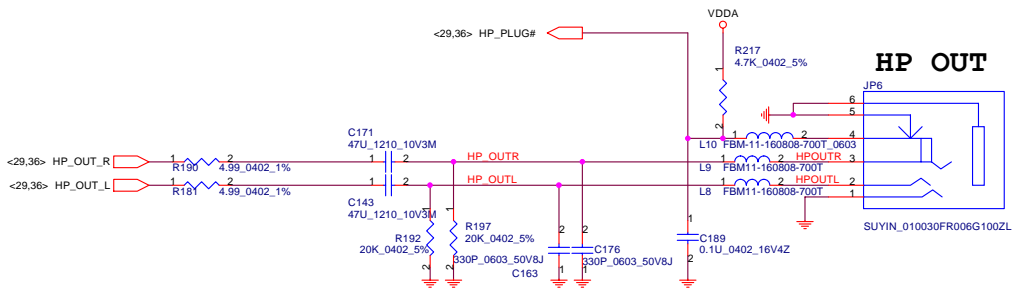
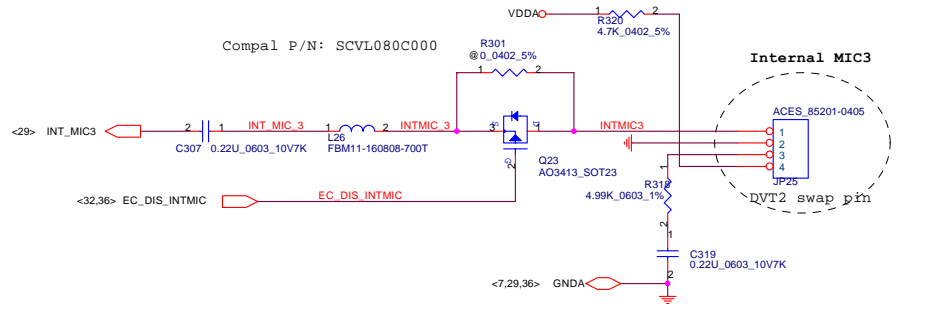
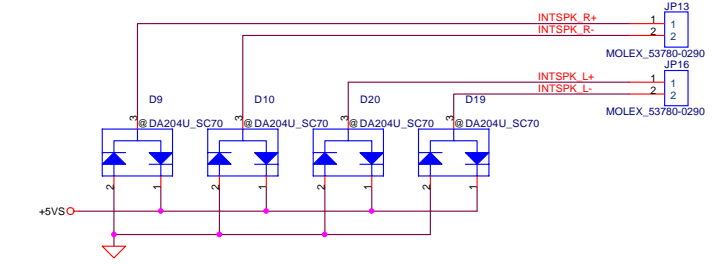
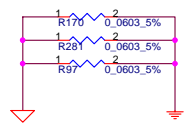
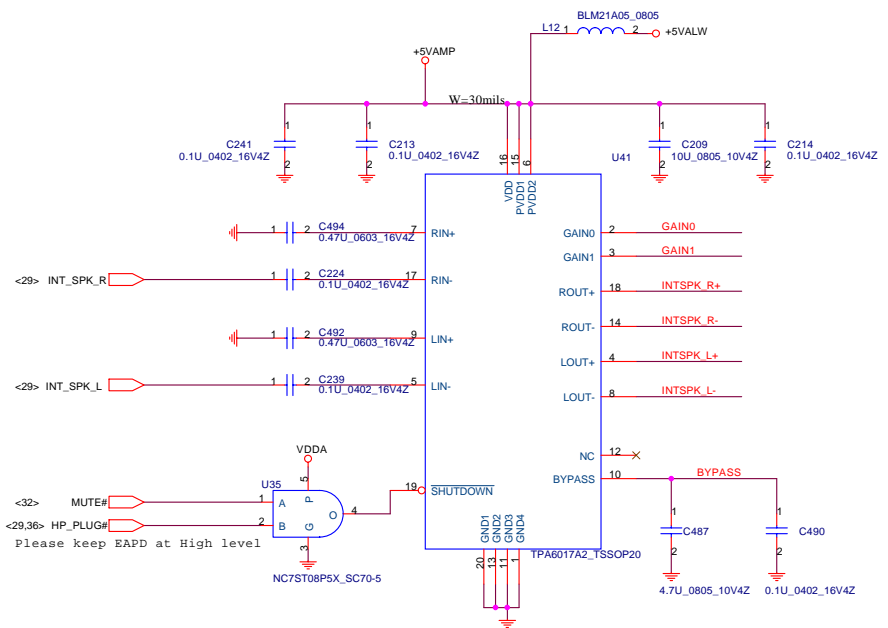


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Compal Electronics, Inc.
 Title: **AC97 STAC9758**
 Size: Document Number **EDX20 LA-2481** Rev: 0.5
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GAIN0	GAIN1	AV(inv)	INPUT IMPEDANCE
0	0	6dB	90K ohm
0	1	10dB	70K ohm
1	0	15.6dB	45K ohm
1	1	21.6dB	25K ohm



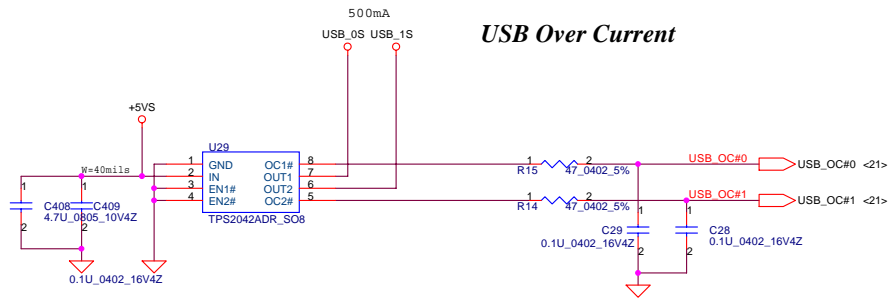
Compal Electronics, Inc.

Title AMP & Audio Jack

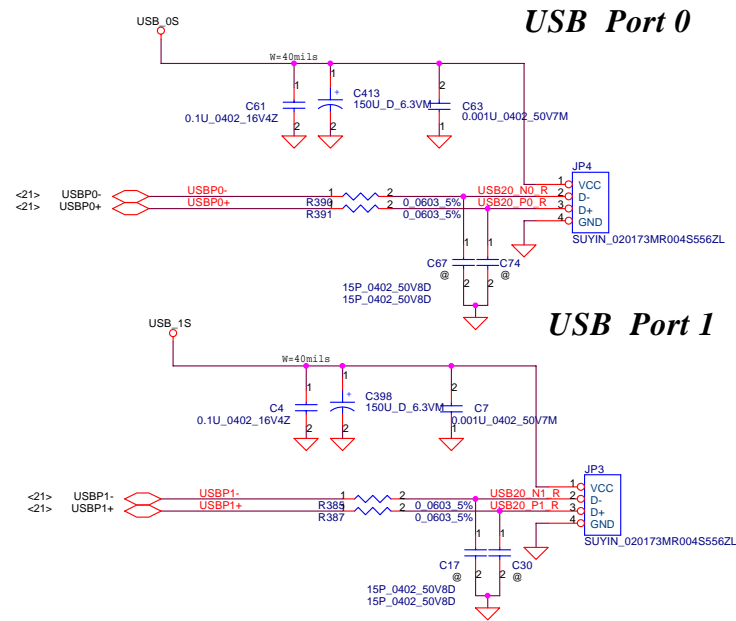
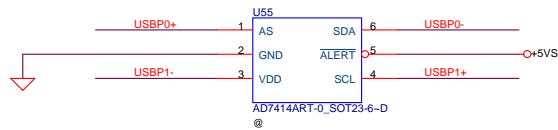
Document Number EDX20 LA-2481

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USB Over Current

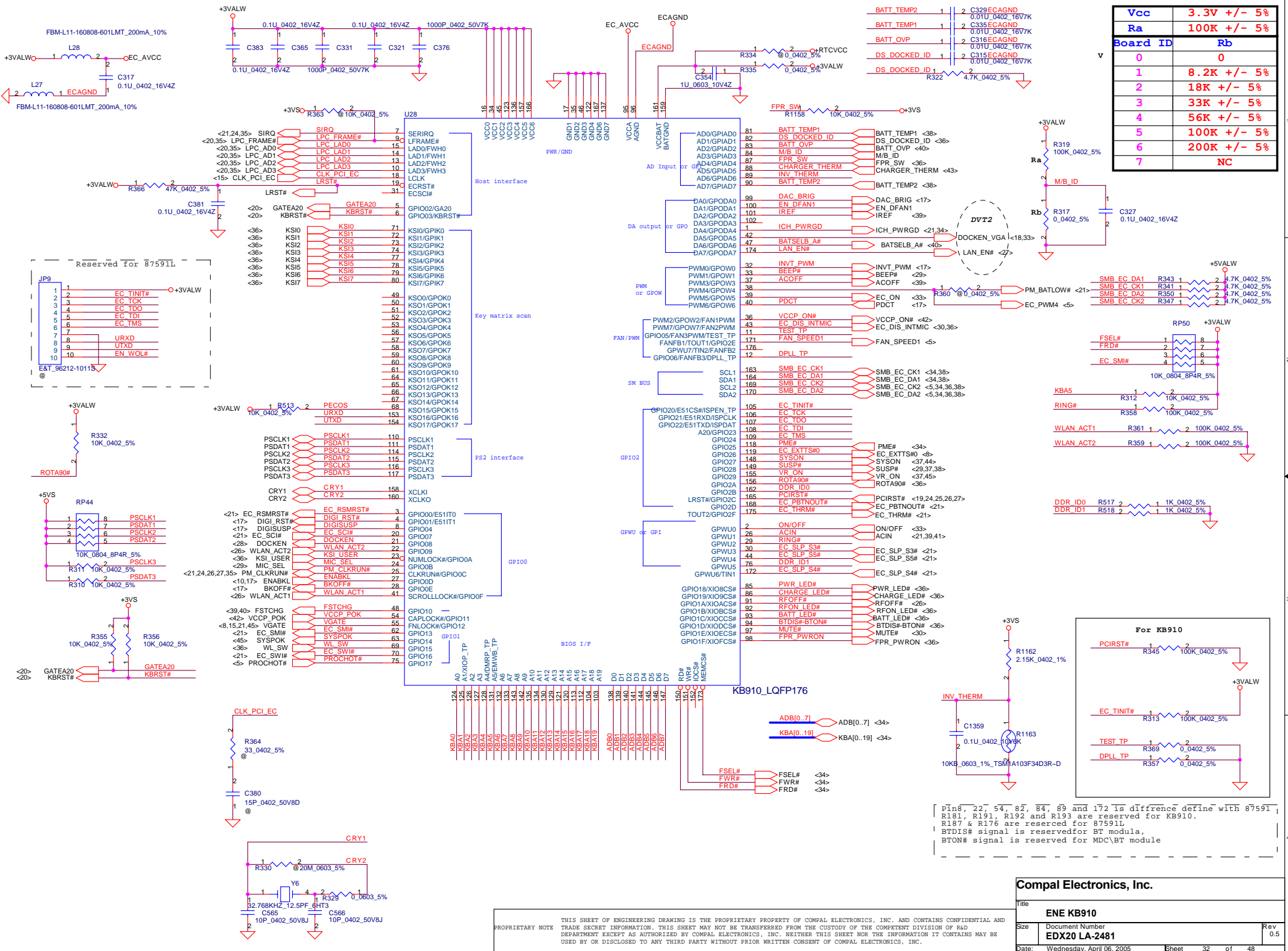


USB Port 0

USB Port 1

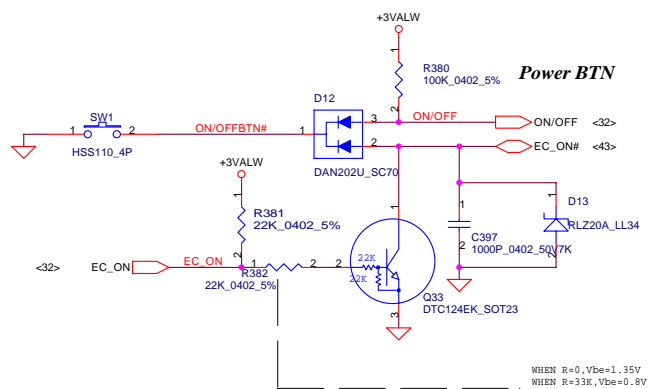
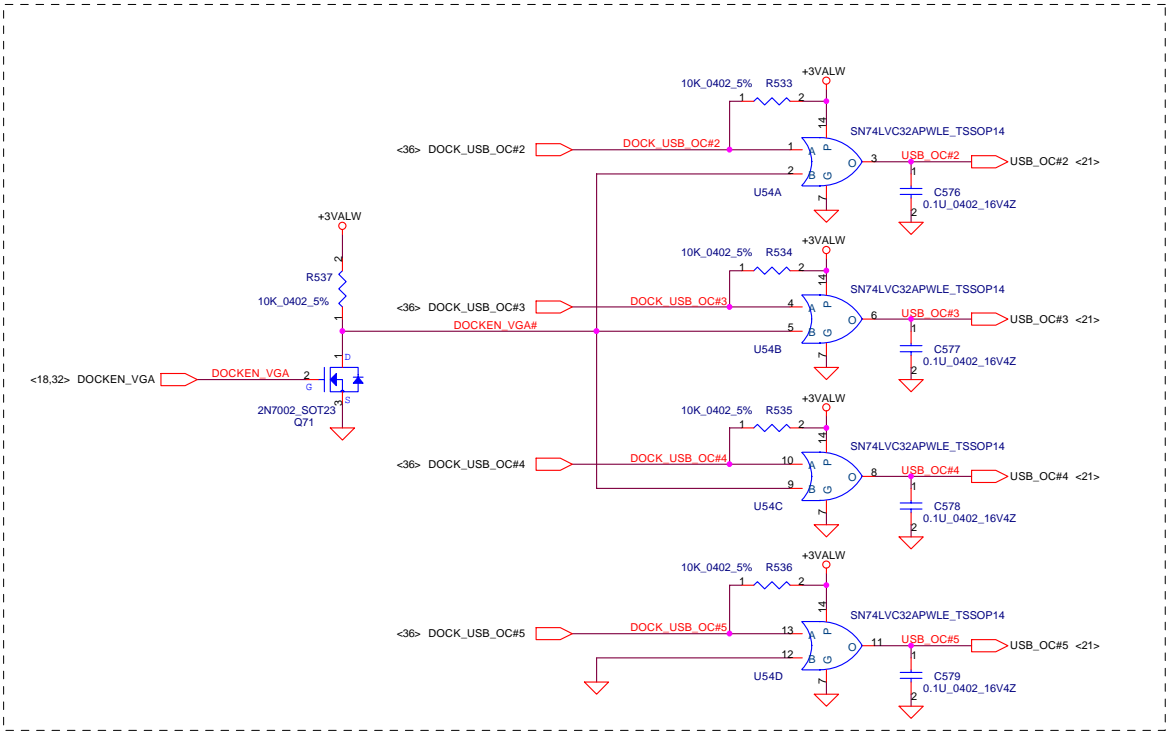
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Compal Electronics, Inc.		
Title		
USB Port x2		
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Pin8, 22, 54, 82, 84, 89 and 172 is difference define with 87591
 R181, R191, R192 and R193 are reserved for KB910.
 R187 & R176 are reserved for 87591L
 BTDIS# signal is reserved for BT module,
 BTON# signal is reserved for MDC/BT module

PVT

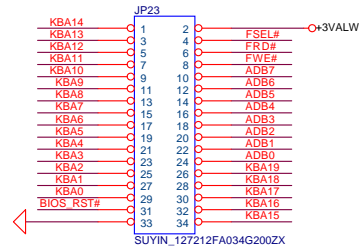
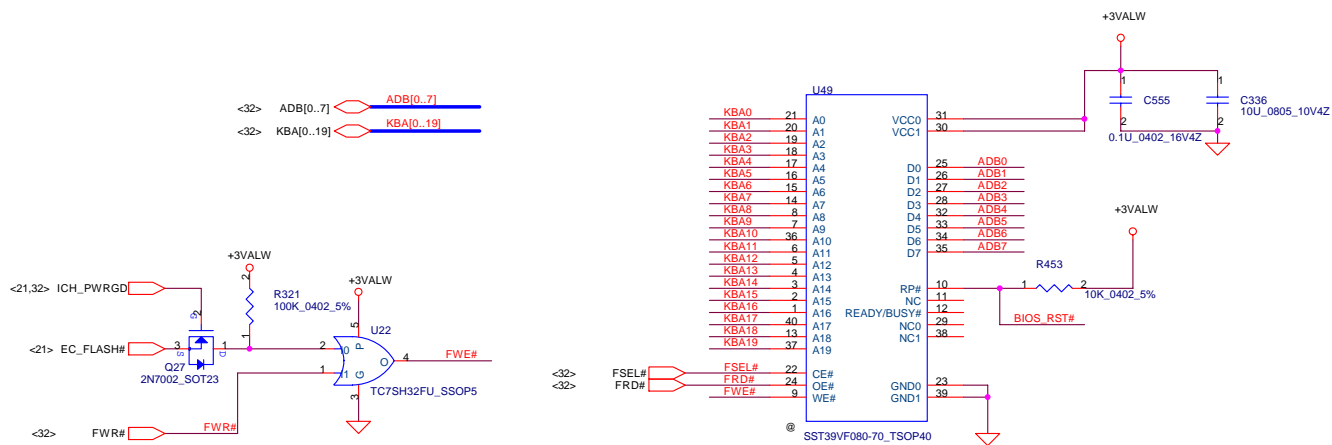
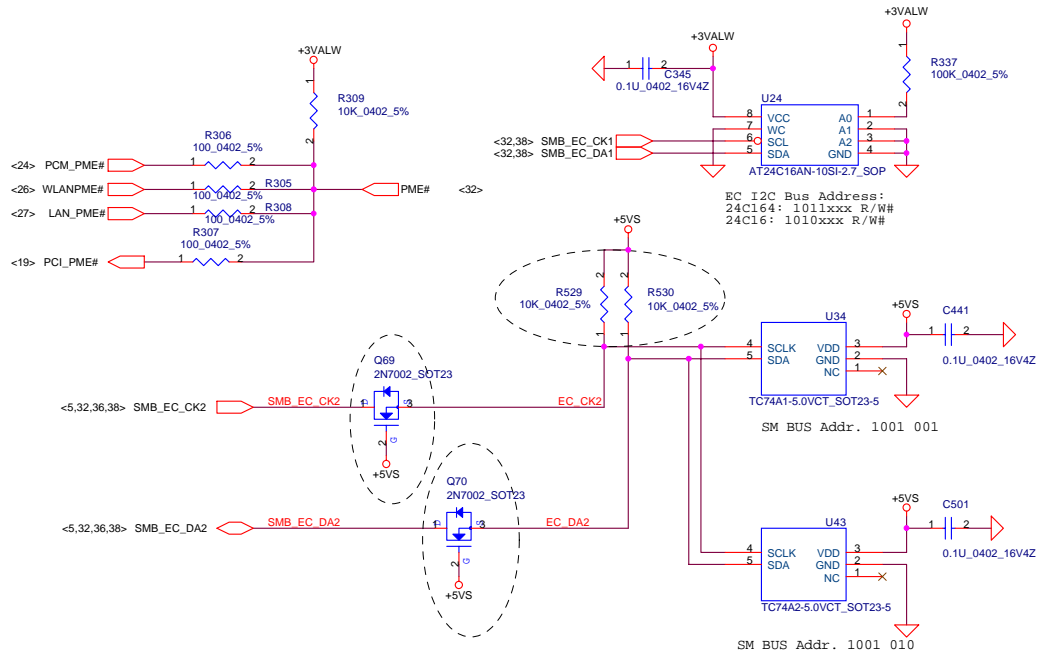


WHEN R=0, Vbe=1.35V
WHEN R=33K, Vbe=0.8V

Compal Electronics, Inc.		
Title		
Power ON/OFF SW		
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NM24C164 Address definition: 1 A2 A1# A0 B2 B1 B0 R/W#



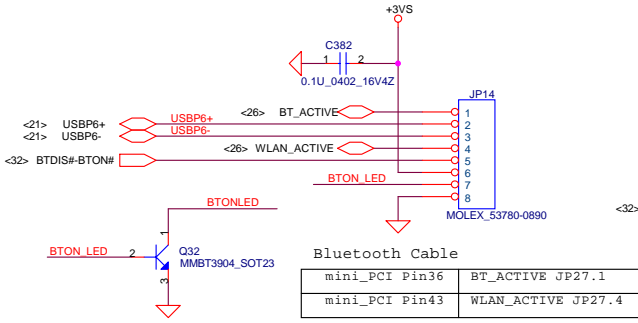
Alternative SA290080100 for U24

Compal Electronics, Inc.

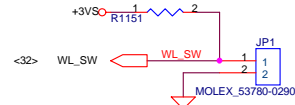
Title		
Thermal Sensor & BIOS		
Size	Document Number	Rev
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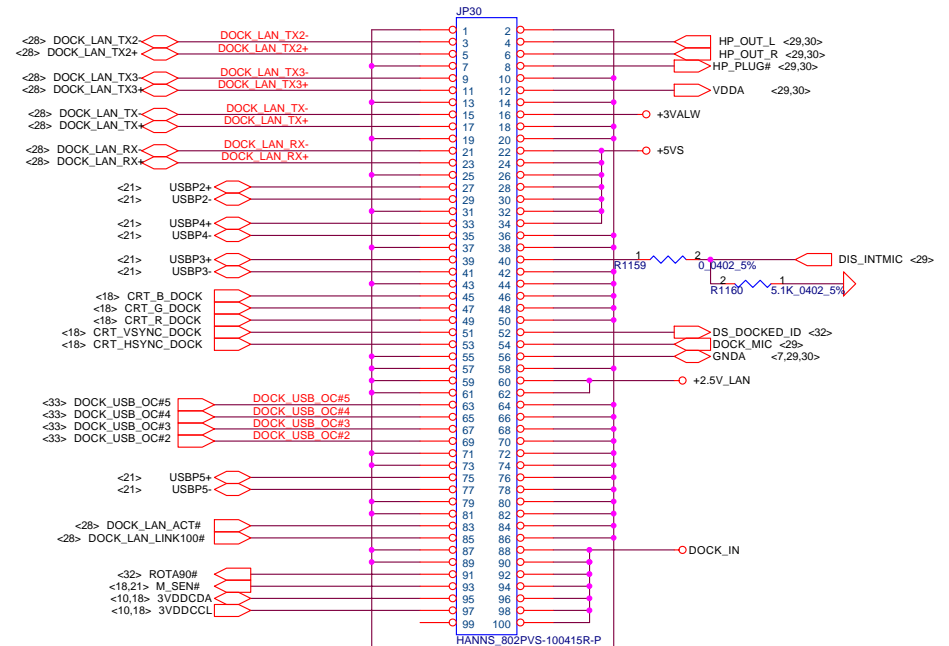
BT MODULE CONN



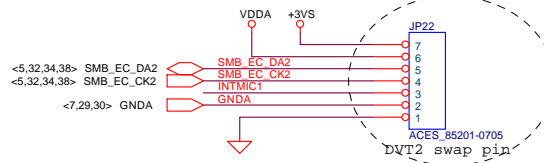
WIRELESS SW



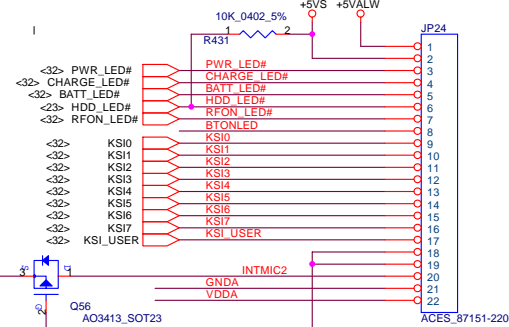
DOCKING BD.



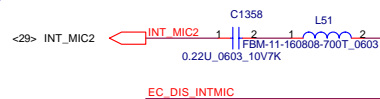
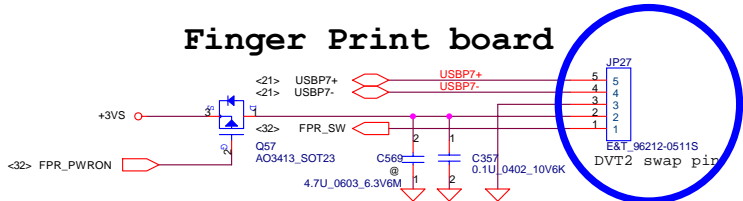
ALS/MIC board



BTN Board



Finger Print board



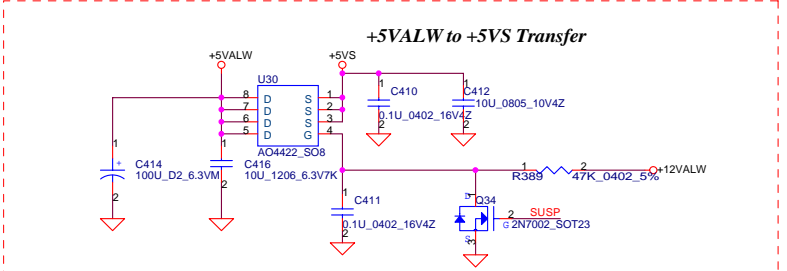
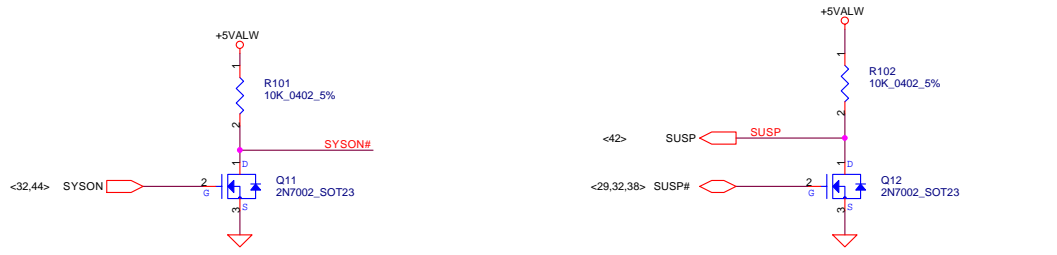
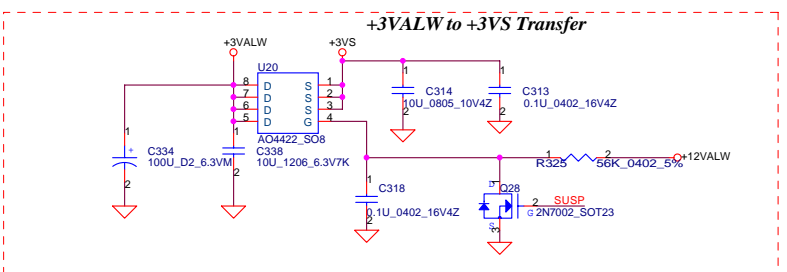
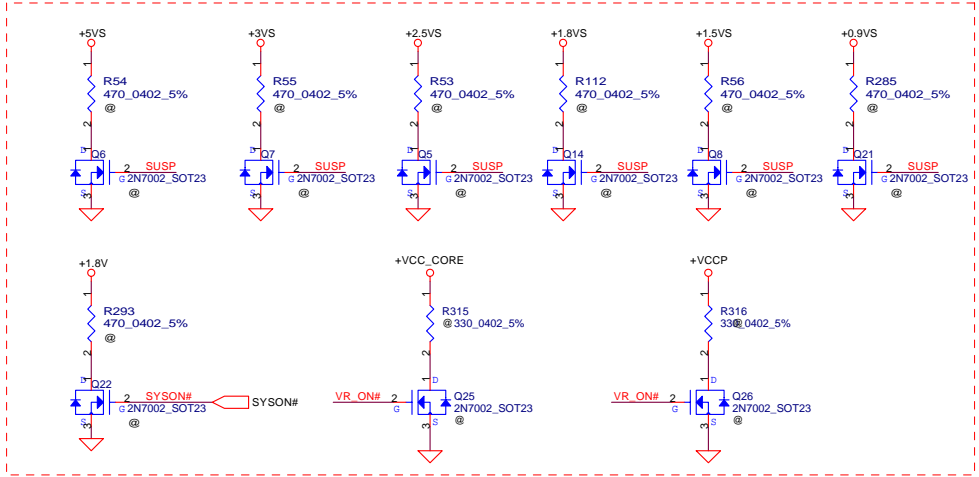
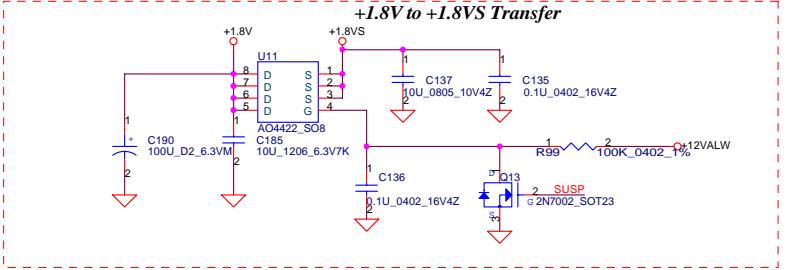
Compal Electronics, Inc.

Title: **DAUGHTER Brd I/F**

Size: Document Number **EDX20 LA-2481** Rev 0.5

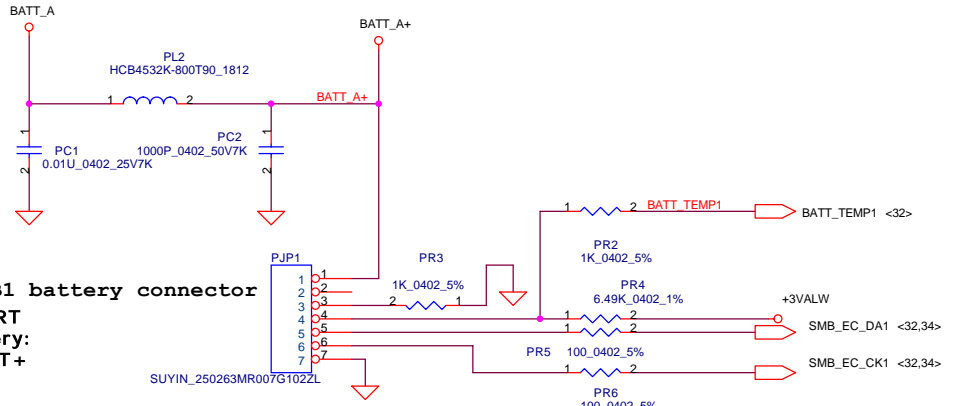
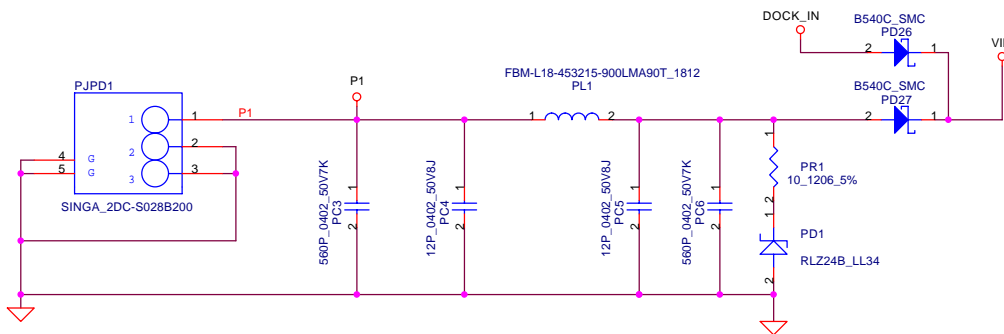
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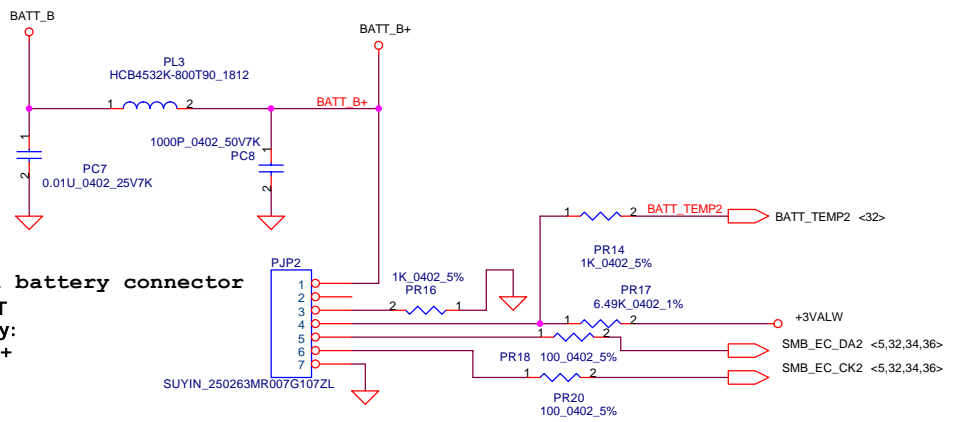
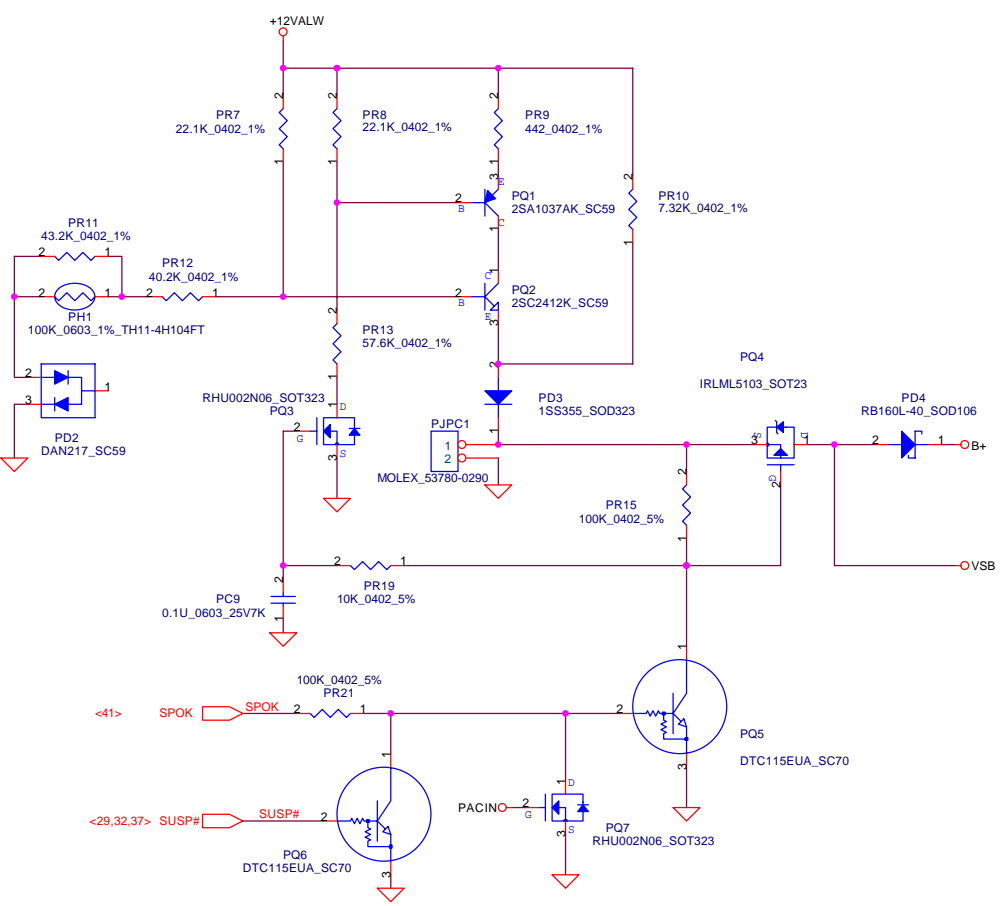


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Compal Electronics, Inc.		
Title	DC/DC Interface	
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PJPB1 battery connector
SMART Battery:
 1. BATT+
 2. ID
 3. B/I
 4. TS
 5. SMD
 6. SMC
 7. GND

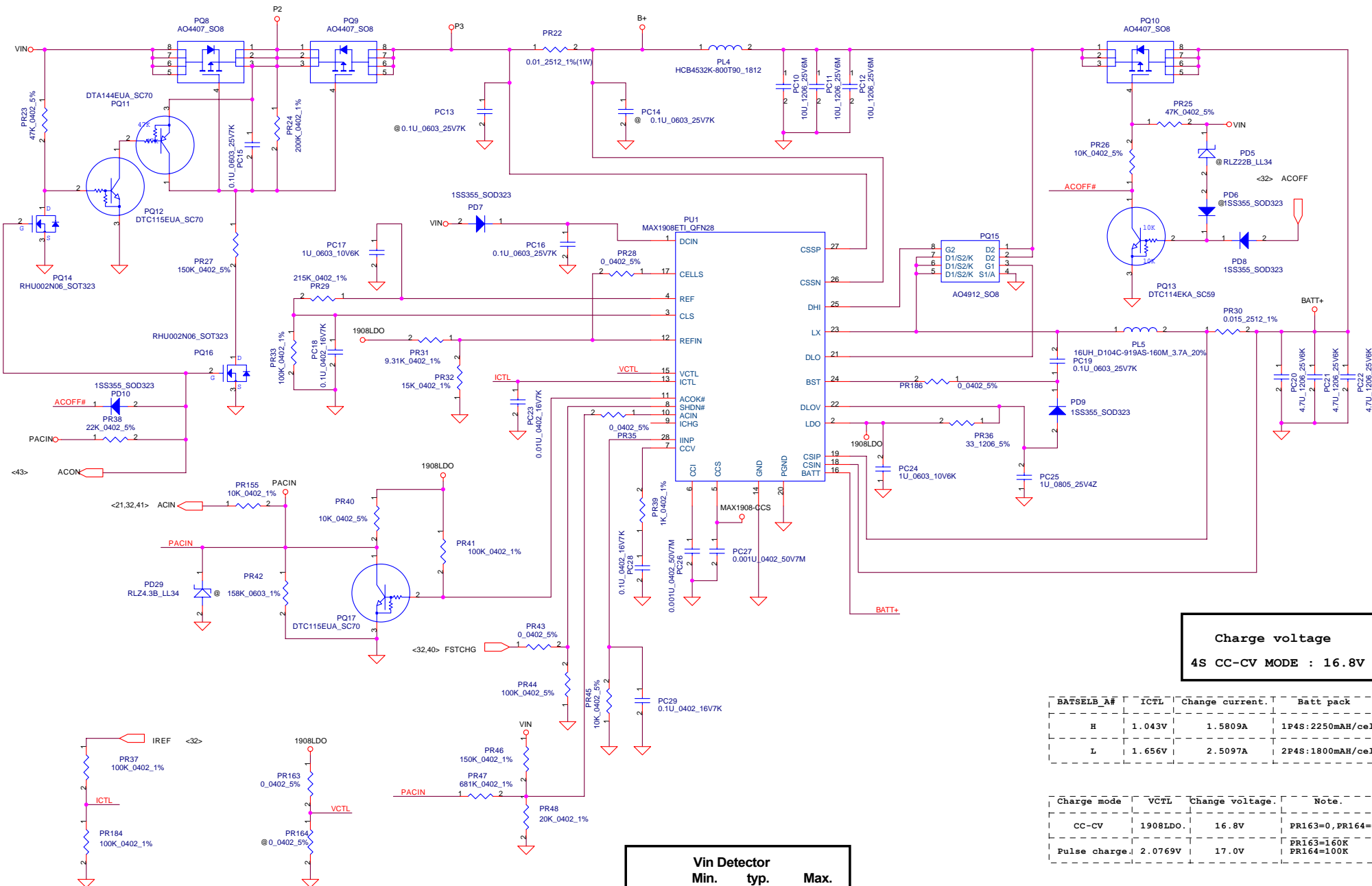


PJPB1 battery connector
SMART Battery:
 1. BATT+
 2. ID
 3. B/I
 4. TS
 5. SMD
 6. SMC
 7. GND

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COMPAL ELECTRONICS, INC			
Title			
Bridge batt & Batt conn & DC in			
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I_{adp}=0~2.25A (42.75W)



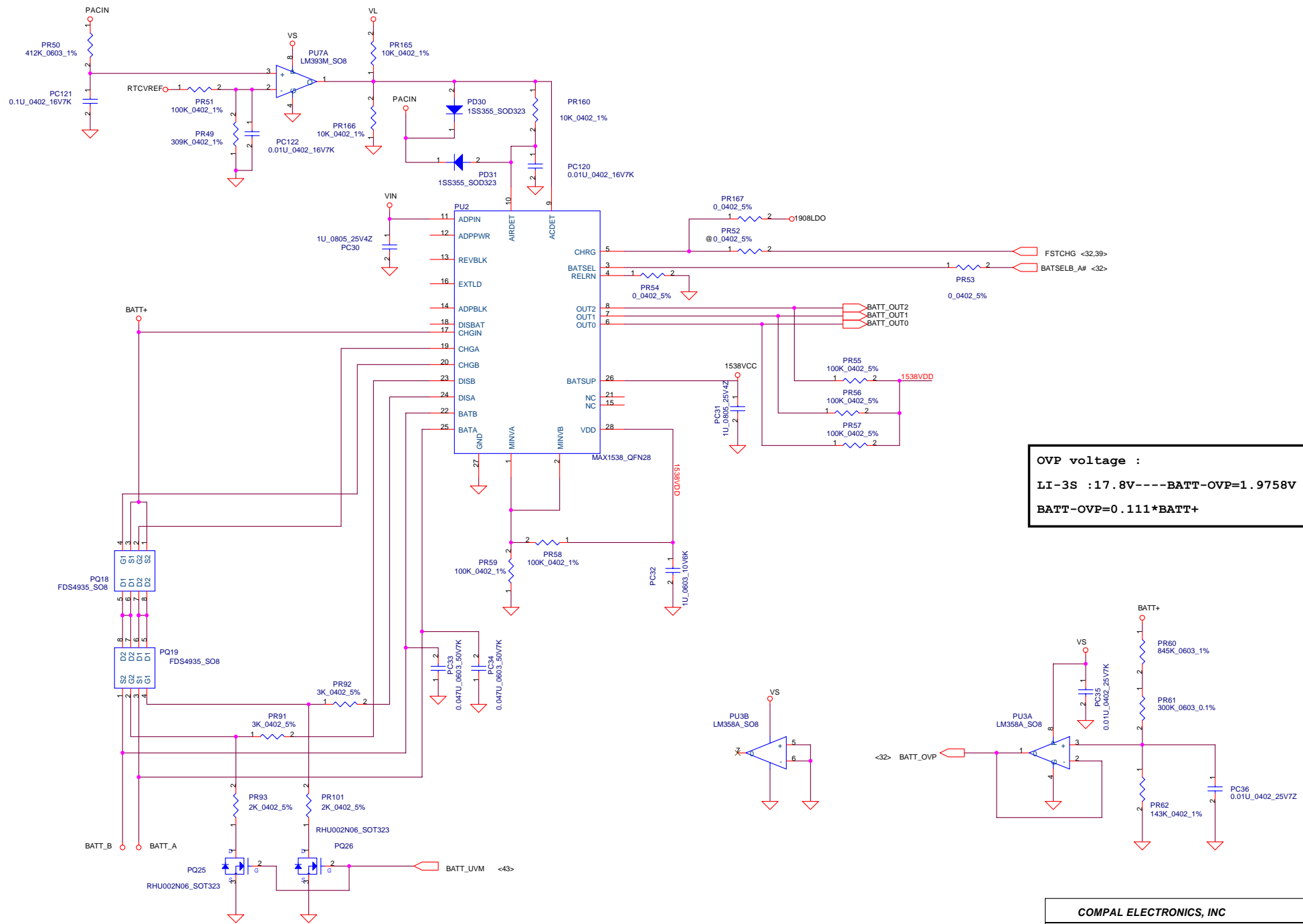
Charge voltage
4S CC-CV MODE : 16.8V

BATSELB_A#	ICTL	Change current.	Batt pack
H	1.043V	1.5809A	1P4S:2250mAh/cell
L	1.656V	2.5097A	2P4S:1800mAh/cell

Charge mode	VCTL	Change voltage.	Note.
CC-CV	1908LDO	16.8V	PR163=0, PR164=0
Pulse charge	2.0769V	17.0V	PR163=150K PR164=100K

Vin Detector

	Min.	typ.	Max.
L→H	V 17.85V	V	V
H→L	V 16.98V	V	V



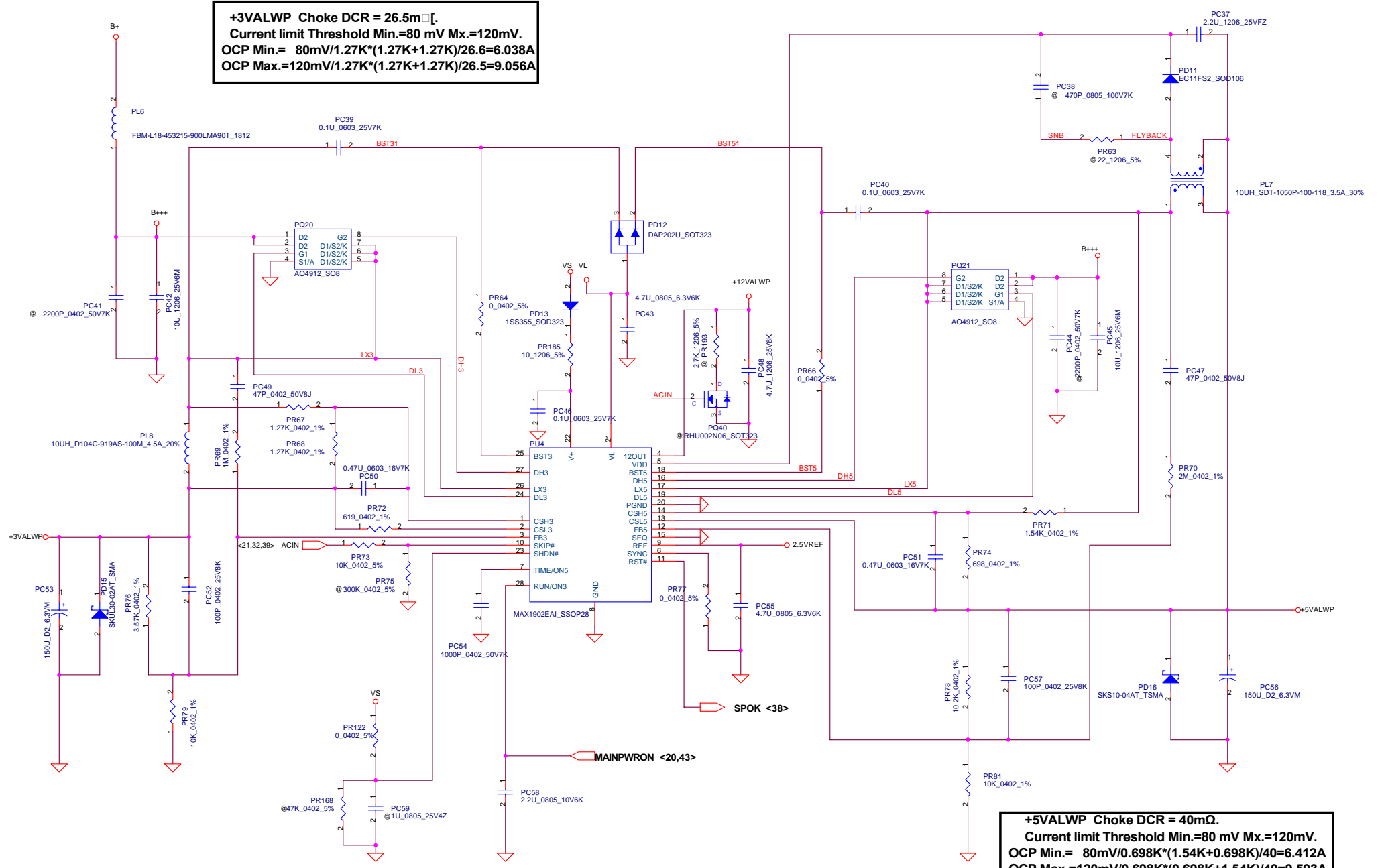
OVP voltage :
LI-3S : 17.8V----BATT-OVP=1.9758V
BATT-OVP=0.111*BATT+

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+3VALWP Choke DCR = 26.5mΩ.
Current limit Threshold Min.=80 mV Mx.=120mV.
OCP Min.= 80mV/1.27K*(1.27K+1.27K)/26.6=6.038A
OCP Max.=120mV/1.27K*(1.27K+1.27K)/26.5=9.056A

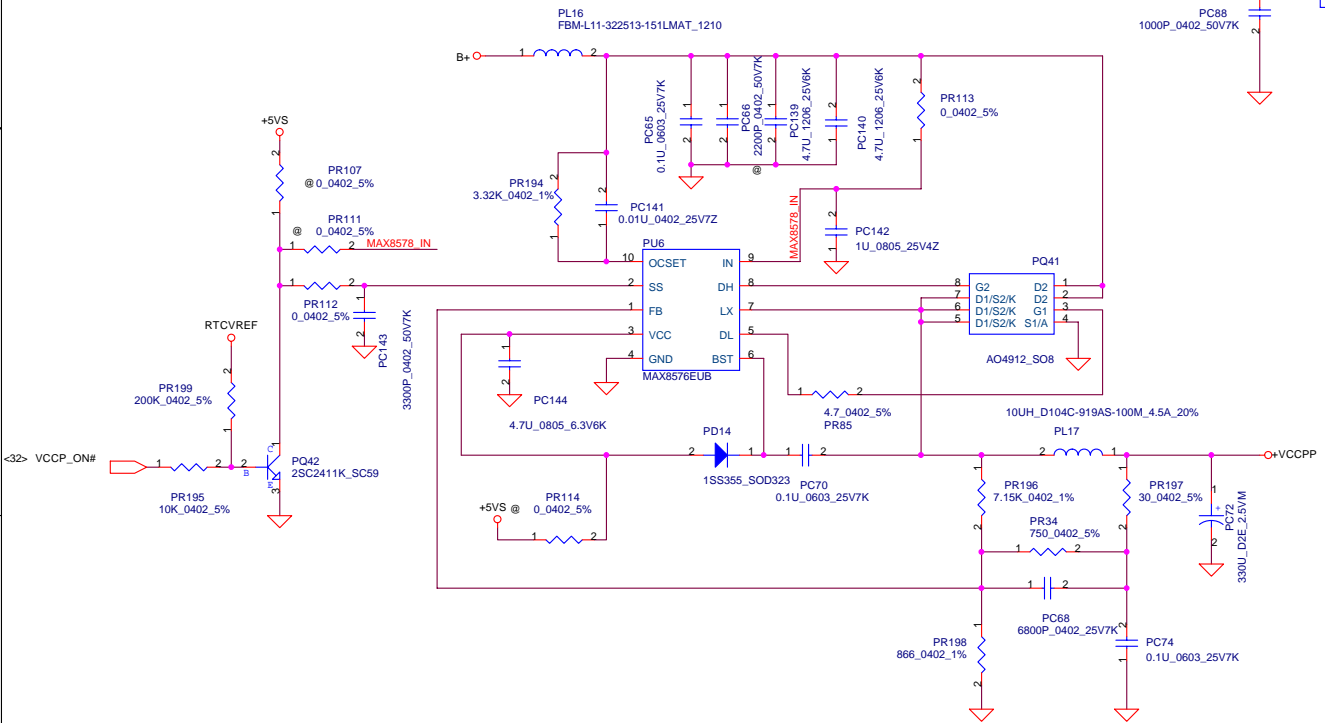
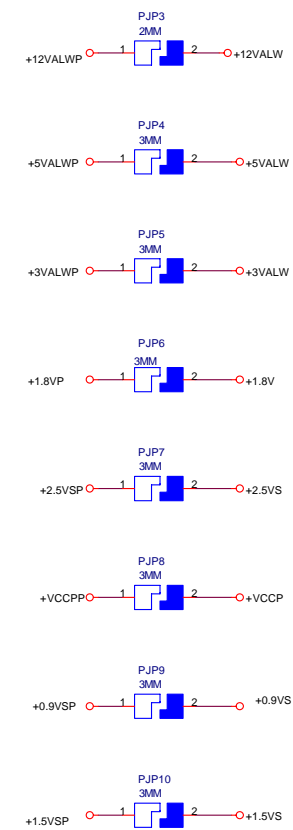
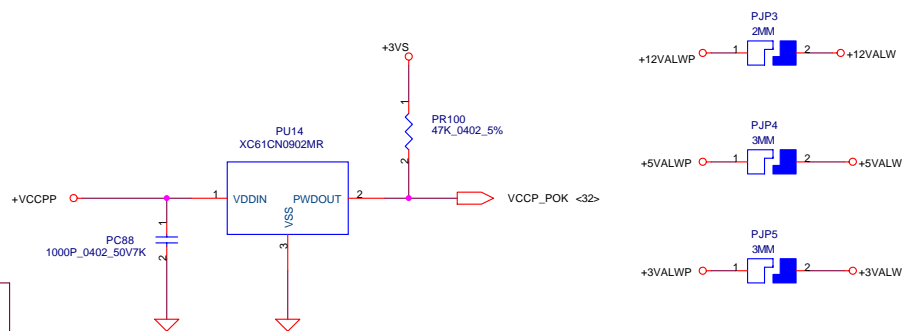
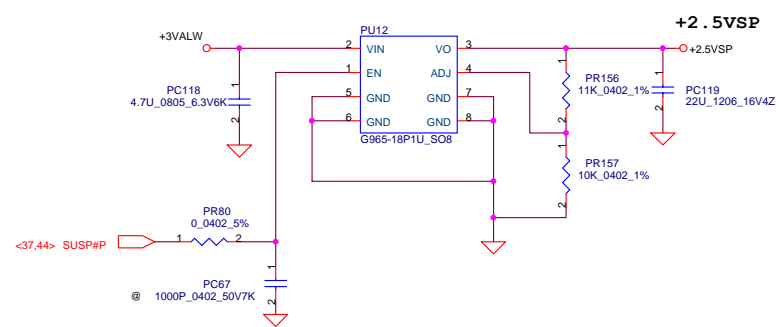
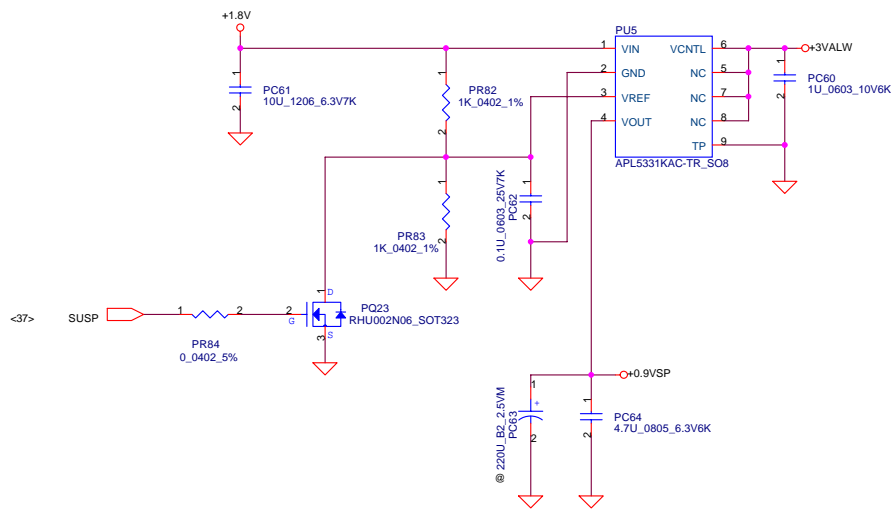
+5VALWP Choke DCR = 40mΩ.
Current limit Threshold Min.=80 mV Mx.=120mV.
OCP Min.= 80mV/0.698K*(1.54K+0.698K)/40=6.412A
OCP Max.=120mV/0.698K*(0.698K+1.54K)/40=9.593A

$RS2(PR64)=RS1(PR58)*RS3(PR61)/(RS1+RS3)$
 $L/RL(DCR)=RS1*RS3(PR61)/(RS1+RS3)*Cs(PC56)$



COMPAL ELECTRONICS, INC		
Title +3VALWP & +5VALWP & +12VALWP		
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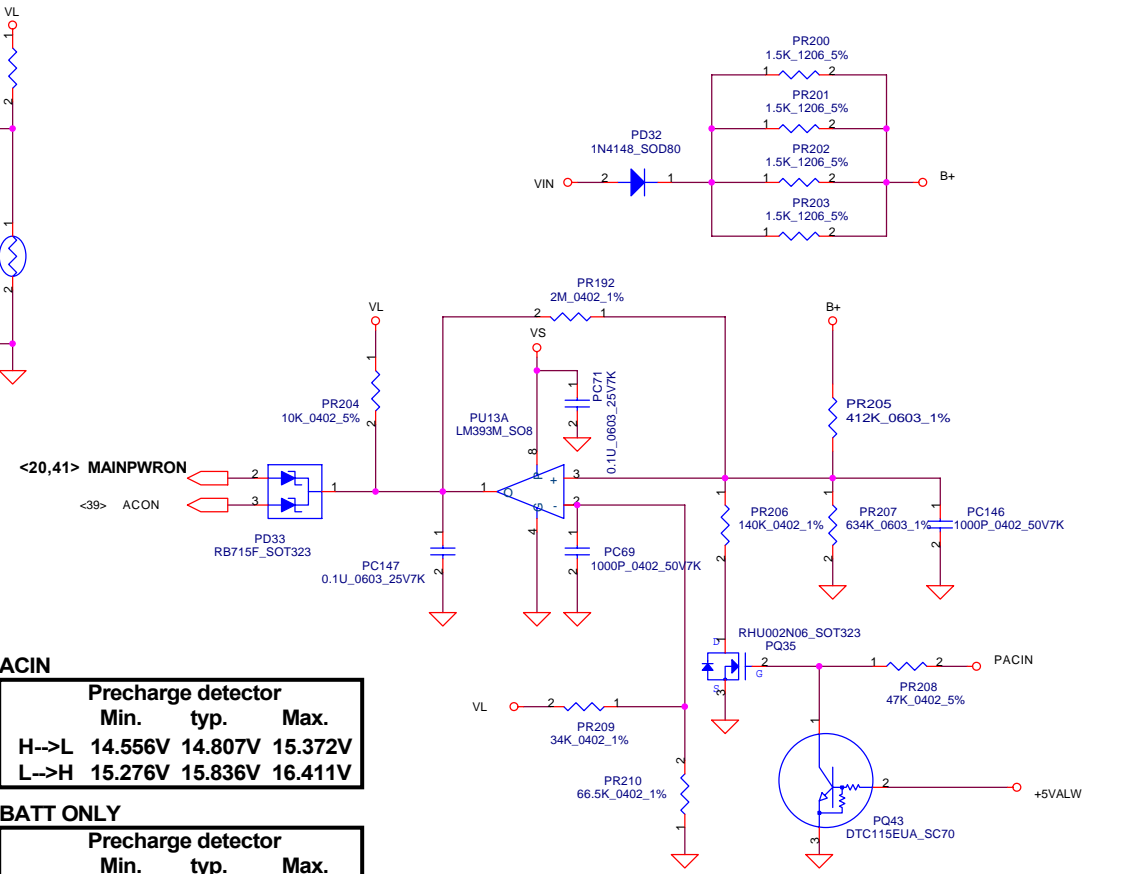
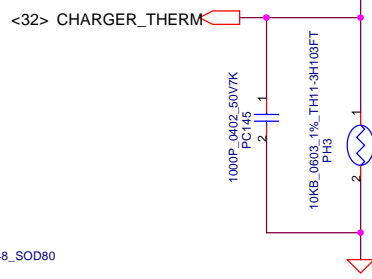
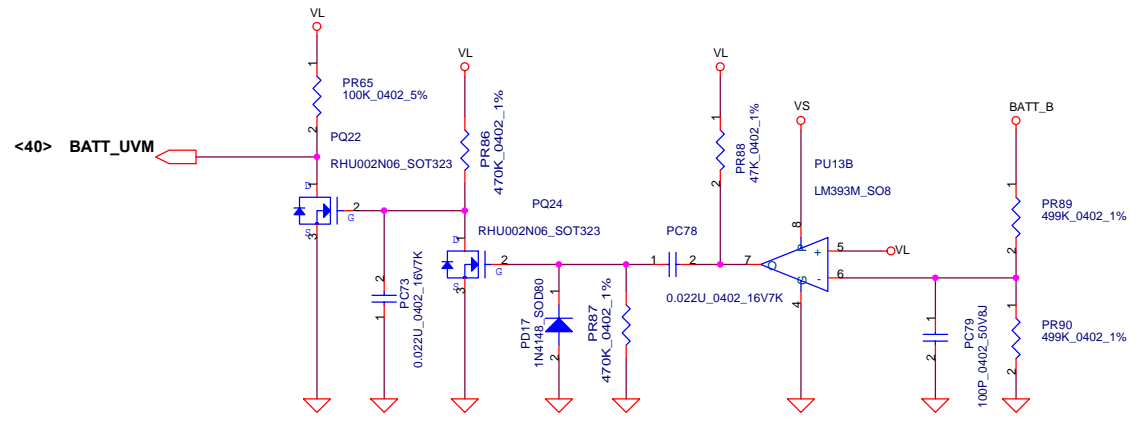
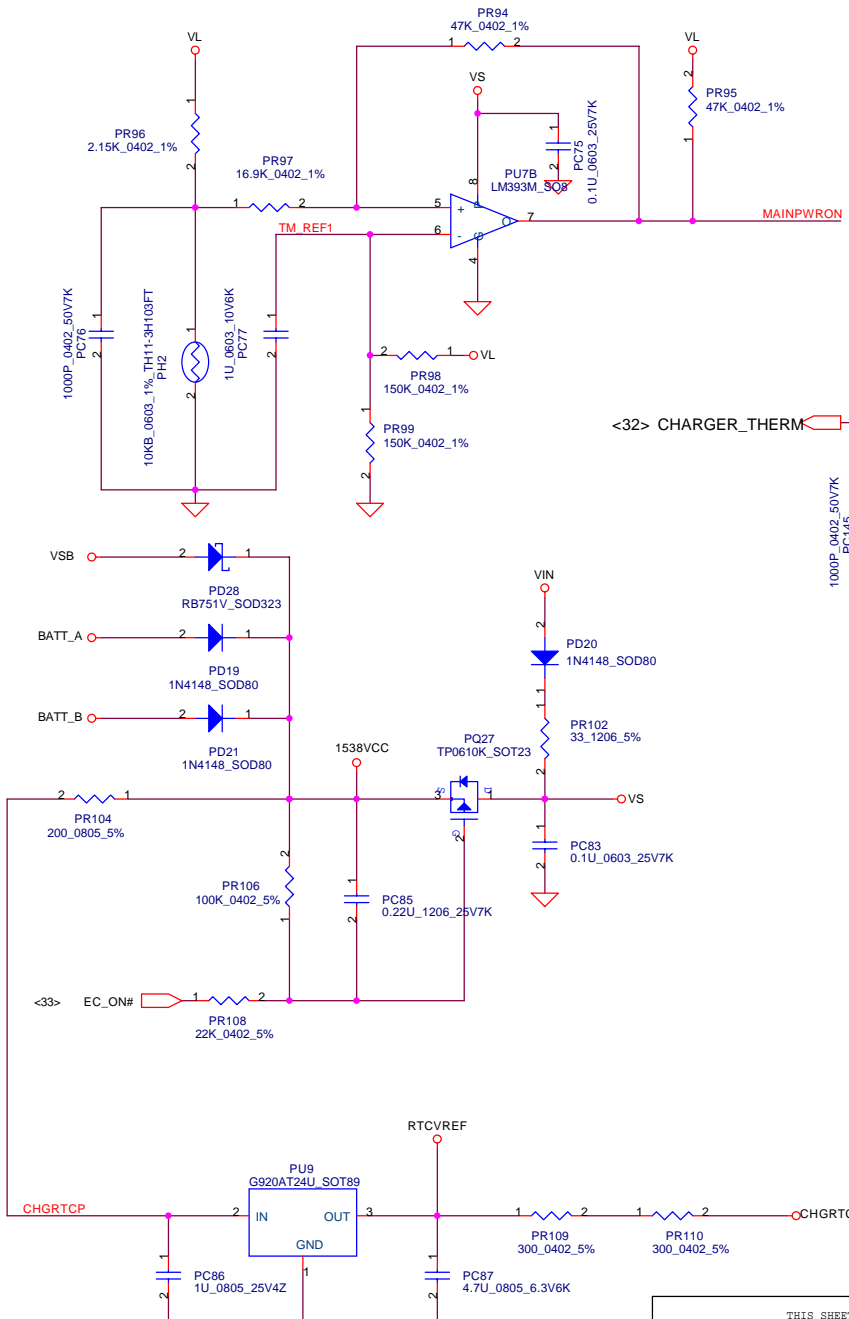
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COMPAL ELECTRONICS, INC		
+VCCPP & +0.9VSP & +2.5VSP		
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PH2 under CPU botten side :
 CPU thermal protection at 80 degree C
 Recovery at 44(45) degree C



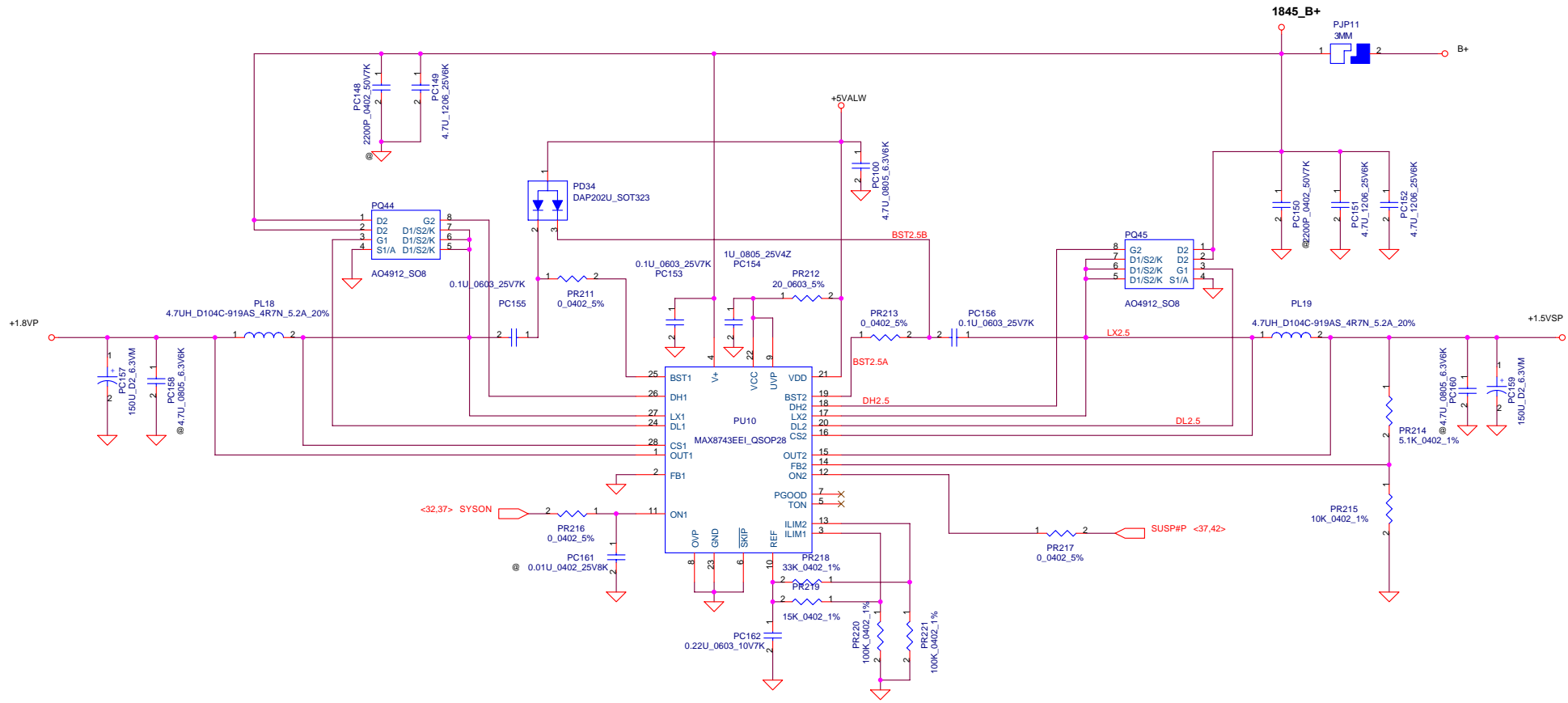
ACIN

	Precharge detector	Min.	typ.	Max.
H->L	14.556V	14.807V	15.372V	
L->H	15.276V	15.836V	16.411V	

BATT ONLY

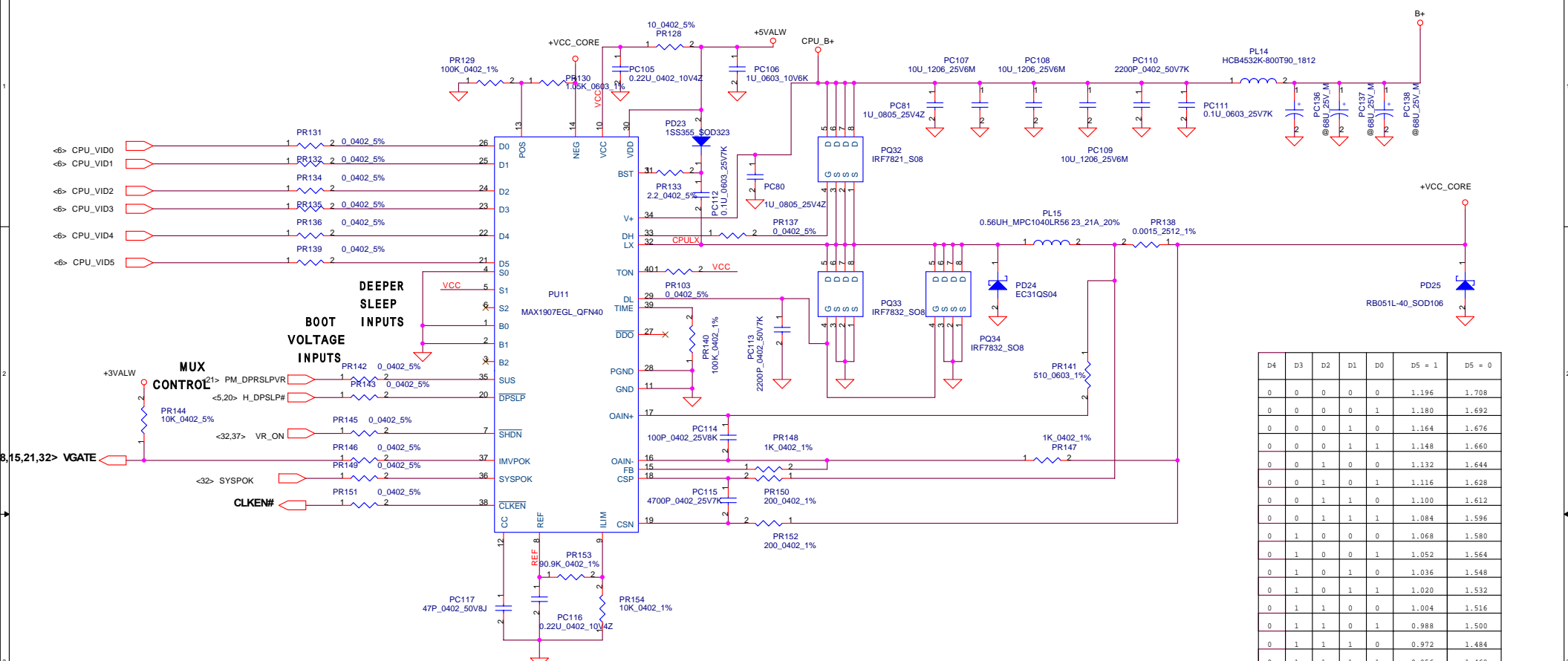
	Precharge detector	Min.	typ.	Max.
H->L	5.044V	5.096V	5.205V	
L->H	6.008V	6.124V	6.243V	

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COMPAL ELECTRONICS, INC		
Title		
+1.5VSP & +1.8VP		
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D4	D3	D2	D1	D0	D5 = 1	D5 = 0
0	0	0	0	0	1.196	1.708
0	0	0	0	1	1.180	1.692
0	0	0	1	0	1.164	1.676
0	0	0	1	1	1.148	1.660
0	0	1	0	0	1.132	1.644
0	0	1	0	1	1.116	1.628
0	0	1	1	0	1.100	1.612
0	0	1	1	1	1.084	1.596
0	1	0	0	0	1.068	1.580
0	1	0	0	1	1.052	1.564
0	1	0	1	0	1.036	1.548
0	1	0	1	1	1.020	1.532
0	1	1	0	0	1.004	1.516
0	1	1	0	1	0.988	1.500
0	1	1	1	0	0.972	1.484
0	1	1	1	1	0.956	1.468
1	0	0	0	0	0.940	1.452
1	0	0	0	1	0.924	1.436
1	0	0	1	0	0.908	1.420
1	0	0	1	1	0.892	1.404
1	0	1	0	0	0.876	1.388
1	0	1	0	1	0.860	1.372
1	0	1	1	0	0.844	1.356
1	0	1	1	1	0.828	1.340
1	1	0	0	0	0.812	1.324
1	1	0	0	1	0.796	1.308
1	1	0	1	0	0.780	1.292
1	1	0	1	1	0.764	1.276
1	1	1	0	0	0.748	1.260
1	1	1	0	1	0.732	1.244
1	1	1	1	0	0.716	1.228
1	1	1	1	1	0.700	1.212

$\Delta I = 0.0528A$ REF MAX=2.01V*10K/(10K+90.2K)=0.2005V
 REF Min=1.99V*10K/(10K+90.2K)=0.1986V
 Iimit Max=0.2005V*10/1.485mΩ+1/2 ΔI=13.528
 Iimit Max=0.1986V*10/1.515mΩ+1/2 ΔI=13.135

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	B.Ver#	Phase
1		Change FAN design	0.4	5	Change FAN circuit to PWM contrl		
2		Delete unused parts	0.4	7	Del C285		
3		Change CPU Ref Location name	0.4	5	Change CPU Ref location name from JP18 to U1		
4		Delete unused parts	0.4	17	Del R252,Del PID0 net on JP20 pin 26		
5		Design Change	0.4	21	Change PID0 net name to ICH GPIO33 Add Q62(2N7002) to contrl ISOLATE# pin		
6		Design Change	0.4	27	Add net ISOLATE# on U39 pin23		
7		Delete SD card LED	0.4	24	Del D11,R377		
8	Save more power in S3 battery mode	Add LAN power control MOSFET	0.4	27	Add Q63(AO3402),Q64(2N7002),R527(10K_0402),C575(0.01uF_0402)		
9		Add MOS on Docking USB_OVC pins	0.4	36	Add Q65,Q66,Q67 on USB_OVC#3, USB_OVC#4, USB_OVC#5		
10		ME change cable design to FFC	0.4	36	Swap JP27,JP22 pin assignment		
11		ME change cable design to FFC	0.4	30	Swap JP25		
12		decrease Audio Gain setting	0.4	30	Depop R245, Pop R243		
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Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	B.Ver#	Phase
1		Del MOS on Docking USB_OVC pins	0.5	36	Del Q65,Q66,Q67,Q68 on USB_OVC#3, USB_OVC#4, USB_OVC#5		
2		Add Docking USB_OVC circuit	0.5	33	Add U54,R533,534,R535,R536,R537,C576,C577,C578,C579		
3		Cost-Down	0.5	7	Del C252		
4		Design Change	0.5	31	Change USB Connector type		
5	External VGA display quality	Design Change	0.5	10	Change R36 to 220 ohm(EE Change List by Motion item 1)		
6	Support new version audio driver	Design Change	0.5	36	Change R1160 to 5.1K ohm(EE Change List by Motion item 2)		
7		Material Change	0.5	32	Change Y6 to KDS_SJ132P7KB10 part. (TXC EOL)		
8				5	Change CPU to C0 step		
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Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	B. Ver#	Phase
1	Add the pre-charge circuit.	Pre-charge function fail.	0.3	43	1.Add the Pre-charge circuit. Add the PD32,PR188, PR189,PR190, PR191,PR192, PR182,PD33, PC71,PC127,PC12,PR194,PR87,PR196,PQ35,PR198,PR199,PR197, PQ38	0.3	DVT
2	To adjust sequence for +5VALWP and +3VALWP.	To adjust sequence for +5VALWP and +3VALWP.	0.3	41	1.Delete PC59. 2.Change the PR122 from 100K to 0.	0.3	DVT
3	Chnage the power solution for +VCCP,+1.8VP and +1.5VSP.	Solve Digitizer issue.	0.3	42,44	1.Change the control IC of +1.5VALWP from FAN5234 to MAX8743. 2.Change the control IC of +1.8VP from ISL6227 to MAX8743. 3.Change the control IC of +VCCPP from ISL6227 to MAX8576.	0.3	DVT
4	Delete the circuit of 1.2VP.	Because H/W donot need.	0.3	43	1.Delete the PC100,PQ29,PR121,PR122,PC99,PC101,PR123,PR127,PQ30,PR158,PC102,PR125,PC103.	0.3	DVT
5	To adjust the input-current limit.	Change the input-cuurrent from 3A to 2.25A	0.3	39	1.Change the PR29 from 150K to 232K.	0.3	DVT
6	The jumping cursor when CPU_CORE heavy loading.	Improve Digitizer issue.	0.4	45	1.Change the PL15 form TOKIN .56UH to Panasoic .36UH.	0.4	DVT2
7	The system has shut down issue when swap battery.	The max1538 has some issue when sawp battery. So add the one shot circuit to improve the issue.	0.4	40,43	1.Add the 2n7002 on PQ22,PQ24,PQ25 and PQ26. 2. Add the 2K_0402_5% on PR93 and PR101. 3. Add the 3K_0402_5% on PR91 and PR92. 4.Add the 470K_0402_5% on PR86 and PR87. 5.Add the 100K_0402_5% on PR65. 6.Add the 47K_0402_1% on PR88. 7.Add the 499_0402_1% on PR89 and PR90. 8.Add the 1N4148 on PD17. 9.Add the .022U_0402_16V on PC73 and PC78. 10.Add the 100P_0402_50V on PC79.	0.4	DVT2
8	Battery drain issue when battery only on system off.	It did not turn off the +VCCP (1.05V) power no matter system was ON or OFF.Add pill down resistor.	0.4	42	1.Add the resistor on the PR199 and value is 100K_0402_5%.	0.4	DVT2
9	The customer request add the cap.	Improve Digitizer issue.	0.4	45	1.Add the 1U_0805_25V on PC80 and PC81.	0.4	DVT2
10	Disable Vin OVP function.	Disable Vin OVP function.	0.4	39	1.Delete the PD5 RLZ22B_LL34. 2.Delete the PD6 ISS355_SOT323.	0.4	DVT2
11.	Change Main battery connect.	For ME request.	0.4	39	1.Change the main battery from SUYIN_250263MR007G102ZL to SUYIN_250263MR007G110ZR.	0.4	DVT2
12.	Because the FDS4935A will phase out.	Because the FDS4935A will phase out.	0.4	40	1.Change the main battery from SUYIN_250263MR007G102ZL to SUYIN_250263MR007G110ZR.	0.4	DVT2

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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2488

Revision : 1.0

EDX20 Sub_Board Schematics Document

MB DOCK Board

2005-03-31

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COVER PAGE		
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	EDX20 LS2488	1.0
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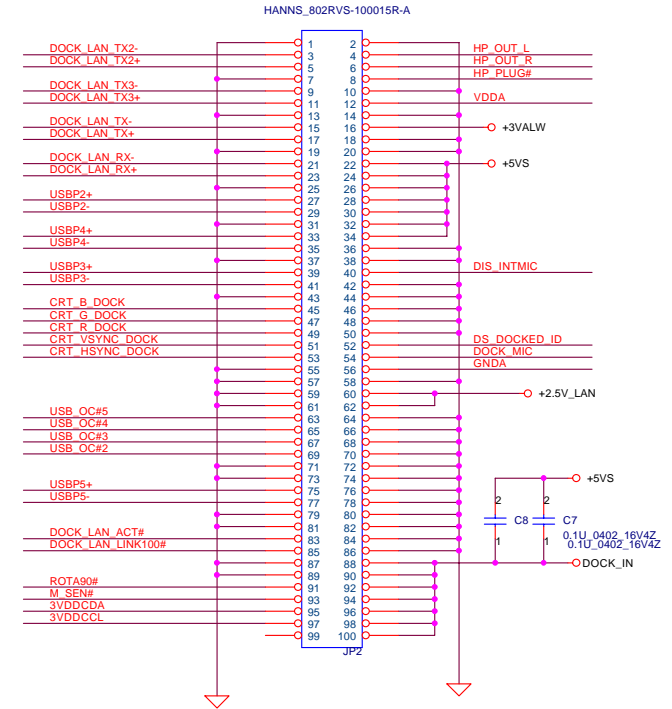
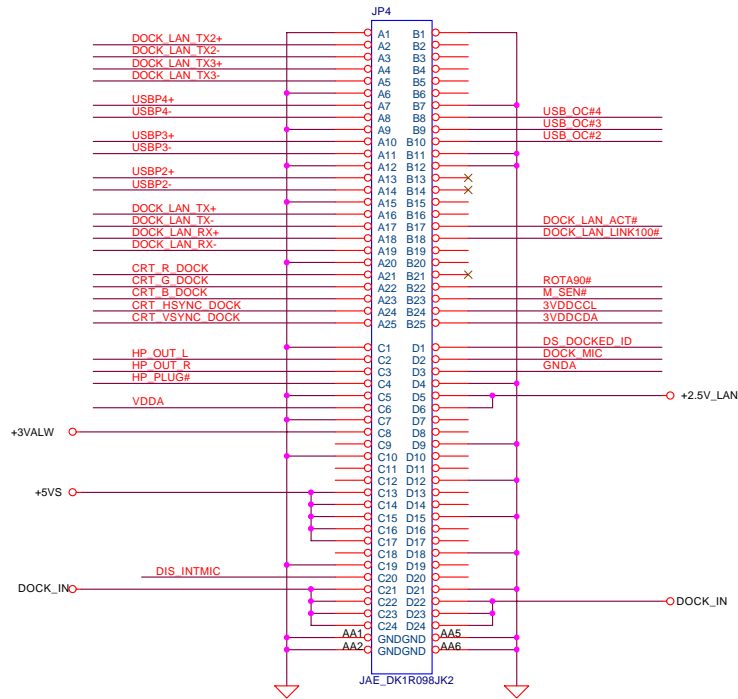
MB_to_Docking_I/F board

MODEL Name : EDX20

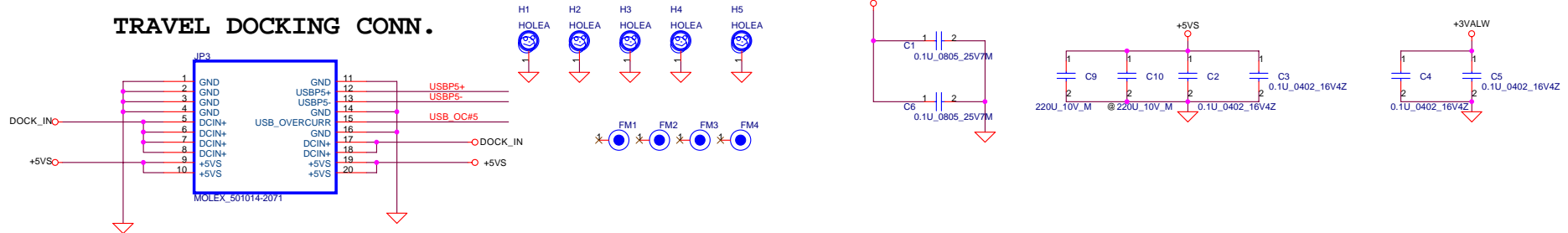
FILE Name : LS 2488

TO LLANO DOCKING

FROM MB 100P Brd-to_Brd CONNECTOR



TRAVEL DOCKING CONN.



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