1- Hardware description

The S820i CPU board is organize as following :

Laminator Station (S820)

Communication

serial port : (Tx, Rx)
9600 to 115200 baud

Parallel port : standard Centronic

μP

Print head management Clock 14.56 MHz

LCD PCBA

Stepper motor

MEMORY

FLASH MEMORY
Base :   512K bytes

DRAM :
4M bytes

DC Motor
advance ribbon, feeder, move head (up/down)

Sensor & measure
including E2pot to allow self adjustment (no potentiometer)

card sensor, Ribbon rotation, color sensor, Head temperature, Power supply, head position (up/down) cover open, Press button

RFID PCBA connection
RF Clock 13.56 MHz
Processor Clock 10 MHz

Flipover PCBA connection

Printer Extension PCBA connection

Processor Clock 32 MHz