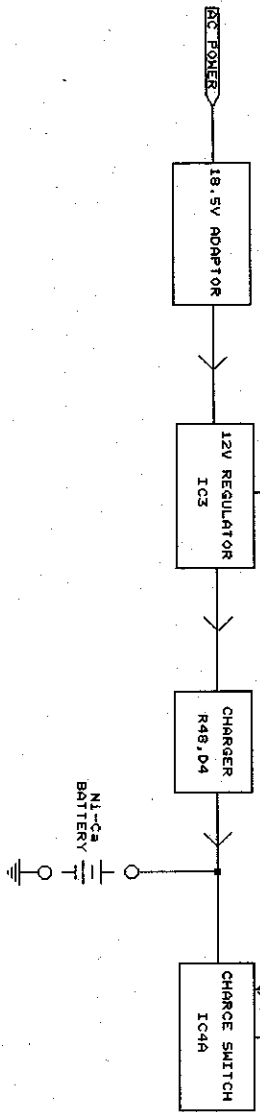
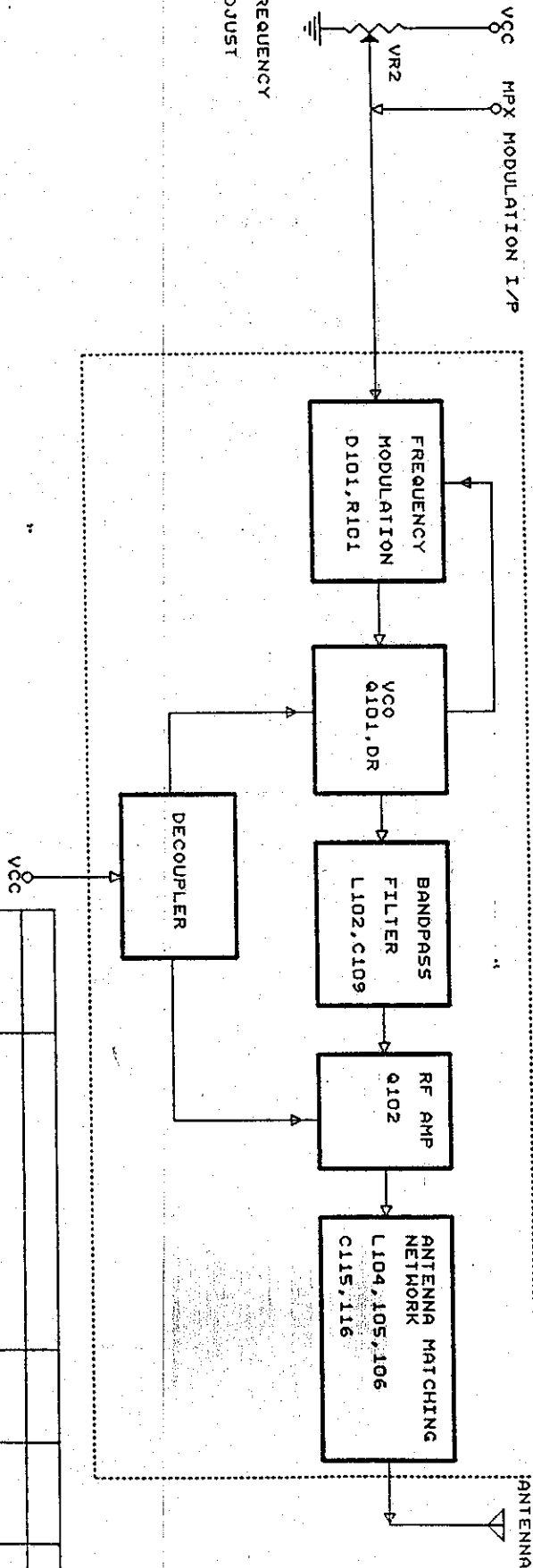


The block diagram illustrates the signal flow and power management of a radio receiver. The signal path begins with the **AUDIO IN** (L and R channels) entering the **ALC AMP IC1**. The output of IC1 passes through an **AF FILTER L3** and an **AF FILTER L4** before reaching the **STEREO MULTIPLEXER IC2**. IC2 is also connected to a **38KHZ** crystal (Y1). The output of IC2 is the **ST. MPX SIGNAL**, which is then processed by the **UHF MODULE** and finally sent to the **ANT** (antenna). Power is supplied to the system via the **AUTO POWER IC4C** and **POWER CONTROL IC4D, 42**, which are connected to a **2.2V REGULATOR ZD2**. A variable resistor **VR2** is connected to the **ST. MPX SIGNAL** line and ground.



ISSUE NO.	DESCRIPTION/REVISION	CHG. BY	APPR. BY	DATE
UNI-PART PRECISE PRODUCTS LTD				
DESCRIPTION:	ALL DIMENSION IN mm GENERAL TOLERANCE: LINEAR: ± 0.1 ANGULAR: ± 0.1 UNLESS OTHERWISE SPEC.			
BLOCK DIAGRAM				
MATERIAL:	SCALE: REV —			
DRAWN BY :	MODEL: RS951			
CHECKED BY :	PART NO.:			
DATE: 3/8/79	SHEET 1 OF 1			
APPROVED BY :	Dwg. NO.: GA4ED246			
DATE: 4/8/80				

BLOCK DIAGRAM OF RS901U



UNI-ART PRECISE PRODUCTS LTD.
CONFIRMED

10 AUG 1999

Issued By: *Z. K. K.*
DEVELOPMENT ENGINEERING DEPT. (02)

UNI-ART PRECISE PRODUCTS LTD

DESCRIPTION:

BLOCK DIAGRAM OF RS901U-001
RS901U-002 & RS901U-003

ALL DIMENSION IN mm
GENERAL TOLERANCE:
LINEAR : ± 0.1
ANGULAR : ± 0.1
UNLESS OTHERWISE SPEC.

MATERIAL: _____ SCALE: _____ REV 2

FINISH : _____ MODEL: RS901U

DRAWN BY : *Z. K. K.* DATE: 30 AUG '96 PART NO.: _____

CHECKED BY : *Z. K. K.* DATE: 30 AUG '96 SHEET 1 OF 1

APPROVED BY: *Z. K. K.* DATE: 30 AUG '96 Dwg. NO.: RA4E0309