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Dear Steve,

Regarding this question, because the area scanned is completely larger than EUT itself, therefore, even there has second peak, it would be the minor.

Besides, attached please find the test plots that were done on Sept. 5.

Thanks a lot for your help.

Best Regards,

Lucy

Steve Cheng <SCheng@CCSEMC.com>

2003/09/13 08:38 AM

? ? ? : ""lucy\_tsai@ccsemc.com.tw" <lucy\_tsai@ccsemc.com.tw>

? ? ? ? : ""Jonson (E-mail)" <jonson@cclab.com.tw>, Mike Kuo <MKUO@CCSEMC.com>

? ? : RE: ??: Re: ASUSTek AN03T3182 TCB question

(Typo resent)

Hi Lucy,

Q1 to Q9 and Q11 OK, But I still **cannot** find the proper info for Q10 as described below,  
Question #10: P33 & P49 of NB-COMPAQ test plot shown second peak and is not completely captured, please re-measure and supply the plot.

P33 now transfer to P36 of "(AGC-100)SAR Test plot\_NB-COMPAQ-2.pdf" remains no change.

P49 now transfer to P53 of "(AGC-100)SAR Test plot\_NB-COMPAQ-2.pdf" and measurement area remains no change.

Please enlarge your scan area to include the second peak. If question is not clear to you, please call me to discuss this issue.

Also, please supply the system verification plots for extra plot generated.

Best regards,  
Steve

-----Original Message-----

9/17/2003

**From:** lucy\_tsai@ccsemc.com.tw [mailto:lucy\_tsai@ccsemc.com.tw]  
**Sent:** Friday, September 12, 2003 4:52 AM  
**To:** Steve Cheng  
**Cc:** Jonson (E-mail); Mike Kuo  
**Subject:** ??: Re: ASUSTek AN03T3182 TCB question

Dear Steve,

Below please find the reply for the SAR portion, Q8-11.

Q8, we have verified the low and mid channels and please refer the SAR test report and plot for details.

Q9, "senser-surface:0mm" here is refer to the configurations of 1, 2,5,6 : touch mode, setting EUT directly connect to the phantom. Besides, we have renamed each test plot which may be easy understood and compared with the SAR test report.

Q10-11, we have verified test and updated as attached SAR test report.

Please help to review them and issue the grant a.s.a.p if there has no other problems, thank you.

Best Regards,

Lucy

-----Original Message-----

**From:** lucy\_tsai@ccsemc.com.tw [mailto:lucy\_tsai@ccsemc.com.tw]  
**Sent:** Wednesday, September 03, 2003 1:57 AM  
**To:** Steve Cheng  
**Cc:** Jonson (E-mail); Lucy (E-mail); Mike Kuo  
**Subject:** ??: Re: ASUSTEK AN03T3182

Hi Steve,

Please refer to the below for the answer to Q1-Q7.

For Q1, it's caused by measurement equipment deviation.

For Q2, we have verified all modes with each type (here refers to PDA and NB) of host, finding out the worst position to perform the test with rest hosts as the result recorded in the report.

For testing with PDAs, we tested EUT with NEC's PDA firstly as the data shew in page 52 that worst case found was EUT pol. in V, Antenna Pol. in H and EUT antenna pol. in Z. Then for other type of PDAs, we all set them at the same position of V/H/Z for final testing. The above rule also applied to Notebook PC.

9/17/2003

Therefore, please recheck it again and hope that will be enough.

For Q3, please find attached for the internal photos.

For Q4, the plastic basket doesn't contain metal material and its characteristic just as test wooden table that won't interfere the RF measurement.

For Q5, please refer the attached for the operational description.

For Q6, please refer the attached for the antenna spec.

For Q7, please take page 22 as example that the test result was recorded in the below of the test plot. the 26dBc bandwidth is the range between two arrow. The display line here mean nothing.

Best Regards,

Lucy

Steve Cheng <SCheng@CCSEMC.com>

? ? ? : "Jonson (E-mail)" <jonson@cclab.com.tw>, "Lucy (E-mail)" <lucy\_tsai@cclab.com.tw>

2003/08/28 09:54 AM

? ? ? ? : Mike Kuo <MKUO@CCSEMC.com>

? ? : Re: ASUSTEK AN03T3182

RT for project: AN03T3182

Notice\_content

-EMC portion-

Question #1: p11 of EMC report shown highest power measured is 28.97dBm. But summary indicated 28.95 dBm, and SAR tested to 28.95 dBm. Please clarify.

Question #2: On P49 section 9 "Field strength of spur radiation measurement", Please justify that full test on one host and then test only VH2 or VVX configuration on rest of two hosts is sufficient to demonstrate the compliance.

Question #3: Please remove the RF shielding and re-take the internal photo.

Question #4: Please confirm that the plastic basket used in the EMC test for raising up the EUT has proper RF characteristic and will not interfere the measurement.

Question #5: OP description does not have proper info

Question #6: Antenna spec needs to be re-scanned. It is not readable.

Question #7: P 22, emission bandwidth test, display line is not in -26dBc position

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-SAR portion-

Question #8: P25 shown full test on ASUS PDA and result with 1.2w/kg maximum on test setup configuration 2, but partial test on NEC shown different SAR pattern and maximum shift to configuration 1 with a maximum of 1.4w/kg. Since SAR reading is close to the limit and no enough test data to support that the high channel is the highest SAR configuration for NEC platform, please supply extra test data on Low and Mid channel to clarify this concern.

Question #9: Please explain the setup purpose of "Sensor-Surface: 0mm (Fix Surface)" on each test plot.

Question #10: P33 & P49 of NB-COMPAQ test plot shown second peak and is not completely captured, please re-measure and supply the plots.

Question #11: The ET3DV6 SN:1577 mentioned on p6 of test report does not matching the probe info supplied. Please correct.

The items indicated above must be submitted before processing can continue on the above referenced application. Failure to provide the requested information within 60 days of the original e-mail date may result in application dismissal and forfeiture of the filing fee. Also, please note that partial responses increase processing time and should not be submitted. Any questions about the content of this correspondence should be directed to the e-mail address listed below the name of the sender.

Best Regards

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Best regards,

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9/17/2003