



FCC ID.: MSQ-ROGSTRIF500

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Federal Communications Commission
Authorization and Evaluation Division

December. 25, 2017

Confidentiality Request

Pursuant to Sections 0.457 and 0.459 of the Commission's Rules, the Applicant
Hereby requests confidential treatment of information accompanying this Application
As outlined below:

Schematics : ROG Strix Fusion 500- Schem.pdf
Block Diagram : ROG Strix Fusion 500- BlkDia.pdf
Operational Description : ROG Strix Fusion 500- OPDes.pdf

The above materials contain trade secrets and proprietary information not customarily
Released to the public. The public disclosure of these matters might be harmful to the
Applicant and provide unjustified benefits to its competitors.

The Applicant understands that pursuant to Rule 0.457, disclosure of this Application
and all accompanying documentation will not be made before the date of the Grant for
this application.

Sincerely,

Jackson Yen / Associate Vice President
ASUSTeK COMPUTER INC.