

D_SUB

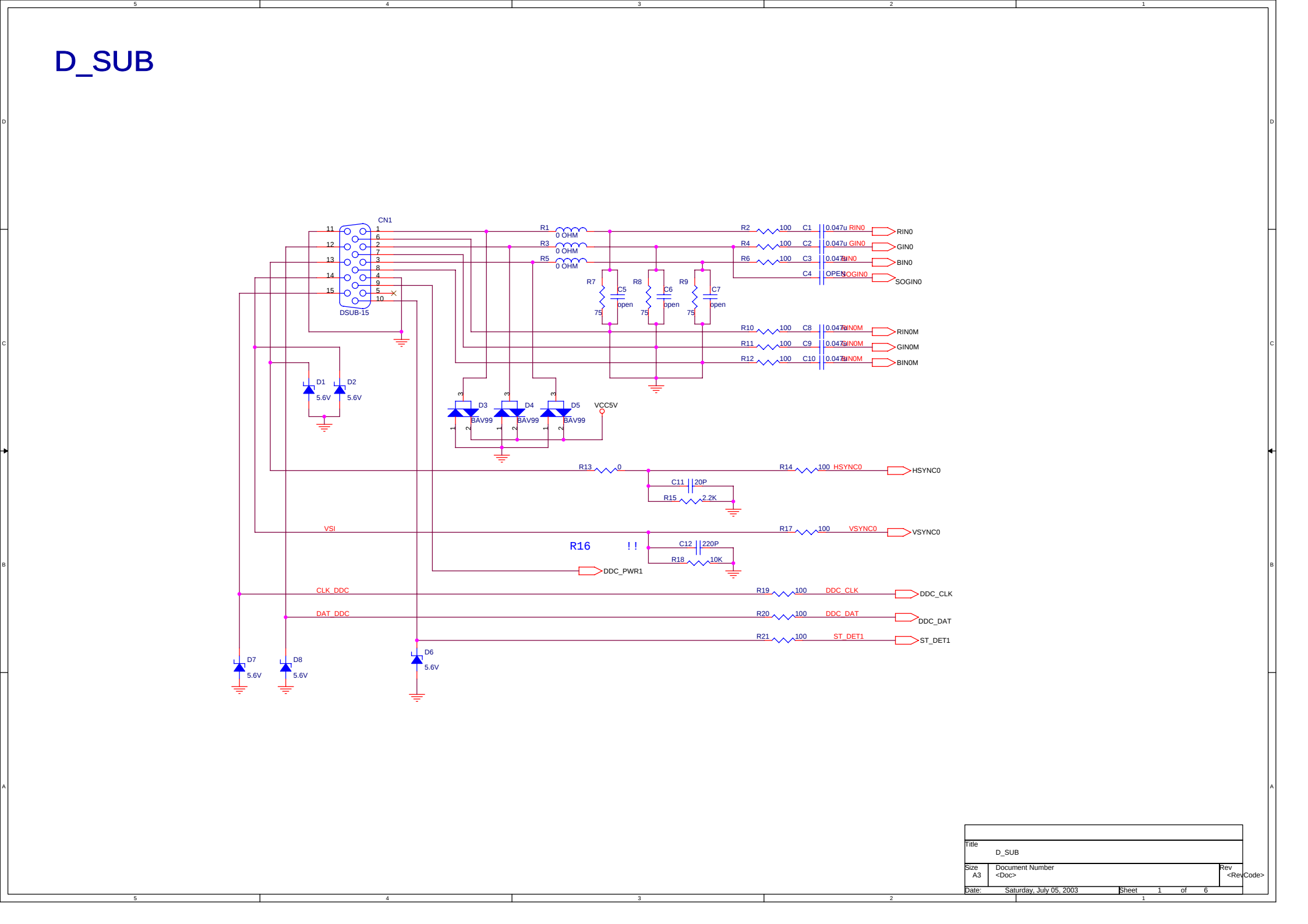
The schematic diagram illustrates the electrical connections for a D_SUB connector (CN1, DSUB-15). The connector pins are numbered 1 through 15. The circuit includes the following components and connections:

- Resistors:** R1, R3, R5 are 0 OHM. R2, R4, R6, R10, R11, R12, R14, R17, R19, R20, R21 are 100. R7, R8, R9 are 75. R13 is 0. R15 is 2.2K. R16 is 10K. R18 is 10K.
- Capacitors:** C1, C2, C3, C4 are 0.047u. C5, C6, C7 are open. C8, C9, C10 are 0.047u. C11 is 20P. C12 is 220P.
- Diodes:** D1, D2, D7, D8 are 5.6V. D3, D4, D5 are BAV99.
- Other Components:** VCC5V supply, DDC_PWR1, and a note "!!" near R16.

The signals are connected to the following pins:

- RIN0, GIN0, BIN0, SOGIN0: Pins 1, 2, 3, 4
- RINOM, GINOM, BINOM: Pins 5, 6, 7
- HSYNC0, VSYNC0: Pins 8, 9
- DDC_CLK, DDC_DAT, ST_DET1: Pins 10, 11, 12
- CLK_DDC, DAT_DDC: Pins 13, 14
- VSI: Pin 15

Title		
D_SUB		
Size	Document Number	Rev
A3	<Doc>	<RevCode>
Date:	Saturday, July 05, 2003	Sheet 1 of 6



D_SUB

The schematic diagram illustrates the electrical connections for a D_SUB connector (CN1, DSUB-15). The connector pins are numbered 1 through 15. The circuit includes the following components and connections:

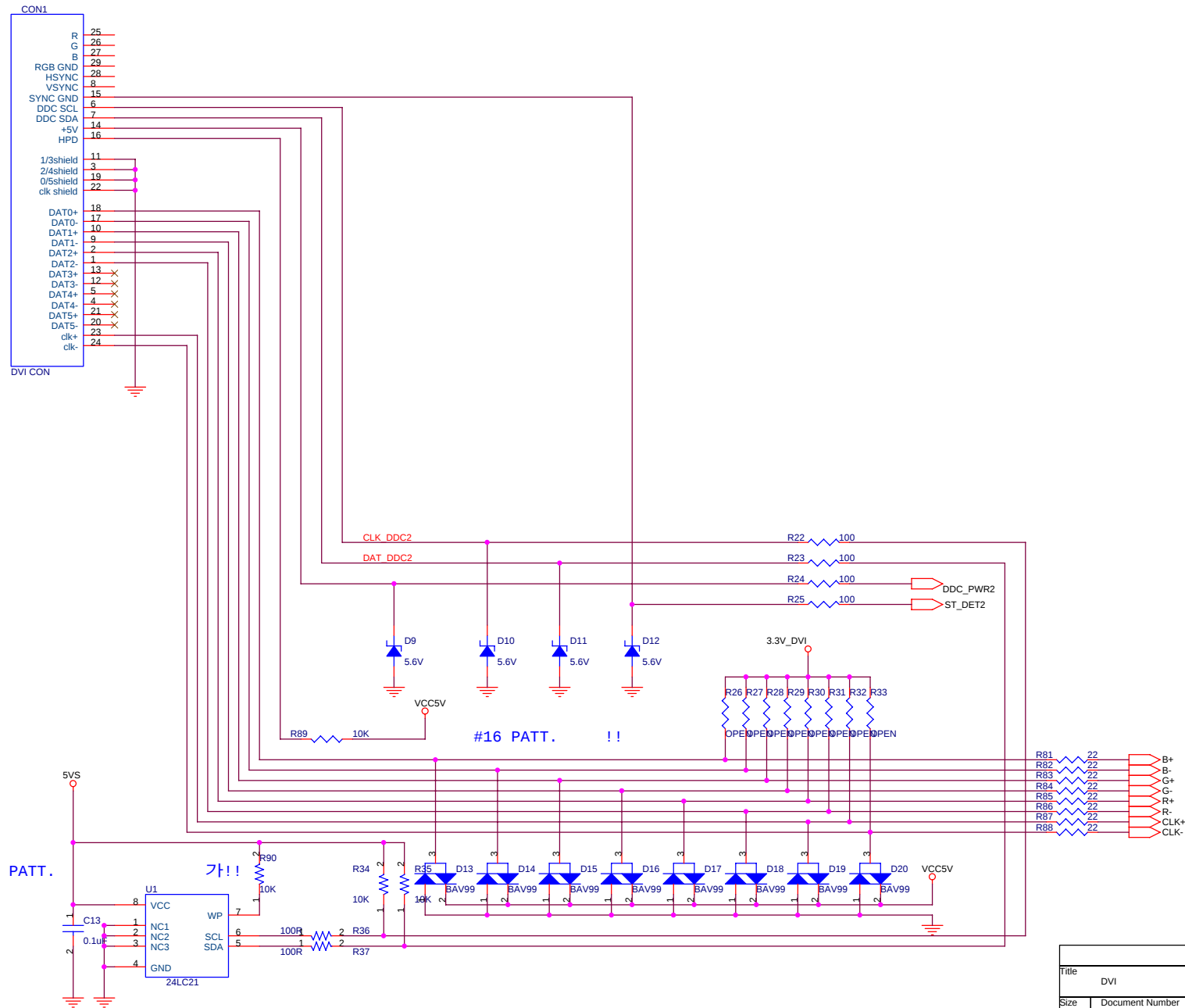
- Power and Grounding:** VCC5V is connected to pin 1. Pins 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, and 15 are connected to ground.
- Signal Lines:**
 - RIN0, GIN0, BIN0, SOGIN0:** These signals are connected to pins 1, 2, 3, and 4 respectively. They pass through resistors R1, R2, R3, and R4 (100Ω) and capacitors C1, C2, C3, and C4 (0.047μF).
 - RINOM, GINOM, BINOM:** These signals are connected to pins 5, 6, and 7 respectively. They pass through resistors R10, R11, and R12 (100Ω) and capacitors C8, C9, and C10 (0.047μF).
 - HSYNC0, VSYNC0:** These signals are connected to pins 8 and 9 respectively. They pass through resistors R13 and R14 (100Ω) and capacitors C11 and C12 (20pF).
 - DDC_CLK, DDC_DAT, ST_DET1:** These signals are connected to pins 10, 11, and 12 respectively. They pass through resistors R19, R20, and R21 (100Ω).
- Diodes and Transistors:** Diodes D1, D2, D3, D4, D5, D6, D7, and D8 are connected to various pins. Transistors T1, T2, T3, and T4 are connected to pins 1, 2, 3, and 4 respectively.
- Resistors:** R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, and R21 are connected to various pins.

!!

Title		
D_SUB		
Size	Document Number	Rev
A3	<Doc>	<RevCode>
Date:	Saturday, July 05, 2003	Sheet 1 of 6

DVI

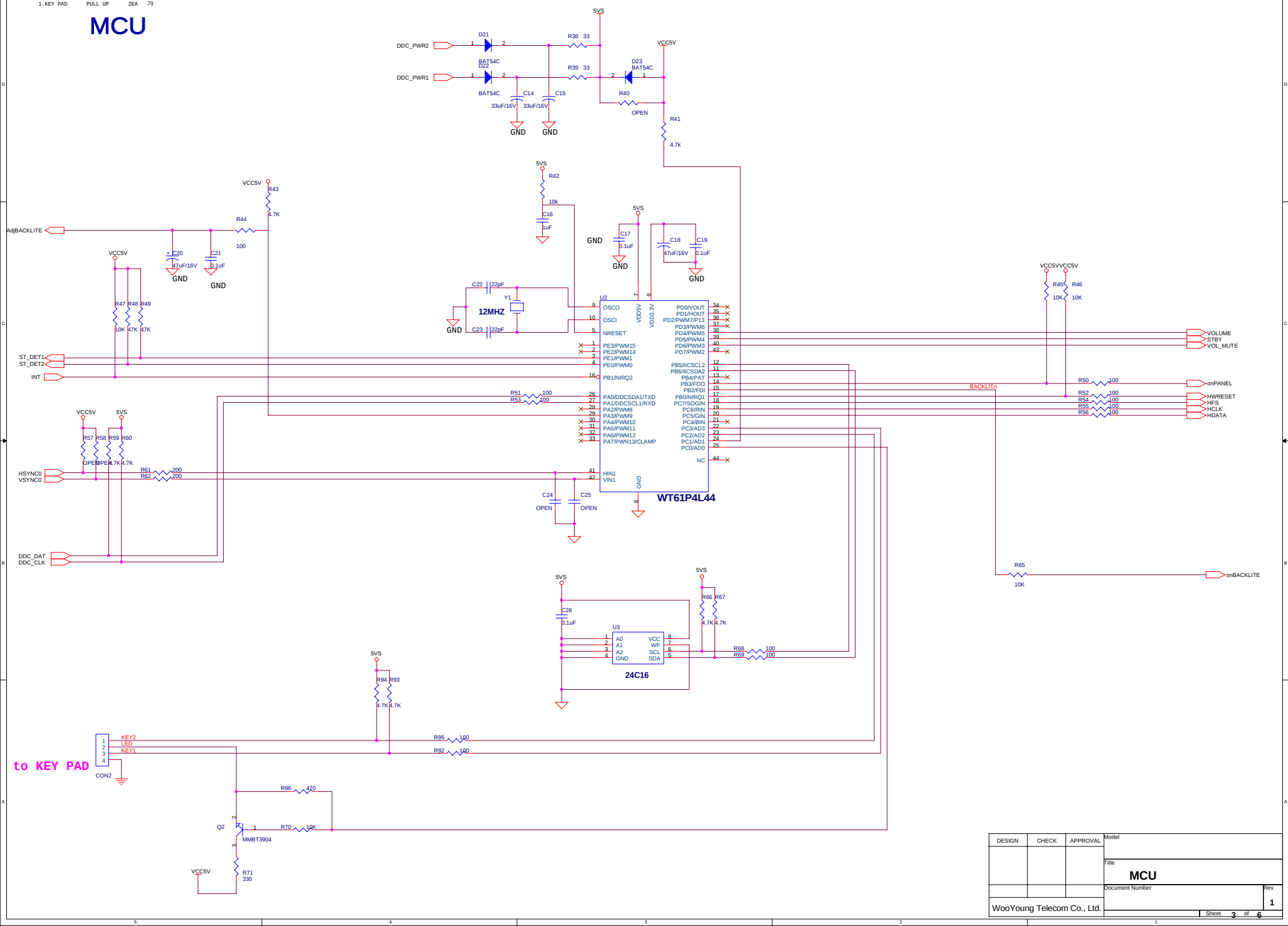
1. ST_DET2 DVI PIN
1. 24LC21 VCLK



Title			DVI
Size	Document Number	Rev	<RevCode>
A3	<Doc>		
Date:	Saturday, July 05, 2003	Sheet	2 of 6

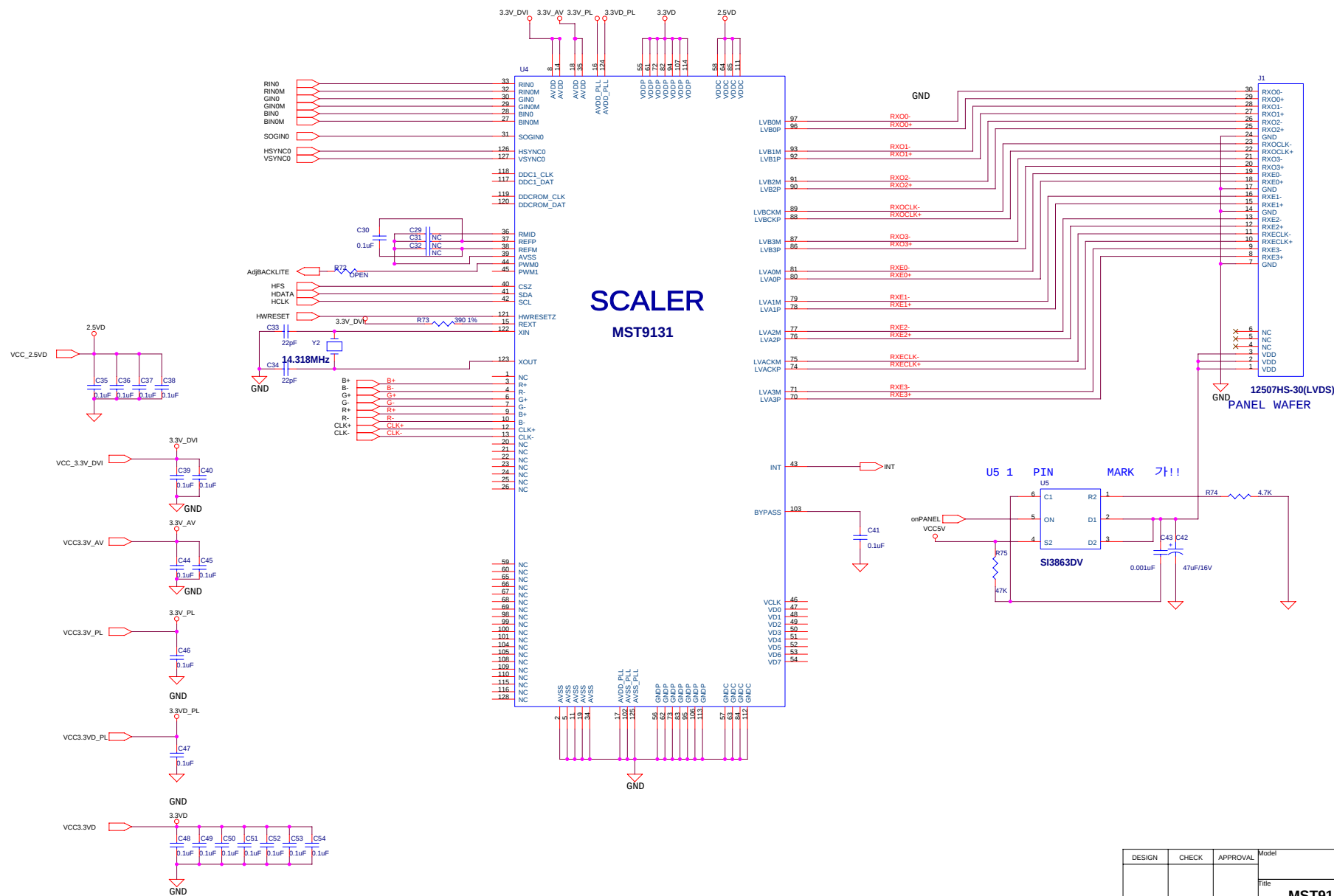
1. R43 !!
1. KEY PAD PULL UP 2EA 가}

MCU

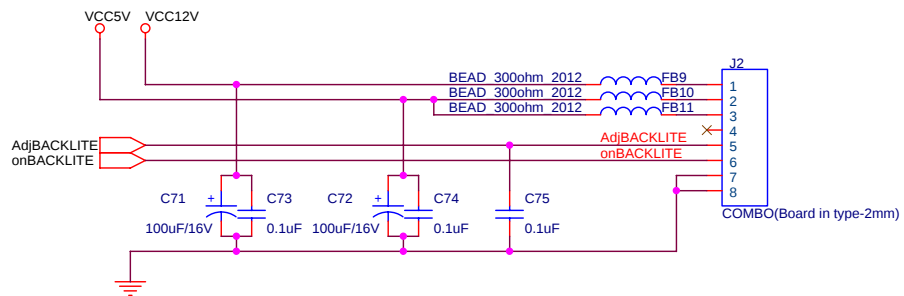
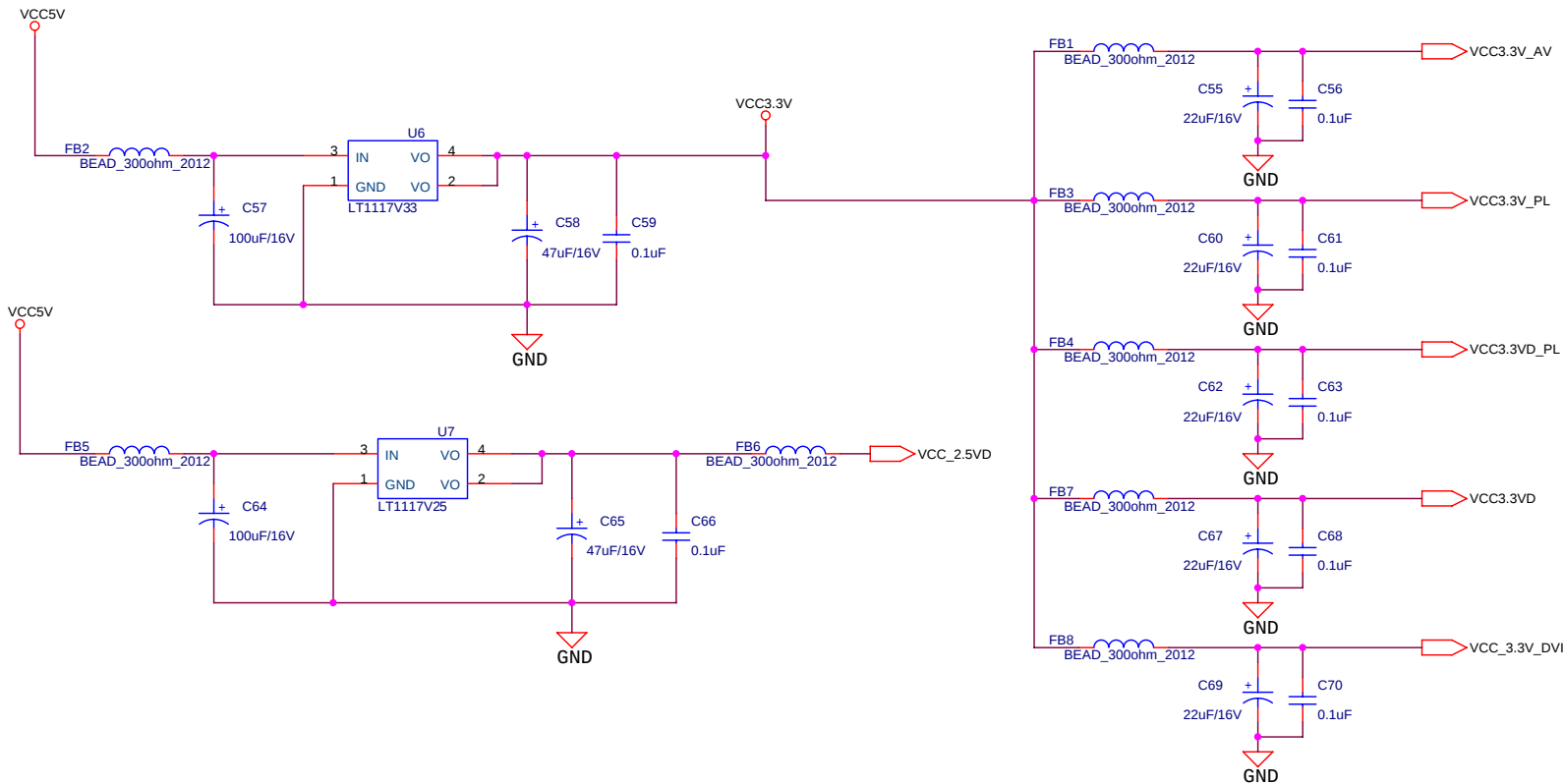


DESIGN	CHECK	APPROVAL	Model
			Title
			MCU
			Document Number
			Rev
			1
WooYoung Telecom Co., Ltd.			Sheet 3 of 6

SCALER



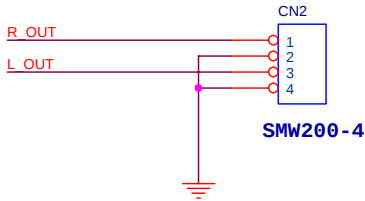
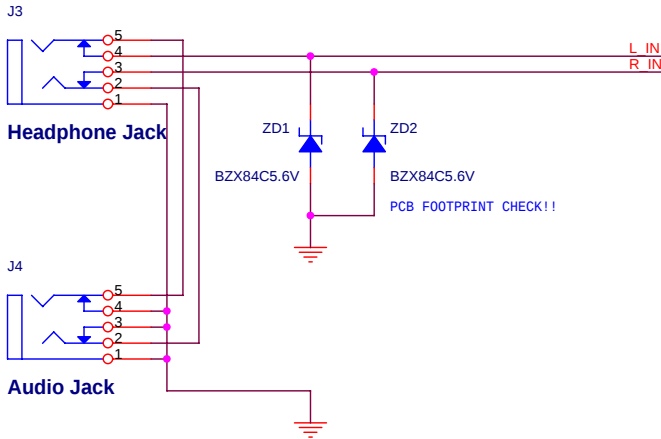
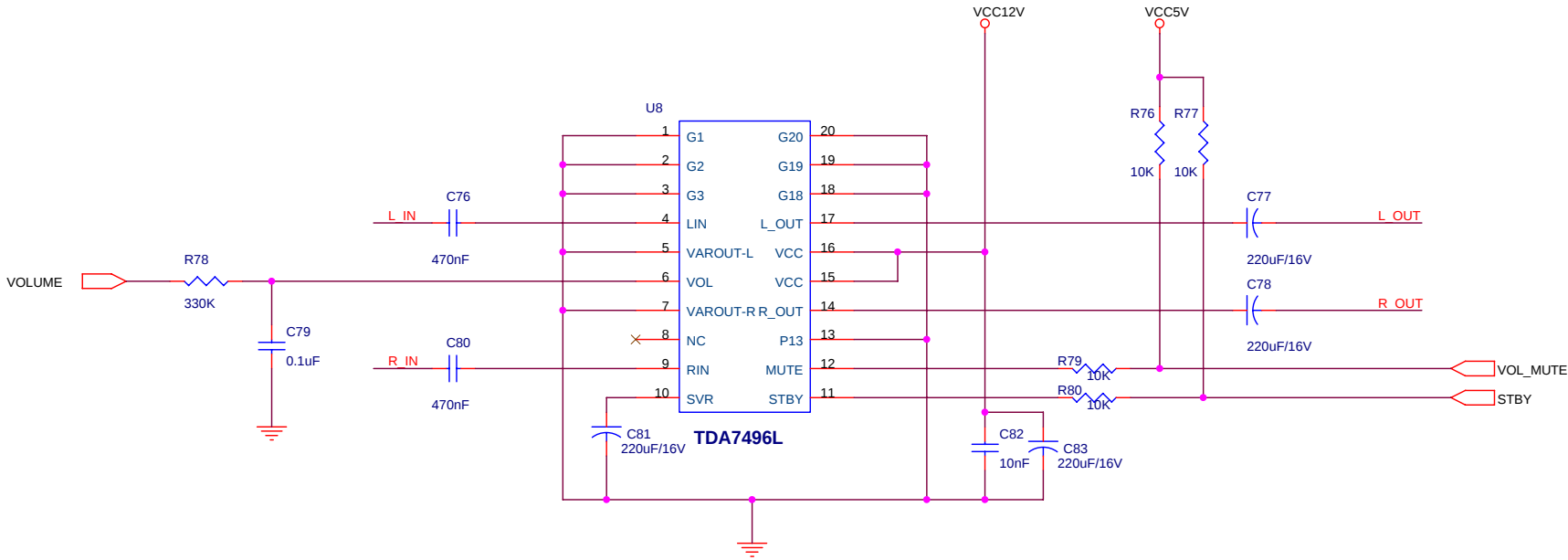
DESIGN	CHECK	APPROVAL	Model
			Title
			MST9111 & PANEL
			Document Number
			97E0D0220Y
			Sheet 4 of 6
			1



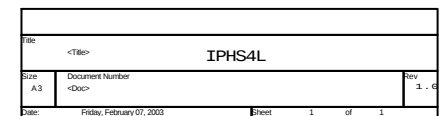
DESIGN	CHECK	APPROVAL	Model
			Title
			Document Number
			Rev
WooYoung Telecom Co., Ltd.			1
Sheet 5 of 6			

INVERTER & POWER

AUDIO



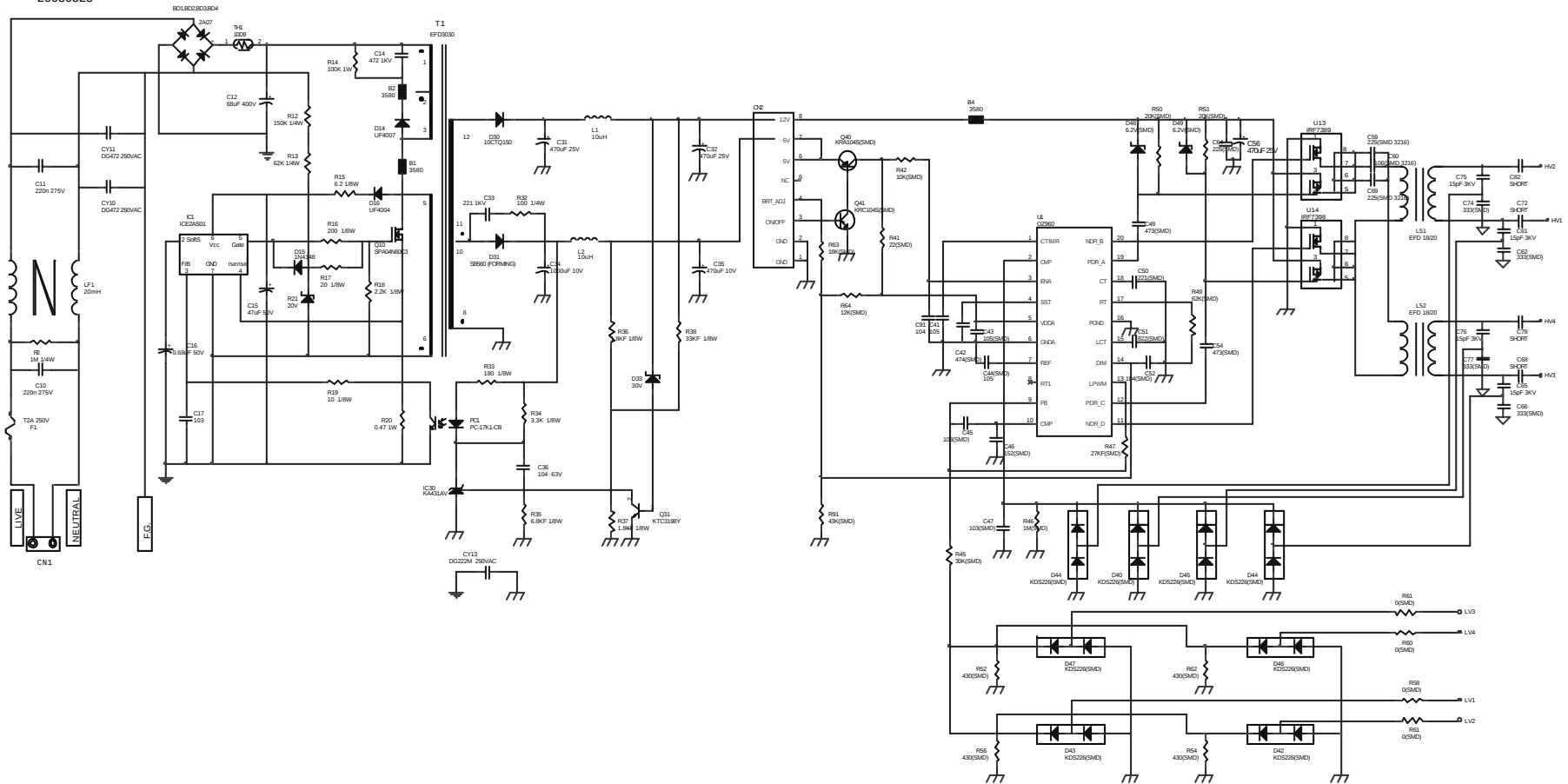
DESIGN	CHECK	APPROVAL	Model	B17DF	
			Title	AUDIO	
			Document Number		Rev
WooYoung Telecom Co., Ltd.					1
				Sheet	6 of 6



- * ON/OFF : ON(5VDC)
OFF(OV)
- * Vin : 11.4-12.6Vdc
- * B'CONT' : 0-4.5VMax



20030528



Title			
IPHS4L-S			
Size A2	Document Number <Doc>	Rev	
Date:	Wednesday, May 28, 2003	Sheet	1 of 1

20030502

