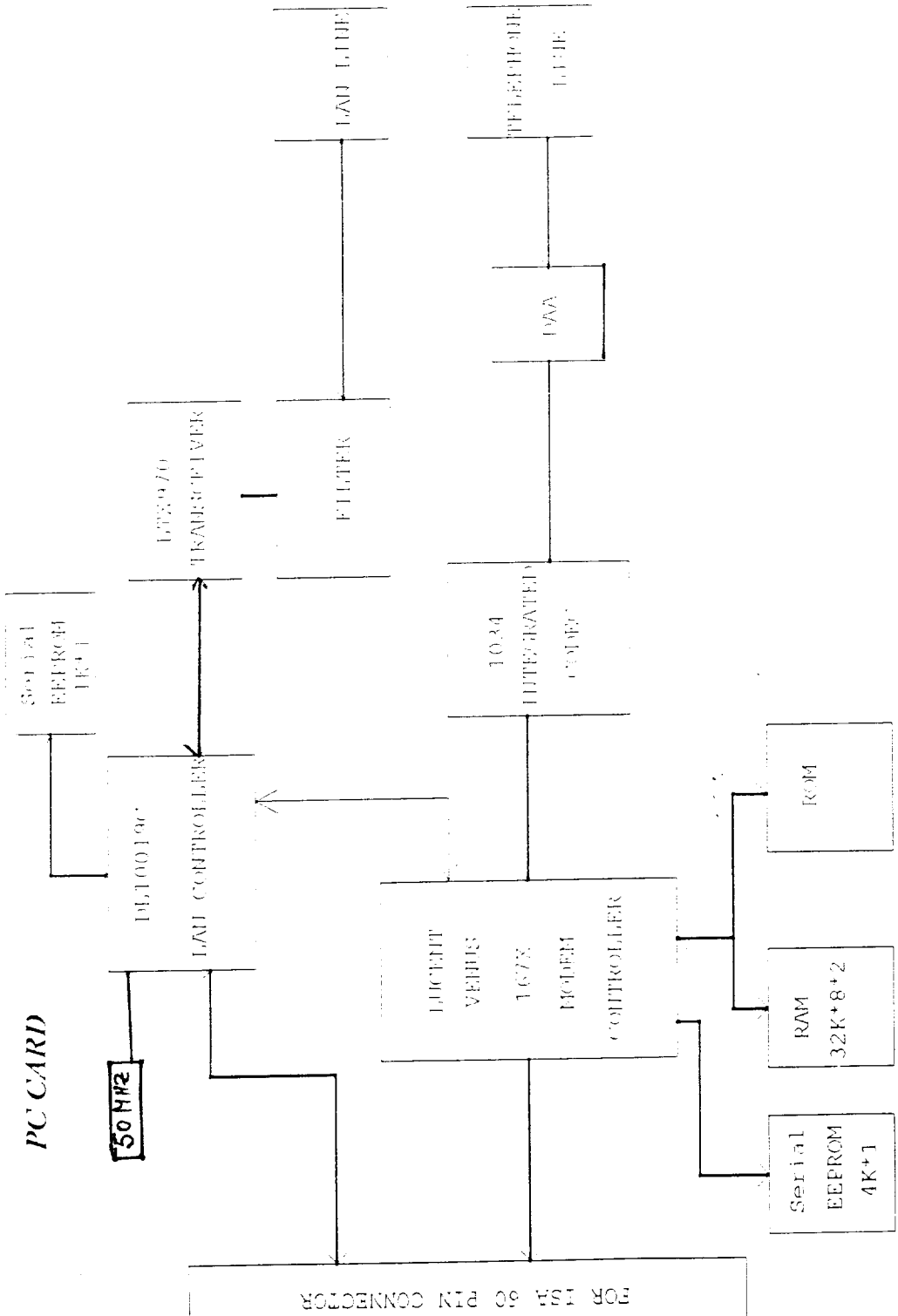


PC CARD

50 MHz

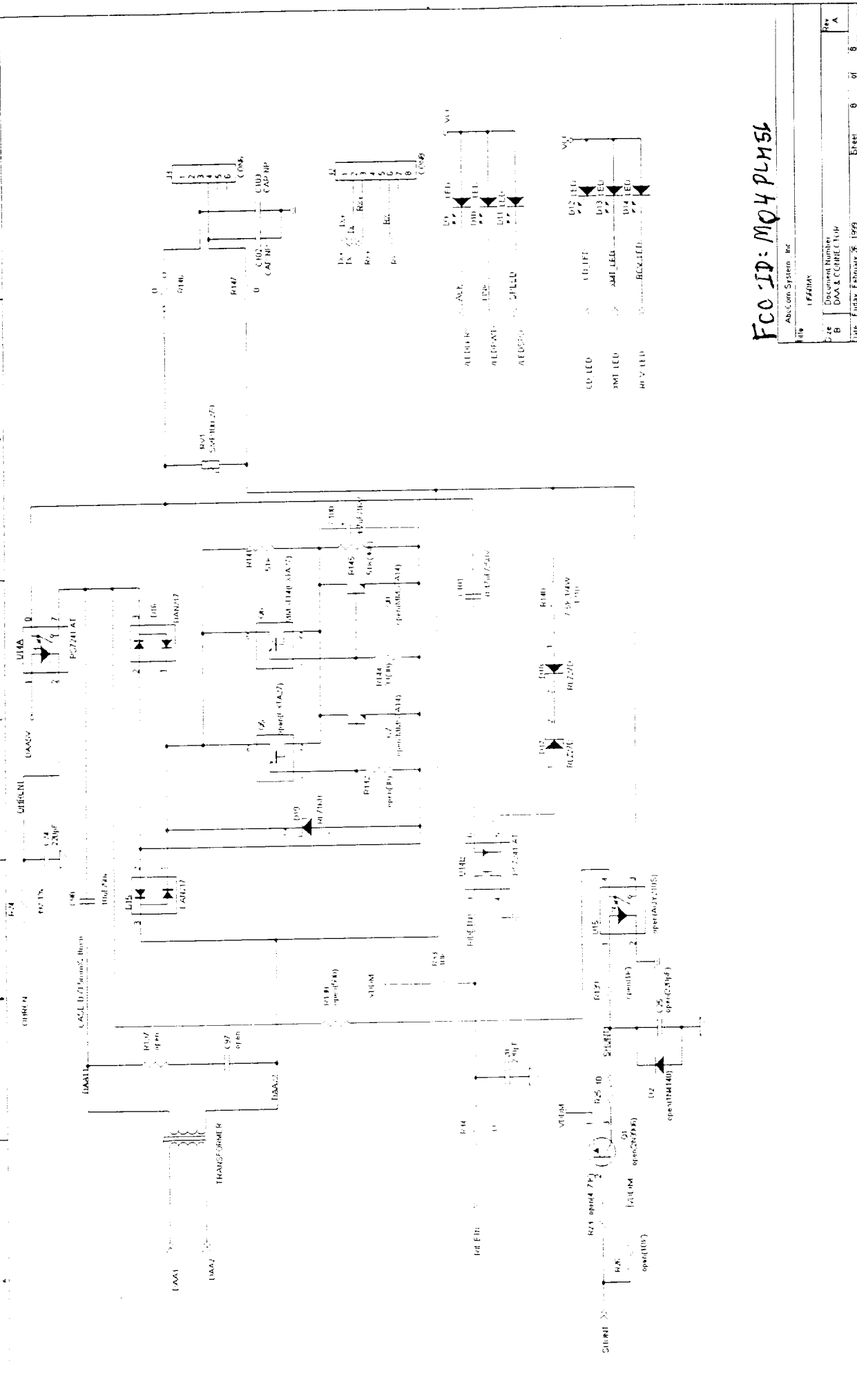
10,000,000



FOR ISA 60 PIN CONNECTOR

Access System, Inc.
 File: 1671M2
 Document Number: BUENY-106CR004
 Date: Thursday, April 08, 1999
 Page: 2 of 8
 Rev: A

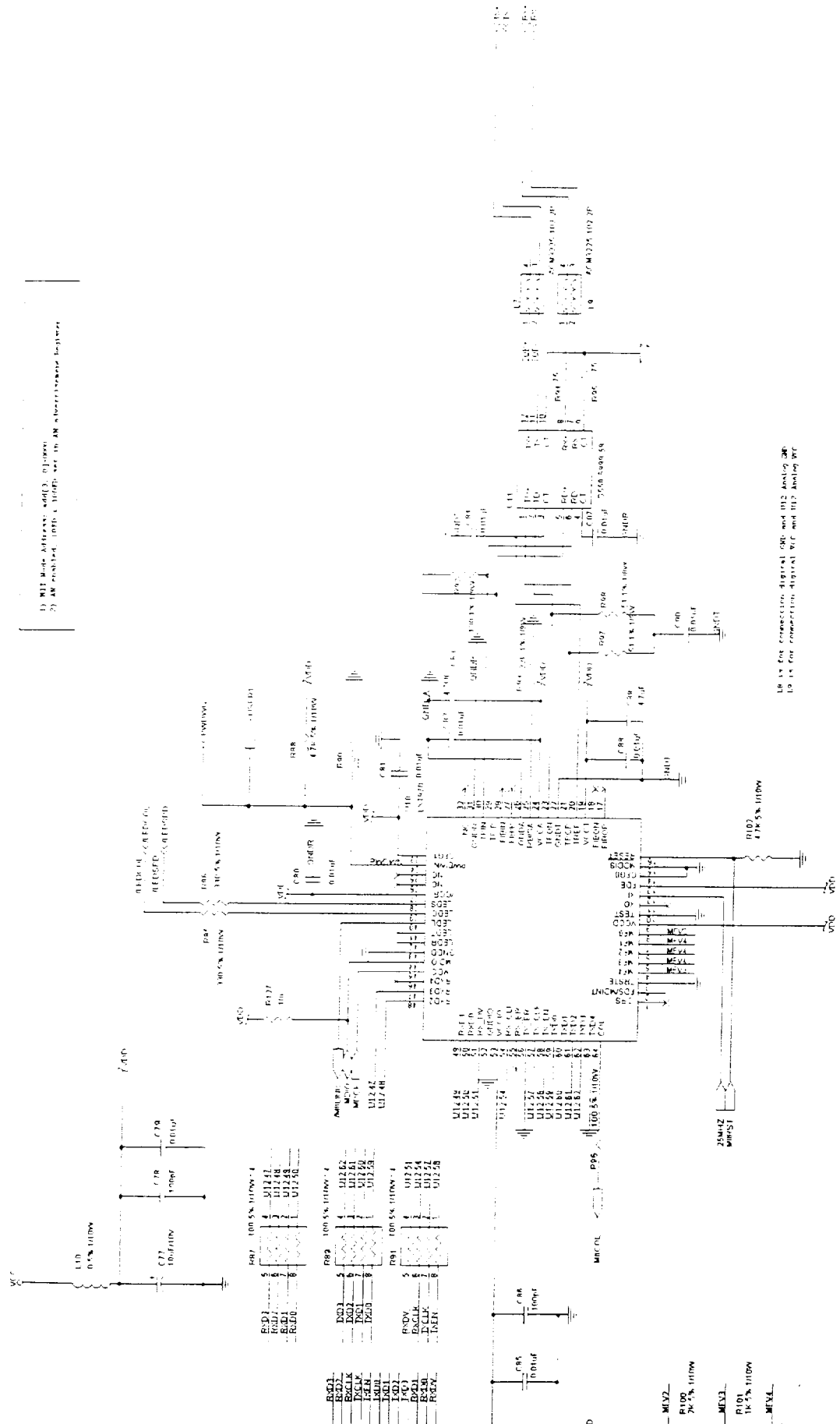
ECID: M04PLM56



FCO ID: MQ4 PLM56

File	AbCom System, Inc
1	1 FABR
2	Document Number
3	DAA & CONNECTION
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1) B12 Made Address: add3, of 1000
 2) AN: address: 1000, of 1000, set to AN: address: 1000



10 is for connecting digital sig and 012 Analog sig
 10 is for connecting digital sig and 012 Analog sig

DATE: 1981-04-15

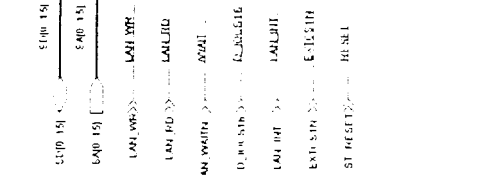
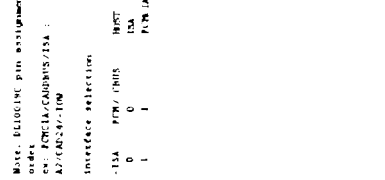
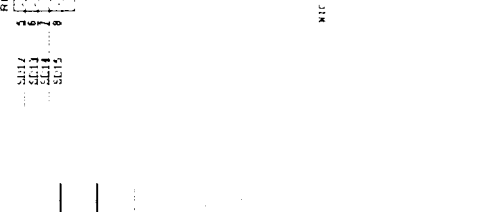
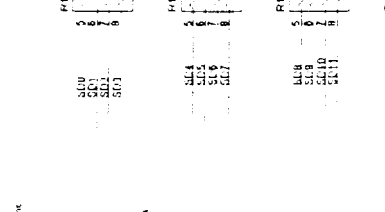
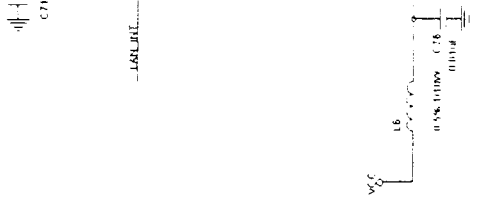
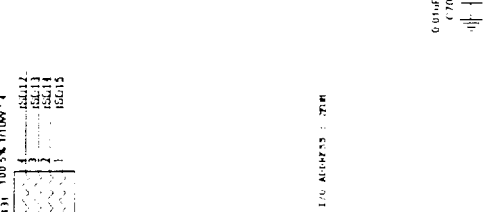
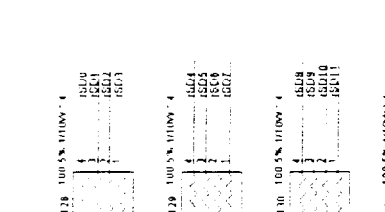
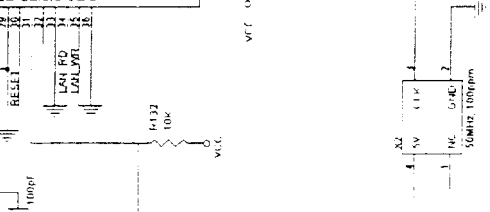
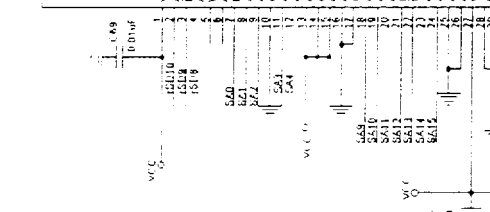
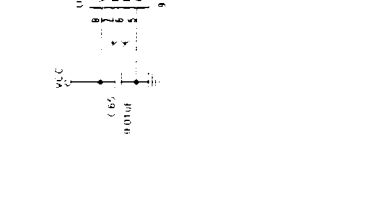
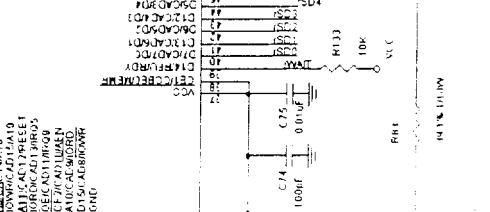
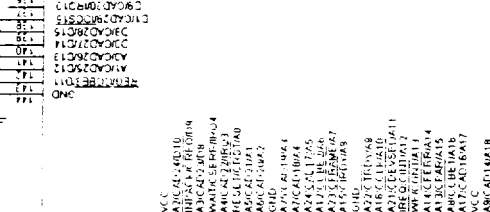
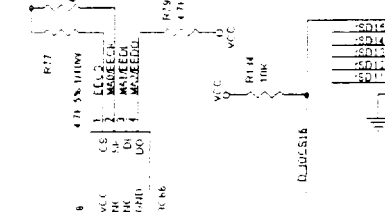
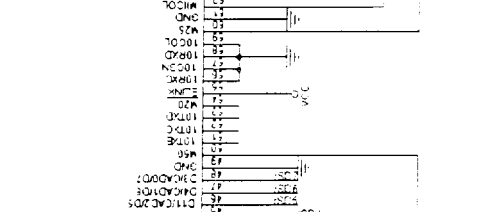
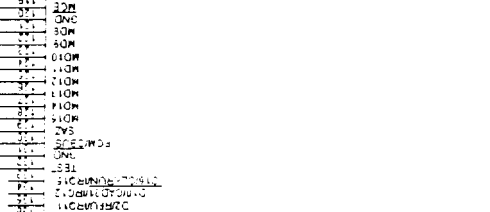
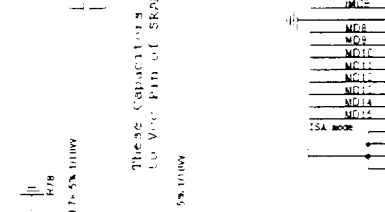
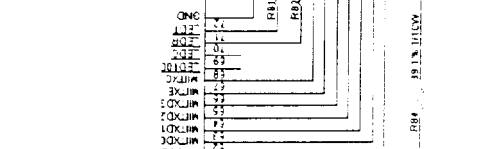
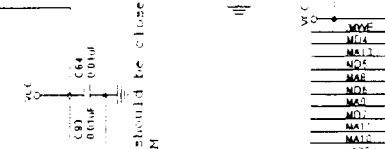
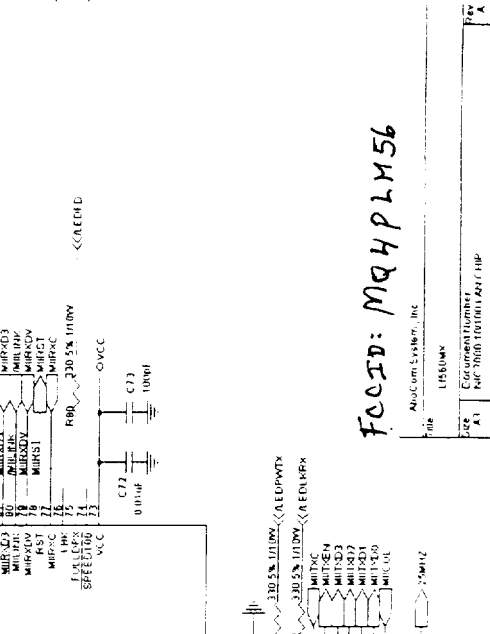
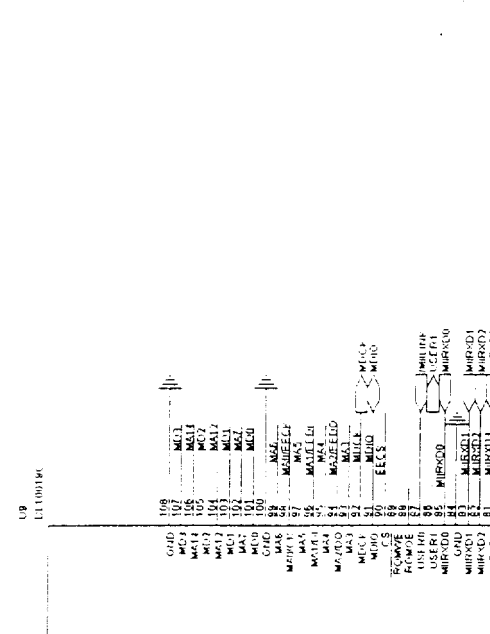
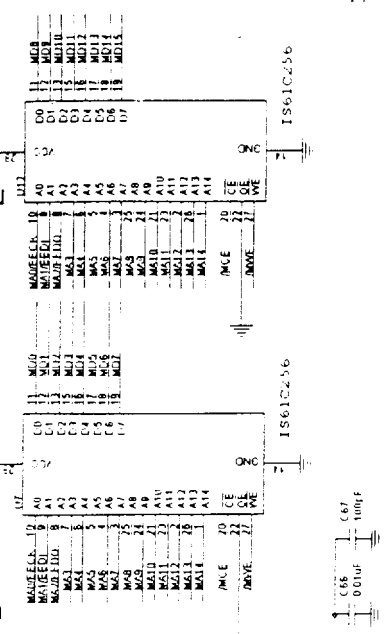
DESIGNER: J. L. BARNETT

INSTRUMENTATION: ...

TEST: ...

EXTENSION: ...

ST. REGISTRATION: ...

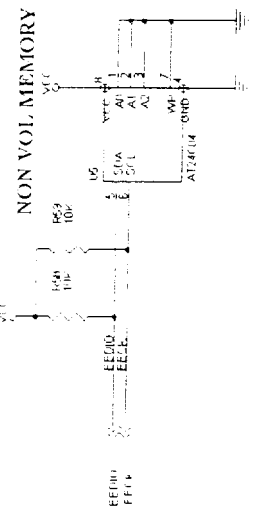
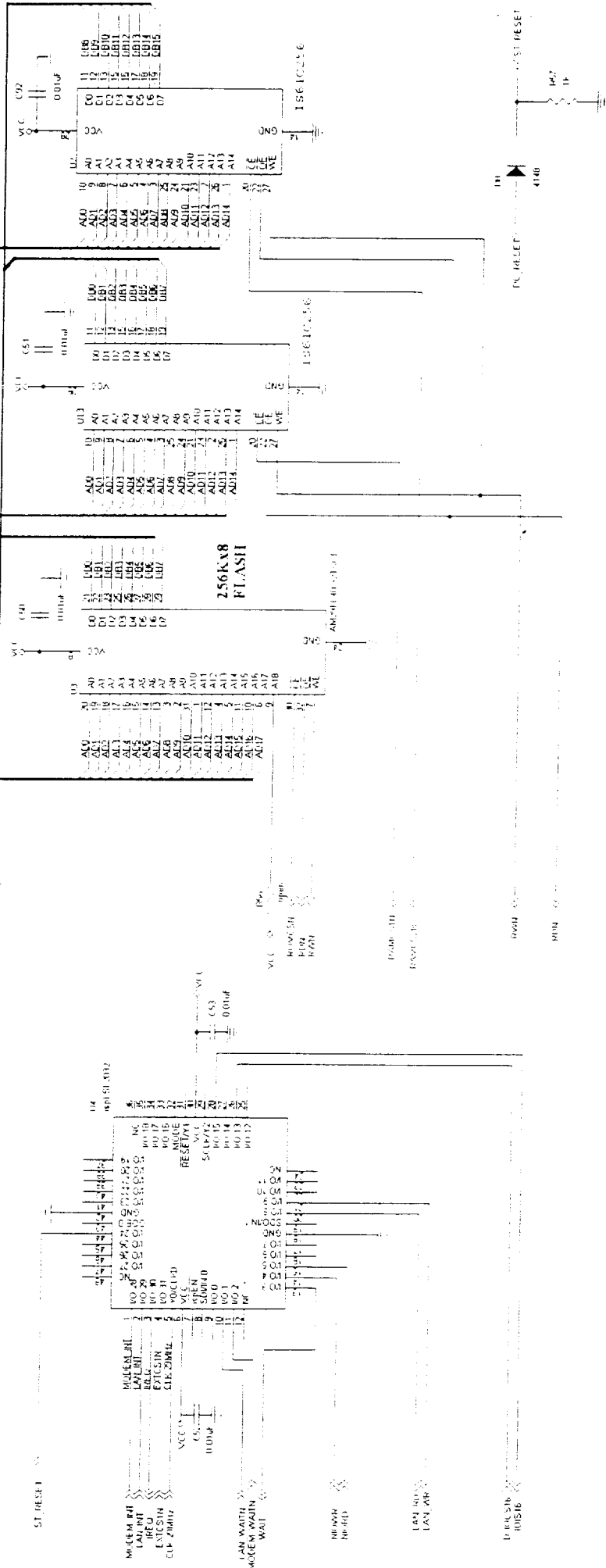


FEED: MQ4PLM56

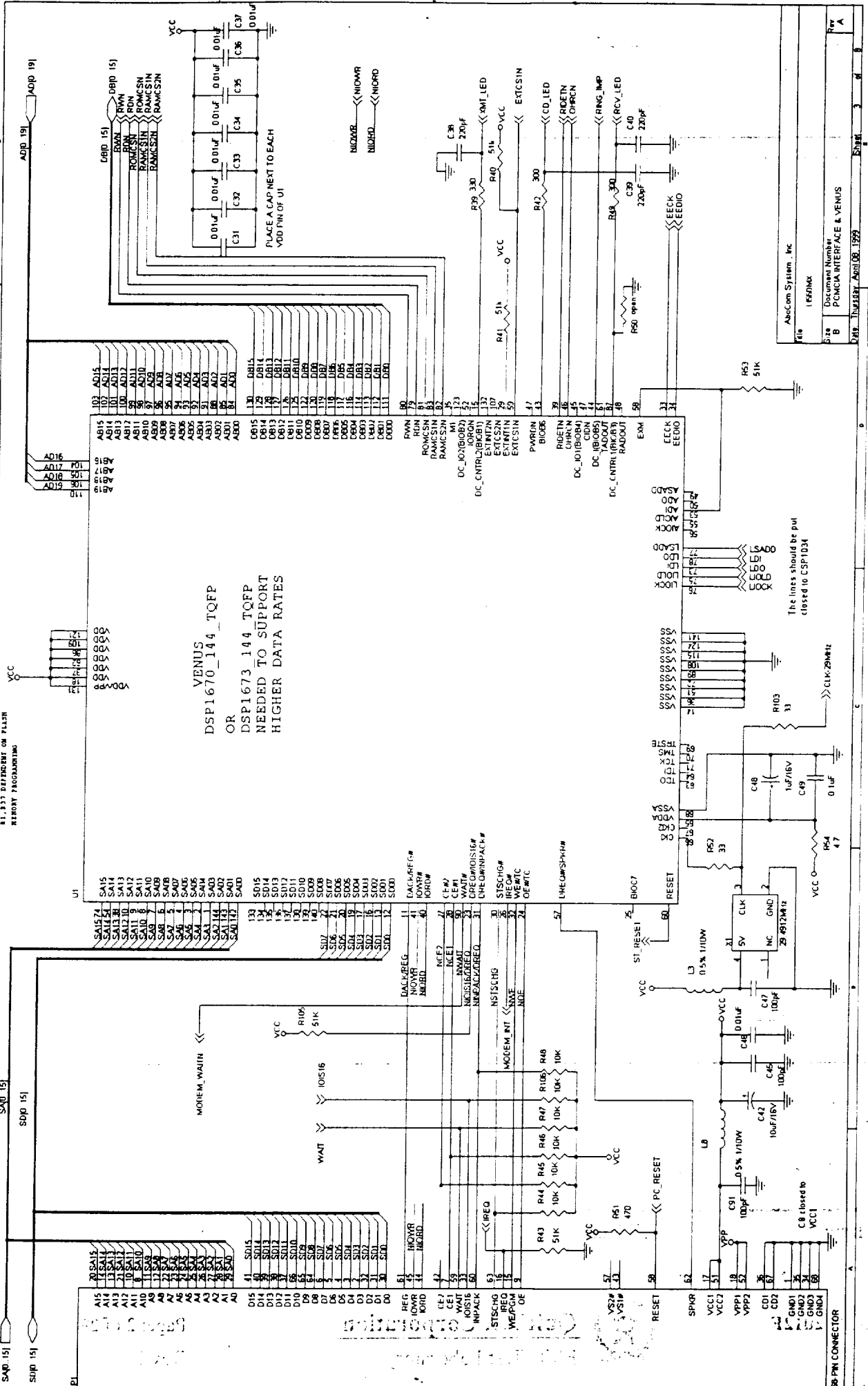
Part	MS1000
Component Number	MS1000-100
Rev	A
Date	1981-04-15

A B C D E

April 19, 1999
 1088141



11.137 DEPENDENT ON FLASH MEMORY PROGRAMMING



VENUS
 DSP1670_144_TQFP
 OR
 DSP1673 144_TQFP
 NEEDED TO SUPPORT
 HIGHER DATA RATES

File	AbicCom System, Inc
Size	162KMB
Docu. Number	PCMCIA INTERFACE & VENUS
Date	THURSDAY, APRIL 06, 1999

Pin	1	2	3	4	5	6	7	8
Per	A							

The lines should be put closed to CSP1034

VS2# VS1#

RESET

SPKR

VCC1

VPP1

VPP2

CD1

CD2

GND01

GND02

GND03

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

VCC1

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