



WLN1501 Circuit Description

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Operational Description

The WLN1501 Wi-Fi[®] Adapter Sled utilizes a highly integrated transceiver that provides virtually all radio functions.

Circuitry & Devices Provided for Determining & Stabilizing Frequency

System Reference Clock:

The WiFi module generates all internal clocks from a 44.000 MHz external reference clock. The 44 MHz reference clock is a crystal Colpitts oscillator.

Circuitry & Devices Provided for Suppression of Spurious Radiation

All RF circuitry is contained within the WiFi module. A quarter-wave linear antenna is the only external RF component in the design.

Circuitry & Devices Provided for Limiting Modulation

All modulation waveforms are generated internal to the WiFi module.

Circuitry & Devices Provided for Limiting Power

All power control is implemented internal to the WiFi module.

Circuitry & Devices Provided for USB communications

The USB interface is accomplished by using a single chip USB controller with integrated transceiver. The controller utilizes a 6 MHz reference oscillator to generate an internal clock of 48MHz to meet the USB 2.0 specification data rate of up to 12Mbps. Data rates are < 1 Mbps for this application.