

**REPLY TO REQUEST FOR ADDITIONAL INFORMATION
INET SPIDER II MODEM
FCC ID MIVWG9701A**

SECOND INFORMATION REQUEST

August 24, 1998

This document has been compiled to reply to a series of questions posed by the FCC as part of the Equipment Authorization Process for the Inet Spider II Modem (FCC ID MIVWG9701A). Each of the questions listed by the FCC is listed prior to the reply.

1) Please provide a plot of the deviation versus input signal level.

Answer

This plot is shown below:

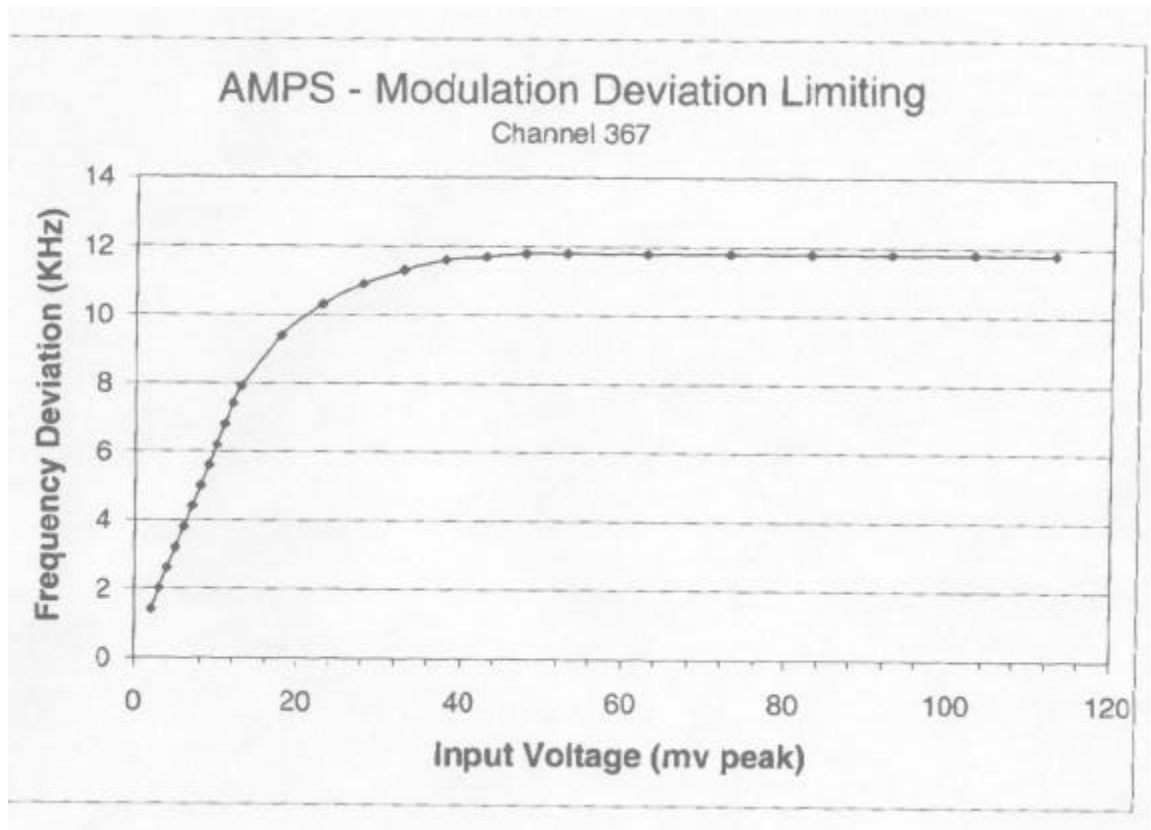


FIGURE 1 MODULATION VERSUS INPUT SIGNAL RESPONSE

2) The audio filter response requested earlier (showing compliance with 22.915(d)(1)) is for the post limiter filter. The test description from the previous reply indicates that the generator was connected to the audio port.

Answer

The Spider II uses an integrated DSP chipset for the signal processing & generation functions. The post-limiter filter is embedded into a Texas Instruments TCM8030 Baseband Signal Processor chip between the modulation limiter and the modulator. A block diagram of the transmit audio path portion of this circuit is shown in Figure 2. The post limiter audio filter is shown as TXLPF. The frequency response of this component (as obtained from Texas Instruments) is shown in Figure 3. The curves shown in Figure 3 are compliant with the specifications of §22.915(d)(1).

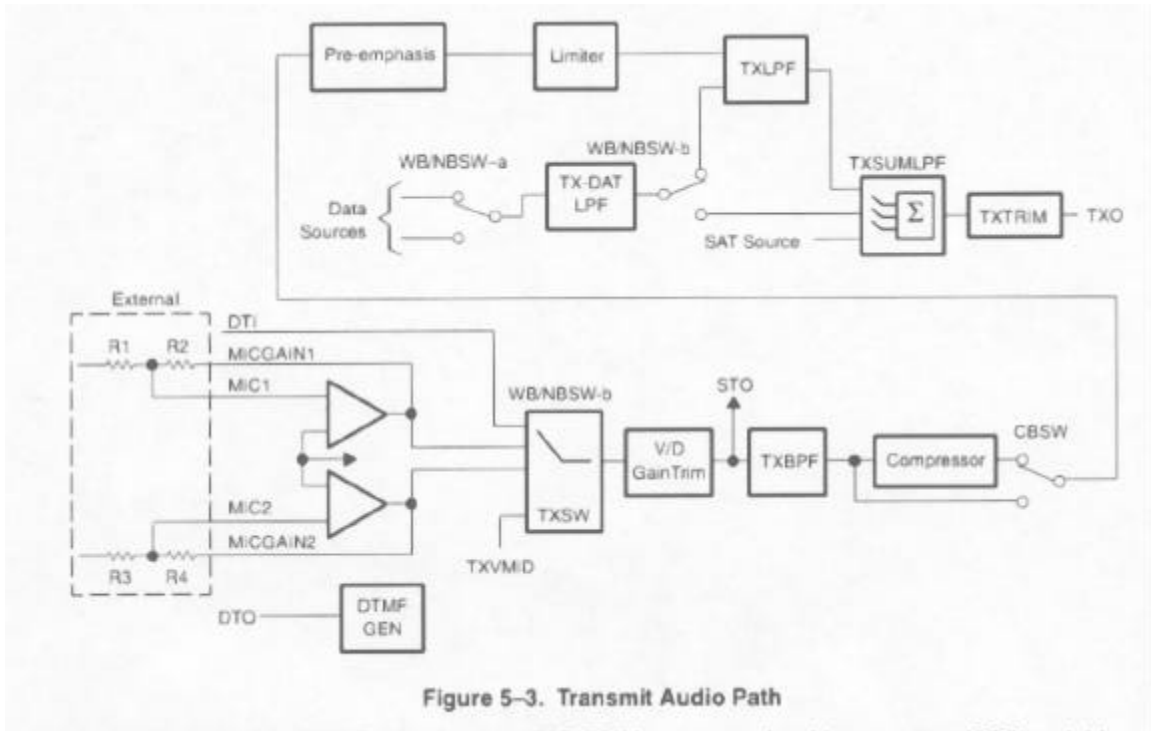


FIGURE 2 TRANSMIT AUDIO SIGNAL PATH

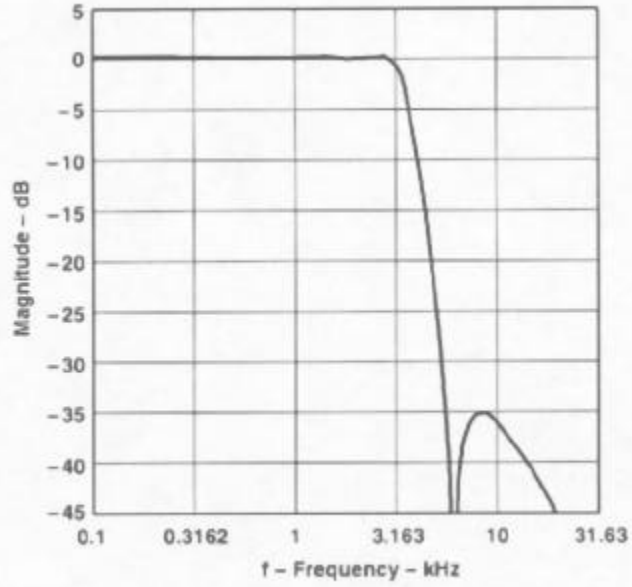


Figure 4-7. TXLPF Frequency Response

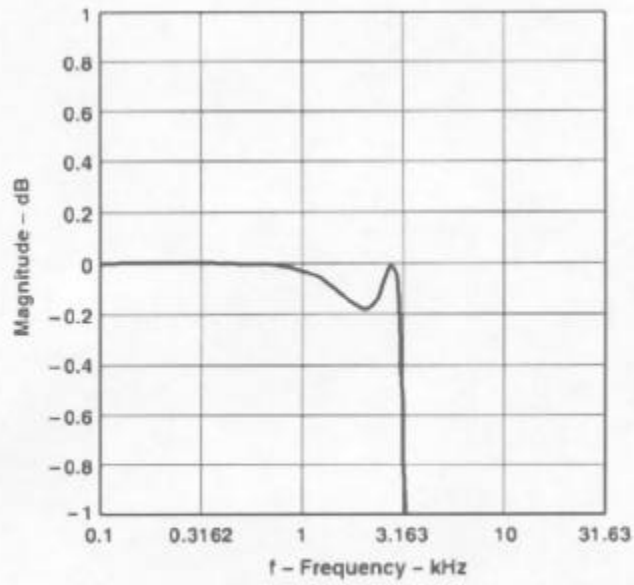


Figure 4-8. TXLPF Frequency Response

FIGURE 3 TRANSMIT AUDIO POST LIMITER FILTER RESPONSE