
AEGIS LABS, INC.

October 24, 2000

Equipment Authorization Branch
Office of Engineering and Technology Laboratory
Federal Communications Commission
7435 Oakland Mills Rd.
Columbia, MD 21046-1609

Re: Correspondence Reference Number: 16209

Dear Mr. Dichoso,

Please find following summary helpful in clarifying our responses to your processing needs:

ITEM 1 Block Diagram uploaded on FCC web site October 24, 2000

ITEM 2 Schematic uploaded on FCC we site October 24, 2000.

ITEM 3 External photos and internal photos of both sides of all circuit board provided with original
submittal under the attachment "Test Report with pertinent attachments" (Section 6) uploaded to
FCC Web site on Wed. Aug 2 17:34:38 EDT 2000.

ITEM 4 Manual uploaded on FCC web site October 24, 2000

ITEM 5 The answer is: Single Channel as per "Product Technical Reference Note" uploaded on FCC we site
October 24, 2000.

ITEM 6 The answer is: Maximum drive and test level used is 25 Watts (44dBm).

ITEM 7 The answer is: A 2400 Hz test tone was used for the modulation because it provides a worst-case
occupied bandwidth scenario.

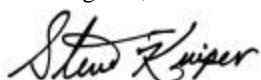
ITEM 8 Refer to "Test Report with pertinent attachments" page B15 for input drive level and page B16 for
output level. Refer to Test Report pages B10, B14 and B18 for output plots @ 200 KHz spans.

ITEM 9 Refer to "Test Report with pertinent attachments"page B15 for input drirve level of +43.3 dBm and
page B16 for output level of +49.4dBm gain. Gain is equal to 49.4 dBm minus 43.3 dBm = 6.1 dBm

Please take the time to confer with your colleagues on this matter, and if there is additional clarification that
doesn't meet the FCC criteria please feel free to provide us with a response at your earliest convenience.

The balance of all the information requested has been uploaded as per correspondence reference number
16209.

Kind regards,



Steve Kuiper
President

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