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Preliminary Datasheet

**BCM4324 WLAN +BT Combo Module
(Foxconn T77H506.00 035)
Product Specification
Rev 0.7**

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1 Revision History

Date	Change Note	Author	REV Note
2013-05-07	Initial release	Wilmer	0.1
2013-05-20	Modify the RX performance result	Wilmer	0.2
2013-05-23	1. Update the TX and RX performance result 2. Modify some pin description. 3. Add Shielding case drawing	Wilmer	0.3
2013-07-02	1. Update the TX and RX SPC in section 3.4 and 3.5. 3. Add interface timing in section 3.6 4. Update the Mechanical drawing in section 4 5. Add the schematics reference design in section 5 6. Add the software support in section 7 7. Add Regulatory in section 8 8. Add environment specification in section 9 9. Add package information	Wilmer	0.4
2013-07-09	1. Update the module mechanical drawing in section 4	Wilmer	0.5
2013-07-27	1. Replace mechanics drawing adding four corners' footprint dimension in Section 4	Wilmer	0.6
2013-08-02	Add FCC Power Table	Wilmer	0.7



2. Introduction

Project Name: 802.11abgn (2X2) + BT4.0 combo module

This documentation describes the product specification of the WLAN+BT combo Module. WLAN is Compliant with IEEE 802.11 a/b/g and 2x2 IEEE 802.11n MAC/ baseband/radio, Bluetooth is compliant with Bluetooth 4.0+HS. This module takes advantage of the high throughput and extended range of Broadcom second-generation MIMO solution. It is a confidential document of Foxconn.

* For b/g/n and a/b/g/n module, Foxconn HW/FW is the same, platform use different firmware and driver to enable or disable 5GHz

2.1 Scope

This combo Module combines WLAN, Bluetooth in one module, the WLAN is available in the 2.4GHz and 5GHz band, it is compatible with the IEEE 802.11 a/b/g standard and the 802.11n standard. It allow user to switch to different vendors' Access Points through the wireless networks and to prevent from eavesdropping. The 802.11 a/g data rate provides for 54, 48, 36, 24, 18, 12, 9, 6Mbps, 802.11b data rate provides for 11, 5.5, 2, 1 Mbps, it can also support 11n high data rate up to MCS15(HT40) with PHY data rate to 300Mbps by dual stream. Bluetooth is compliant to Core Specification version 4.0.

2.2 Function

- Single stream 802.11n support for 20MHz channels provide PHY Layer rates up to 72Mbps.
- Dual stream 802.11n support for 20MHz/40MHz channels provide PHY Layer rates up to 300Mbps.
- Bluetooth supports Class 1 and Class 2 output power.
- Diplexer (which reject 2170MHz) integrated
- Provides a small form factor solution and ultra low power consumption to support low cost requirement.
- Host interface supports:
 - WLAN: SDIO;
 - BT data: UART
 - BT digital audio: PCM

3. Product Specification

3.1 Hardware Characteristic

Form factor	13mmx17mmx1.5mm LGA
Host Interface	WLAN: SDIO BT: UART for data, PCM for Audio
PCB	6-layer HDI design
RF connector	Two MHF4 RF connector on module

3.2 Hardware Architecture

The WLAN+BT combo module is designed base on BROADCOM BCM4324 chip, the Broadcom BCM4324 is a highly integrated single chip solution for single and dual stream dual-band WLAN and BT4.0. Single and dual Antenna support, a 37.4MHz crystal is used for reference clock generation, see the block diagram as below:

3.3 Electrical Specification

Absolute Maximum Ratings

These specifications indicate levels where permanent damage to the device can occur. Functional operation is not guaranteed under these conditions. Operation at absolute maximum conditions for extended can adversely affect long-term reliability of the device.

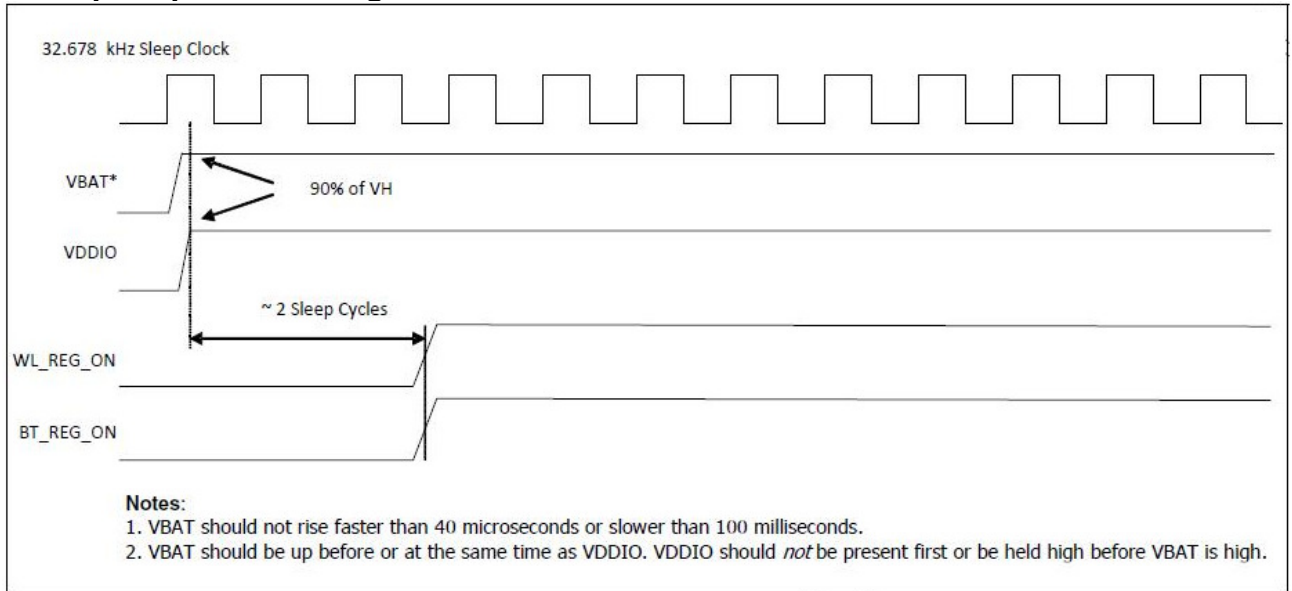
Rating	Symbol	Value	Unit
DC supply voltage for the device	3.3V	2.3 to 4.8	V

Recommended Operating Condition

Element	Symbol	Value			Unit
		Minimum	Typical	Maximum	
DC supply voltage for the device	3.3V	3.0	3.3	3.6	V

Function operation is not guaranteed outside this limit, and operation outside this limit for extended periods can adversely affect long-term reliability of the device.

Power-Up Sequence Timing



WLAN = ON, BT = ON

Current Consumption

* Note: it would be updated after EDVT testing.

3.4 WLAN RF Characteristics

3.4.1 IEEE802.11b

Parameter	Condition	Min.	Type	Max.	Units
Target Power	CH1~CH13, 1~11Mbps	16.5	18.5	19.5	dBm
Spectrum Mask	fc-22MHz<f< fc-11MHz fc+11MHz<f<fc+22MHz			-30	dBr
	f<fc-22MHz f>fc+22MHz			-50	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
OBW	--			26	MHz
Spread Bandwidth	--	7			
RF Carrier Suppression	Data=0101 and DQPSK modulation	15			dB
Transmit power –on ramp	10% to 90% of max power			2	us
Transmit power –down ramp	90% to 10% of max power			2	us
Center Frequency Tolerance		-20		20	ppm
EVM (peak)	1Mbps			35	%
	2Mbps			35	
	5.5Mbps			35	
	11Mbps			35	
Receiver Minimum Input Level Sensitivity at Antenna	1 Mbps		-97	-94	dBm
	2 Mbps		-94.5	-92	
	5.5 Mbps		-92.5	-90	
	11 Mbps		-89	-86	
Adjacent Channel Rejection	11Mbps	35			dB
Receiver Maximum Input Level	ALL	0			dBm

3.4.2 IEEE802.11g (SISO)

Parameter	Condition	Min.	Type	Max.	Units
Target Power	CH1~CH13,6~24Mbps	15	17	18	dBm
	CH1~CH13, 36~54Mbps	14	16	17	
Spectrum Mask	+/- 11MHz			-20	dBr



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	+/- 20MHz			-28	
	+/- 30MHz			-40	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/ Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
	1,574.4~1,576.4MHz			-145	
Output Center Frequency Tolerance	Overall	-20		+20	ppm
OBW	--			26	MHz
Output Center Frequency Leakage	--			-15	dB
Output Spectrum Flatness	In-band	-2		2	dB
	Out-band	-4		2	
EVM	6 Mbps			-5	dB
	9 Mbps			-8	
	12 Mbps			-10	
	18 Mbps			-13	
	24 Mbps			-16	
	36 Mbps			-19	
	48 Mbps			-22	
	54 Mbps			-25	
Receiver Minimum Input Level Sensitivity at the Antenna	6 Mbps		-93	-90	dBm
	9 Mbps		-91	-88	
	12 Mbps		-89.5	-87	
	18 Mbps		-87.5	-85	
	24 Mbps		-83.5	-81	
	36 Mbps		-81	-78	
	48 Mbps		-75.5	-73	
	54 Mbps		-74.5	-72	
Adjacent Channel Rejection	6 Mbps	16			dB
	9 Mbps	15			
	12 Mbps	13			
	18 Mbps	11			
	24 Mbps	8			
	36 Mbps	4			
	48 Mbps	0			
	54 Mbps	-1			
Receiver Maximum Input Level	ALL	-10			dBm
Receiver Spurious	<1GHz			-54	dBm
	1~10GHz			-54	
	10GHz~			-54	

3.4.3 IEEE802.11gn HT20 Single chain

Parameter		Min.	Type	Max.	Units
Target Power	CH1~CH13,MCS0~MCS4	14.5	16.5	17.5	dBm
	CH1~CH13,MCS5~MCS7	13.5	15.5	16.5	
Spectrum Mask	+/- 11MHz			-20	dBr
	+/- 20MHz			-28	
	+/- 30MHz			-45	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/ Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
	1,574.4~1,576.4MHz			-145	
Output Center Frequency Tolerance	Overall	-20		+20	ppm
OBW	--			26	MHz
Output Center Frequency Leakage	--			-20	dB
Output Spectrum Flatness	Inband	-2		2	dB
	Outband	-4		2	
EVM	MCS0 Mbps			-5	dB
	MCS1 Mbps			-10	
	MCS2 Mbps			-13	
	MCS3 Mbps			-16	
	MCS4 Mbps			-19	
	MCS5 Mbps			-22	
	MCS6 Mbps			-25	
	MCS7 Mbps			-28	
Receiver Minimum Input Level Sensitivity at the Antenna	MCS0 Mbps		-91	-88	dBm
	MCS1 Mbps		-88.5	-86	
	MCS2 Mbps		-86	-83	
	MCS3 Mbps		-82.5	-80	
	MCS4 Mbps		-79.5	-77	
	MCS5 Mbps		-75	-72	
	MCS6 Mbps		-73.5	-71	
Adjacent Channel Rejection	MCS0 Mbps	16			dB
	MCS1 Mbps	13			
	MCS2 Mbps	11			
	MCS3 Mbps	8			
	MCS4 Mbps	4			
	MCS5 Mbps	0			
	MCS6 Mbps	-1			
MCS7 Mbps	-2				
Receiver Maximum Input Level	ALL	-10			dBm
Receiver Spurious	<1GHz			-54	dBm
	1~10GHz			-54	
	10GHz~			-54	

3.4.4 IEEE802.11a

Parameter		Min.	Type	Max.	Units
Target Power	B1,B2,B3,B4,6~24Mbps	15.5	17.5	18.5	dBm
	B1,B2,B3,B4, 36~54Mbps	15.5	16.5	17.5	
Spectrum Mask	+/- 11MHz			-20	dBr
	+/- 20MHz			-28	
	+/- 30MHz			-40	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/ Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
	1,574.4~1,576.4MHz			-145	
Output Center Frequency Tolerance	Overall	-20		+20	ppm
OBW	--			26	MHz
Output Center Frequency Leakage	--			-15	dB
Output Spectrum Flatness	In-band	-2		2	dB
	Out-band	-4		2	
EVM	6 Mbps			-5	dB
	9 Mbps			-8	
	12 Mbps			-10	
	18 Mbps			-13	
	24 Mbps			-16	
	36 Mbps			-19	
	48 Mbps			-22	
	54 Mbps			-25	
Receiver Minimum Input Level Sensitivity at the Antenna	6 Mbps		-92	-89	dBm
	9 Mbps		-88.5	-86	
	12 Mbps		-86.5	-84	
	18 Mbps		-84.5	-82	
	24 Mbps		-81.5	-79	
	36 Mbps		-78.5	-76	
	48 Mbps		-74.5	-72	
Adjacent Channel Rejection	6 Mbps	16			dB
	9 Mbps	15			
	12 Mbps	13			
	18 Mbps	11			
	24 Mbps	8			
	36 Mbps	4			
	48 Mbps	0			
54 Mbps	-1				
Receiver Maximum Input Level	ALL	-15			dBm
Receiver Spurious	<1GHz			-54	dBm
	1~10GHz			-54	
	10GHz~			-54	

B1:5180~5240MHz;B2:5260~5320MHz;B3:5500~5700MHz;B4:5745~5825MHz

3.4.5 IEEE802.11an HT20 Single chain

Parameter		Min.	Type	Max.	Units
Target Power	B1,B2,B3,B4;MCS0~MCS4	14.5	16.5	17.5	dBm
	B1,B2,B3,B4;MCS5~MCS7	13.5	15.5	16.5	
Spectrum Mask	+/- 11MHz			-20	dBr
	+/- 20MHz			-28	
	+/- 30MHz			-45	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/ Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
	1,574.4~1,576.4MHz			-145	
Output Center Frequency Tolerance	Overall	-20		+20	ppm
OBW	--			26	MHz
Output Center Frequency Leakage	--			-20	dB
Output Spectrum Flatness	In-band	-2		2	dB
	Out-band	-4		2	
EVM	MCS0 Mbps			-5	dB
	MCS1 Mbps			-10	
	MCS2 Mbps			-13	
	MCS3 Mbps			-16	
	MCS4 Mbps			-19	
	MCS5 Mbps			-22	
	MCS6 Mbps			-25	
	MCS7 Mbps			-28	
Receiver Minimum Input Level Sensitivity at the Antenna	MCS0 Mbps		-91.5	-89	dBm
	MCS1 Mbps		-88.5	-86	
	MCS2 Mbps		-86.5	-84	
	MCS3 Mbps		-82.5	-80	
	MCS4 Mbps		-79.5	-77	
	MCS5 Mbps		-76	-73	
	MCS6 Mbps		-74	-71	
	MCS7 Mbps		-72.5	-70	
Adjacent Channel Rejection	MCS0 Mbps	16			dB
	MCS1 Mbps	13			
	MCS2 Mbps	11			
	MCS3 Mbps	8			
	MCS4 Mbps	4			
	MCS5 Mbps	0			
	MCS6 Mbps	-1			
	MCS7 Mbps	-2			
Receiver Maximum Input Level	ALL	-15			dBm
Receiver Spurious	<1GHz			-54	dBm
	1~10GHz			-54	
	10GHz~			-54	

B1:5180~5240MHz;B2:5260~5320MHz;B3:5500~5700MHz;B4:5745~5825MHz

3.4.6 IEEE802.11an HT40 Single chain

Parameter		Min.	Type	Max.	Units
Target Power	B1,B2,B3,B4;MCS0~MCS4	14.5	16.5	17.5	dBm
	B1,B2,B3,B4;MCS5~MCS7	13.5	15.5	16.5	
Spectrum Mask	+/- 11MHz			-20	dBr
	+/- 20MHz			-28	
	+/- 30MHz			-45	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-125	dBm/ Hz
	869~894MHz			-130	
	925~960MHz			-130	
	1,805~1,880MHz			-130	
	1,930~1,990MHz			-130	
	2,110~2,170MHz			-130	
	1,574.4~1,576.4MHz			-145	
Output Center Frequency Tolerance	Overall	-20		+20	ppm
OBW	--			26	MHz
Output Center Frequency Leakage	--			-20	dB
Output Spectrum Flatness	In-band	-2		2	dB
	Out-band	-4		2	
EVM	MCS0 Mbps			-5	dB
	MCS1 Mbps			-10	
	MCS2 Mbps			-13	
	MCS3 Mbps			-16	
	MCS4 Mbps			-19	
	MCS5 Mbps			-22	
	MCS6 Mbps			-25	
	MCS7 Mbps			-28	
Receiver Minimum Input Level Sensitivity at the Antenna	MCS0 Mbps		-89	-86	dBm
	MCS1 Mbps		-85	-82	
	MCS2 Mbps		-81	-78	
	MCS3 Mbps		-77	-74	
	MCS4 Mbps		-76	-73	
	MCS5 Mbps		-74	-71	
	MCS6 Mbps		-71	-68	
Adjacent Channel Rejection	MCS0 Mbps				dB
	MCS1 Mbps				
	MCS2 Mbps				
	MCS3 Mbps				
	MCS4 Mbps				
	MCS5 Mbps				
	MCS6 Mbps				
Receiver Maximum Input Level	ALL	-15			dBm
Receiver Spurious	<1GHz			-54	dBm
	1~10GHz			-54	
	10GHz~			-54	

B1:5190~5230MHz;B2:5270~5310MHz;B3:5510~5670MHz;B4:5755~5795MHz

* The performance will be updated after EDVT testing.

3.5 Bluetooth Standard Specifications

Bluetooth Core Specification version 4.0:

Host interface: UART, baud rates up to 4Mbps

Support all Bluetooth 4.0+HS packet types.

Operating frequency range: 2400MHz ~2483.5MHz

Modulation type:

Basic rate 1Mbps: GFSK,

Enhanced data rate 2Mbps: QPSK

Enhanced data rate 3Mbps: 8PSK

Parameter	Condition	Specification			Units
		Min	Typ	Max	
Basic Data Rate – Transmit Performance					
RF Transmit Power at the Antenna		+5	+8	+11	dBm
Tx Output Spectrum	-20 dB Bandwidth			1	MHz
	Frequency range			83.5	
Initial Carrier Frequency Tolerance		$\leq \pm 75$			KHz
Carrier Frequency Drift	DH1/3/5 Drift rate	$\leq \pm 20$			kHz/50 μ s
	DH1	$< \pm 20$			
	DH3	$\leq \pm 40$			
	DH5	$< \pm 40$			
Modulation Characteristics	F1avg	140< Δ f1 avg<175			kHz
	F2max	≥ 115			
	F2avg/F1avg	80			%
Adjacent Channel Transmit Power	+/-500KHz		-27		dBc
	M-N =2			-20	dBm
	M-N \geq 3			-40	
Transmission Spurious Emission-1	f<2.387GHz			-26	dBm
	2.387 GHz< f<2.400 GHz			-16	
	2.4835 GHz<f<2.4965 GHz			-16	
	f>2.4965 GHz			-26	
Transmission Spurious Emission-2	470~805MHz			-130	dBm/ Hz
	869~894MHz			-135	
	925~960MHz			-135	
	1,805~1,880MHz			-135	
	1,930~1,990MHz			-135	
	2,110~2,170MHz			-135	
1,574.4~1,576.4MHz			-150		
Enhanced Data Rate – Transmit Performance					
RF Transmit Power	$\pi/4$ DQPSK	+3	+6	+9	dBm
	8DPSK	+3	+6	+9	
Relative Transmit Power	Pdpsk	$(P_{GFSK}-4 \text{ dB}) < P_{DPSK} < (P_{GFSK}+1 \text{ dB})$			
Carrier Frequency Stability	ω_i	-75		75	kHz
	ω_0	-10		10	
	$\omega_i + \omega_0$	-75		75	
Modulation Accuracy – RMS DEVM	$\pi/4$ DQPSK	≤ 20			%
	8DPSK	≤ 13			
Modulation Accuracy – Peak DEVM	$\pi/4$ DQPSK	≤ 35			
	8DPSK	≤ 25			
Modulation Accuracy – 99%	$\pi/4$ DQPSK	≤ 30			

DEV M	8DPSK		≤ 20			
In-band Spurious Emissions	$f > f_0 + 3$ MHz		≤ -40	dBm		
	$f < f_0 - 3$ MHz		≤ -40			
	$f = f_0 - 3$ MHz		≤ -40			
	$f = f_0 - 2$ MHz		≤ -20			
	$f = f_0 - 1$ MHz		≤ -26	dBr		
	$f = f_0 + 1$ MHz		≤ -26			
	$f = f_0 + 2$ MHz		≤ -20	dBm		
	$f = f_0 + 3$ MHz		≤ -40			
EDR Differential Phase Coding		99			%	
Transmission Spurious Emission-1	$f < 2.387$ GHz			-26	dBm	
	2.387 GHz $< f < 2.400$ GHz			-16		
	2.4835 GHz $< f < 2.4965$ GHz			-16		
	$f > 2.4965$ GHz			-26		
Transmission Spurious Emission-2	470~805MHz			-130	dBm/ Hz	
	869~894MHz			-135		
	925~960MHz			-135		
	1,805~1,880MHz			-135		
	1,930~1,990MHz			-135		
	2,110~2,170MHz			-135		
	1,574.4~1,576.4MHz			-150		
Basic Data Rate – Receiver Performance at the Antenna						
Sensitivity (DH1) at 0.1% BER	Nominal			-86	dBm	
C/I Performance at BER \leq 0.1%	Co-ch interface C/I _{co}		< 11		dB	
	Adjacent Chnel Sensitivity C/I $f = f_0 \pm 1$ MHz		< 0			
	Adjacent Chnel Sensitivity C/I $f = f_0 \pm 2$ MHz		< -30			
	Adjacent Chnel Sensitivity C/I $f \geq f_0 \pm 3$ MHz		< -40			
	Image Ch interference C/I _{image}		< -9			
	Image Ch interference C/I _{image\pm} 1MHz		< -20			
Blocking Performance at BER \leq 0.1%	30MHz~2GHz	-10			dBm	
	2GHz~2.4GHz	-27				
	2.5GHz~3GHz	-27				
	3GHz~12.75GHz	-10				
Intermodulation Performance at BER \leq 0.1%	Carrier Level:-64dBm	-39			dBm	
Max input Level		-20			dBm	
Receiver Spurious	< 1 GHz			-54	dBm	
	1~10GHz			-54		
	10GHz~			-54		
Enhanced Data Rate – Receiver Performance at the Antenna						
Sensitivity at	Nominal	$\pi/4$ DQPSK(ch0~78)			-88	dBm

BER≤0.01%		8DPSK(ch0~78)			-85	
Max input Level		$\pi/4$ DQPSK	-20			dBm
		8DPSK	-20			
C/I Performance at BER≤0.1%($\pi/4$ DQPSK)		Co-ch interface C/I _{co}	<13			dB
		Adjacent Chanel Sensitivity C/I f=f ₀ ±1 MHz	< 0			
		Adjacent Chanel Sensitivity C/I f=f ₀ ±2 MHz	<-30			
		Adjacent Chanel Sensitivity C/I f≥f ₀ ±3 MHz	< -40			
		Image Ch interference C/I _{image}	< -7			
		Image Ch interference C/I _{image±} 1MHz	< -20			
C/I Performance at BER≤0.1%(8DPSK)		Co-ch interface C/I _{co}	<21			dB
		Adjacent Chanel Sensitivity C/I f=f ₀ ±1 MHz	< 5			
		Adjacent Chanel Sensitivity C/I f=f ₀ ±2 MHz	<-25			
		Adjacent Chanel Sensitivity C/I f≥f ₀ ±3 MHz	< -33			
		Image Ch interference C/I _{image}	< -0			
		Image Ch interference C/I _{image±} 1MHz	< -13			
Receiver Spurious		<1GHz			-54	dBm
		1~10GHz			-54	
		10GHz~			-54	

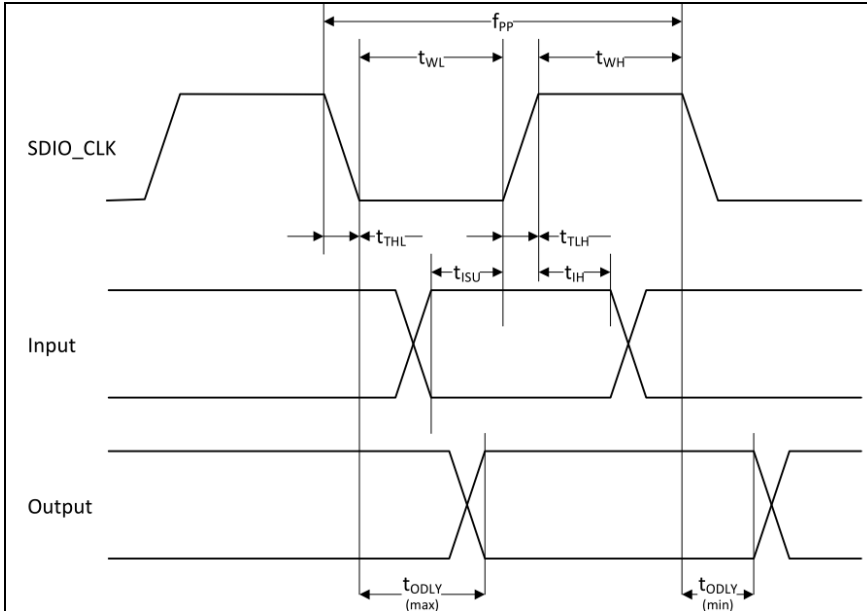
BLE RF specification:

BLE RF specification:						
Sensitivity at BER≤0.01%	Nominal	GFSK 0.1%, 1M		TBD	-85.5	dBm
RF Transmit Power		GFSK	1	TBD	8	dBm
Mod char:delta f1 average			225		275	kHz
Mod char:delta f2 max			99.9			%
Receiver Spurious		<1GHz			-54	dBm
		1~10GHz			-54	
		10GHz~			-54	
Transmission Spurious Emission-1		f<2.387GHz			-26	dBm
		2.387 GHz< f<2.400 GHz			-16	
		2.4835 GHz<f<2.4965 GHz			-16	
Transmission Spurious Emission-2		f>2.4965 GHz			-26	dBm/ Hz
		470~805MHz			-130	
		869~894MHz			-135	
		925~960MHz			-135	
		1,805~1,880MHz			-135	
		1,930~1,990MHz			-135	
		2,110~2,170MHz			-135	
	1,574.4~1,576.4MHz				-150	

3.6 Interface timing

3.6.1 SDIO Interface timing

- SDIO Default Mode timing

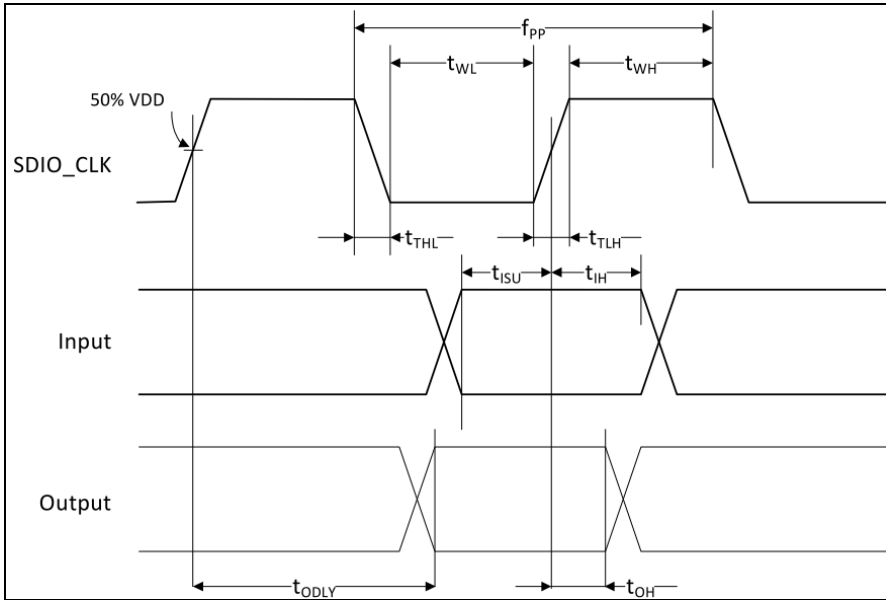


SDIO Bus Timing^a Parameters (Default Mode)

Parameter	Symbol	Minimum	Typical	Maximum	Unit
SDIO CLK (All values are referred to minimum VIH and maximum VIL^b)					
Frequency – Data Transfer mode	f_{PP}	0	–	25	MHz
Frequency – Identification mode	f_{OD}	0	–	400	kHz
Clock low time	t_{WL}	10	–	–	ns
Clock high time	t_{WH}	10	–	–	ns
Clock rise time	t_{TLH}	–	–	10	ns
Clock low time	t_{THL}	–	–	10	ns
Inputs: CMD, DAT (referenced to CLK)					
Input setup time	t_{ISU}	5	–	–	ns
Input hold time	t_{IH}	5	–	–	ns
Outputs: CMD, DAT (referenced to CLK)					
Output delay time – Data Transfer mode	t_{ODLY}	0	–	14	ns
Output delay time – Identification mode	t_{ODLY}	0	–	50	ns

a. Timing is based on $CL \leq 40pF$ load on CMD and Data.
 b. $\min(V_{ih}) = 0.7 \times V_{DDIO}$ and $\max(V_{il}) = 0.2 \times V_{DDIO}$.

● **SDIO High-Speed Mode Timing**



SDIO Bus Timing^a Parameters (High-Speed Mode)

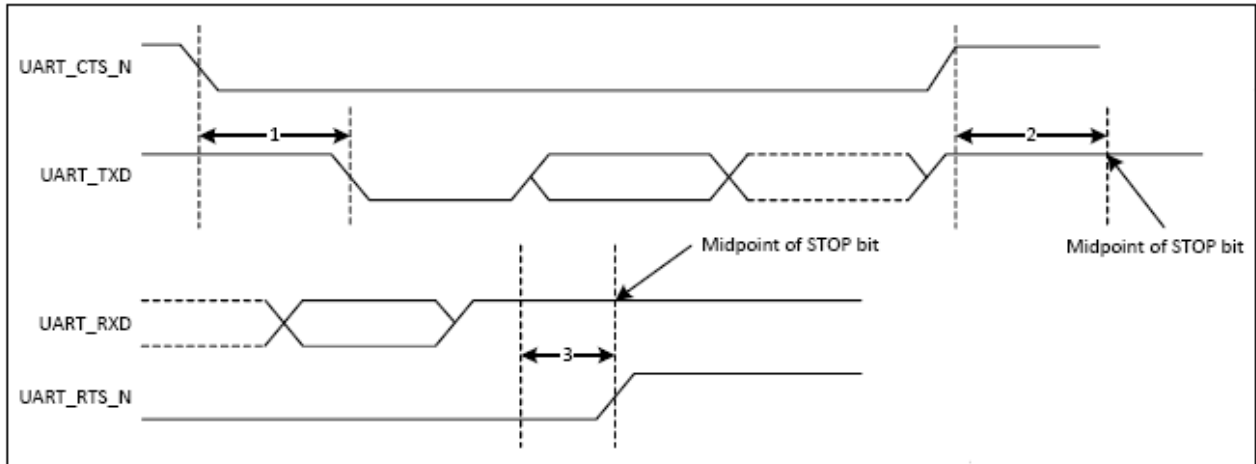
Parameter	Symbol	Minimum	Typical	Maximum	Unit
SDIO CLK (all values are referred to minimum VIH and maximum VIL^b)					
Frequency – Data Transfer Mode	f _{pp}	0	–	50	MHz
Frequency – Identification Mode	f _{OD}	0	–	400	kHz
Clock low time	t _{WL}	7	–	–	ns
Clock high time	t _{WH}	7	–	–	ns
Clock rise time	t _{TLH}	–	–	3	ns
Clock low time	t _{THL}	–	–	3	ns
Inputs: CMD, DAT (referenced to CLK)					
Input setup Time	t _{ISU}	6	–	–	ns
Input hold Time	t _{IH}	2	–	–	ns
Outputs: CMD, DAT (referenced to CLK)					
Output delay time – Data Transfer Mode	t _{ODLY}	–	–	14	ns
Output hold time	t _{OH}	2.5	–	–	ns
Total system capacitance (each line)	CL	–	–	40	pF

a. Timing is based on CL ≤ 40 pF load on CMD and Data.

b. min(V_{ih}) = 0.7 × VDDIO and max(V_{il}) = 0.2 × VDDIO.

3.6.2 UART Interface timing

UART Timing



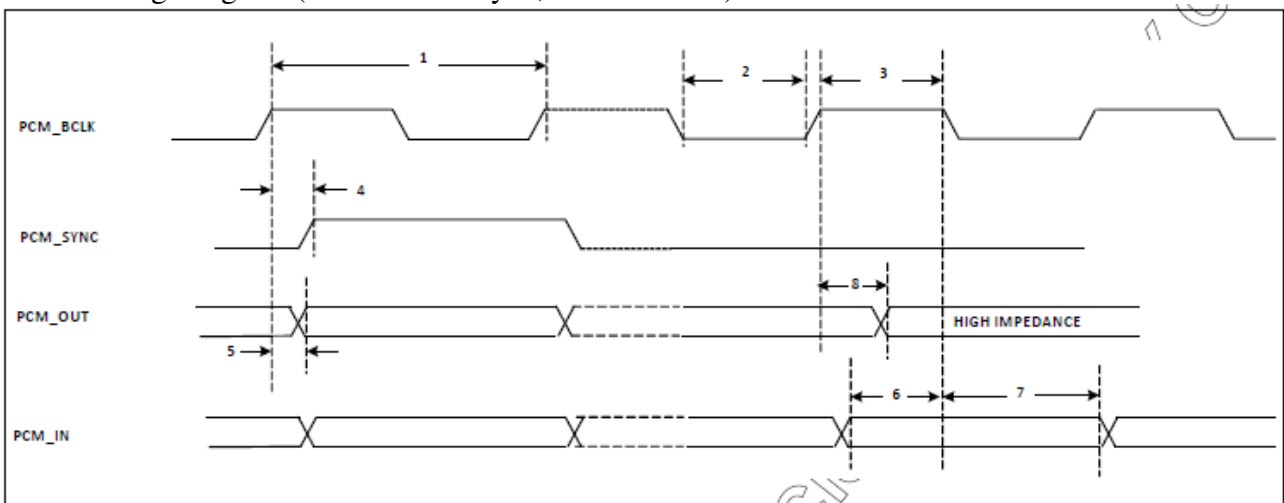
UART Timing Specifications

Ref No.	Characteristics	Minimum	Typical	Maximum	Unit
1	Delay time, UART_CTS_N low to UART_TXD valid	–	–	1.5	Bit periods
2	Setup time, UART_CTS_N high before midpoint of stop bit	–	–	0.5	Bit periods
3	Delay time, midpoint of stop bit to UART_RTS_N high	–	–	0.5	Bit periods

3.6.3 PCM Interface timing

- Short Frame Sync, Master Mode

PCM Timing Diagram (Short Frame Sync, Master Mode)

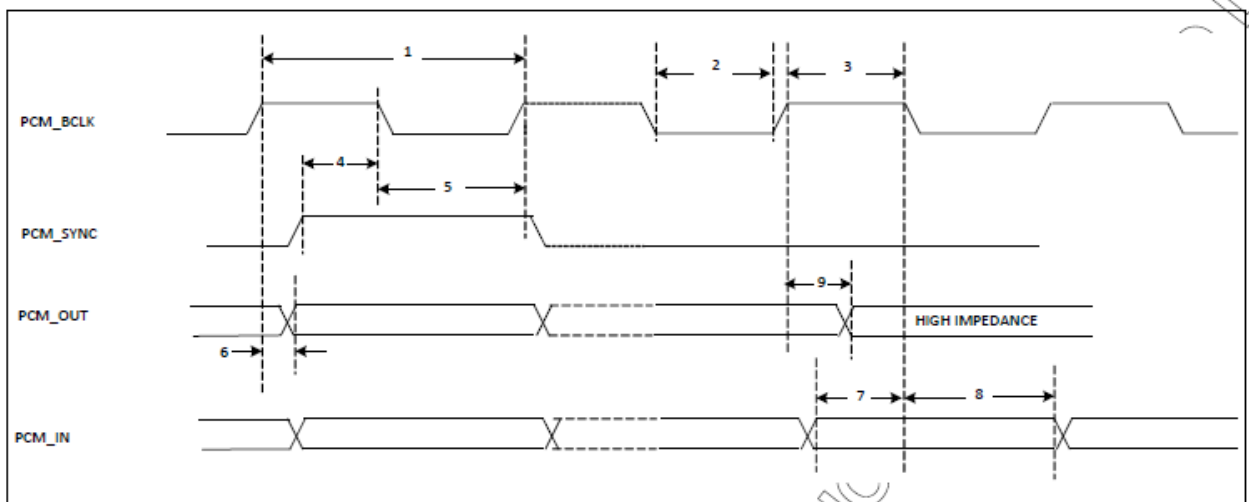


PCM Interface Timing Specifications (Short Frame Sync, Master Mode)

Ref No.	Characteristics	Minimum	Typical	Maximum	Unit
1	PCM bit clock frequency	–	–	12	MHz
2	PCM bit clock HIGH	41	–	–	ns
3	PCM bit clock LOW	41	–	–	ns
4	PCM_SYNC delay	0	–	25	ns
5	PCM_OUT delay	0	–	25	ns
6	PCM_IN setup	8	–	–	ns
7	PCM_IN hold	8	–	–	ns
8	Delay from rising edge of PCM_BCLK during last bit period to PCM_OUT becoming high impedance	0	–	25	ns

- Short Frame Sync, Slave Mode

PCM Timing Diagram (Short Frame Sync, Slave Mode)

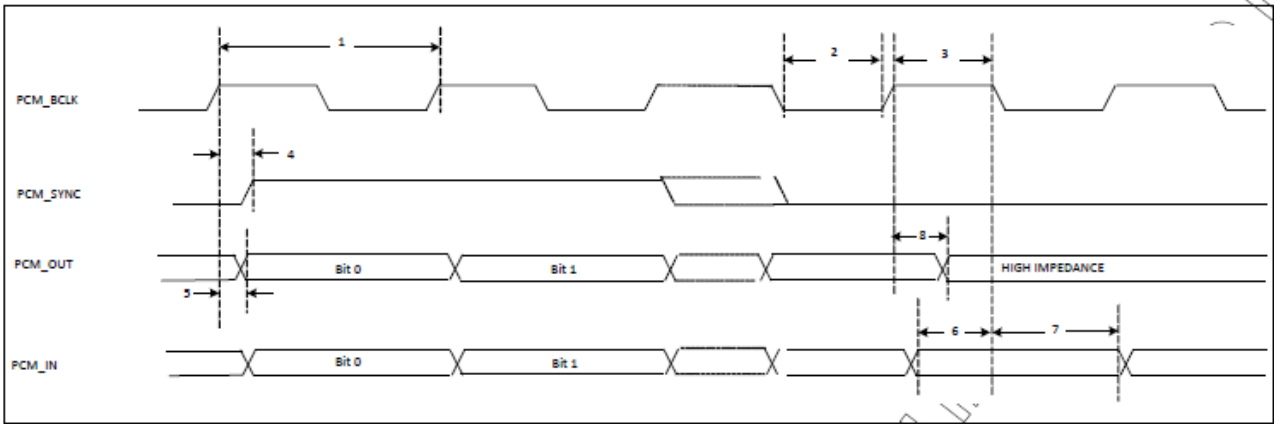


PCM Interface Timing Specifications (Short Frame Sync, Slave Mode)

Ref No.	Characteristics	Minimum	Typical	Maximum	Unit
1	PCM bit clock frequency	–	–	12	MHz
2	PCM bit clock HIGH	41	–	–	ns
3	PCM bit clock LOW	41	–	–	ns
4	PCM_SYNC setup	8	–	–	ns
5	PCM_SYNC hold	8	–	–	ns
6	PCM_OUT delay	0	–	25	ns
7	PCM_IN setup	8	–	–	ns
8	PCM_IN hold	8	–	–	ns
9	Delay from rising edge of PCM_BCLK during last bit period to PCM_OUT becoming high impedance	0	–	25	ns

- Long Frame Sync, Master Mode

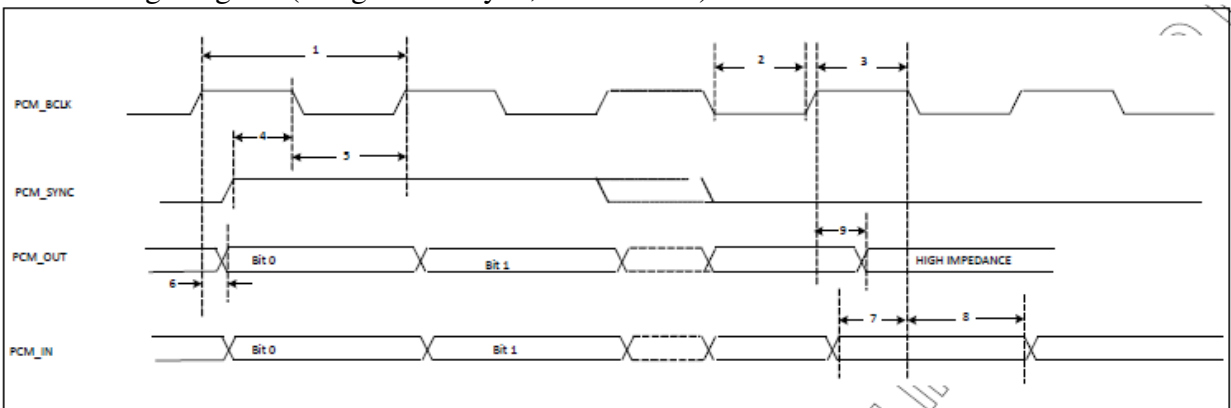
PCM Timing Diagram (Long Frame Sync, Master Mode)



PCM Interface Timing Specifications (Long Frame Sync, Master Mode)

Ref No.	Characteristics	Minimum	Typical	Maximum	Unit
1	PCM bit clock frequency		-	12	MHz
2	PCM bit clock HIGH	41	-	-	ns
3	PCM bit clock LOW	41	-	-	ns
4	PCM_SYNC delay	0	-	25	ns
5	PCM_OUT delay	0	-	25	ns
6	PCM_IN setup	8	-	-	ns
7	PCM_IN hold	8	-	-	ns
8	Delay from rising edge of PCM_BCLK during last bit period to PCM_OUT becoming high impedance	0	-	25	ns

PCM Timing Diagram (Long Frame Sync, Slave Mode)



PCM Interface Timing Specifications (Long Frame Sync, Master Mode)

<i>Ref No.</i>	<i>Characteristics</i>	<i>Minimum</i>	<i>Typical</i>	<i>Maximum</i>	<i>Unit</i>
1	PCM bit clock frequency		–	12	MHz
2	PCM bit clock HIGH	41	–	–	ns
3	PCM bit clock LOW	41	–	–	ns
4	PCM_SYNC setup	8	–	–	ns
5	PCM_SYNC hold	8	–	–	ns
6	PCM_OUT delay	0	–	25	ns
7	PCM_IN setup	8	–	–	ns
8	PCM_IN hold	8	–	–	ns
9	Delay from rising edge of PCM_BCLK during last bit period to PCM_OUT becoming high impedance	0	–	25	ns

3.7 LGA Pin Definition

- Module pin-out definition:

GND (G1)		GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND (G4)		
1	UIM_Power_In/GPIO1																					GND	76
2	UIM_Power_Out																					GND	75
3	UIM_SWP																					GND	74
4	3.3V																					3.3V	73
5	3.3V																					3.3V	72
6	GND																					GND	71
7	Reserved																					USB_D+	70
8	ALERT																					USB_D-	69
9	I2C_CLK																					GND	68
10	I2C_DATA																					Reserved	67
11	COEX1																					Reserved	66
12	COEX2																					LED#1	65
13	COEX3																					LED#2	64
14	SYSCLK/GNSS0																					W_DISABLE#2	63
15	TX_Blinking/GNSS1																					GND	62
16	Reserved																					PCMCLK	61
17	GND																					PCMOUT	60
18	Reserved																					BT_PCMIN	59
19	Reserved																					PCMF1	58
20	GND																					UART_RTS	57
21	Reserved																					UART_Rx	56
22	Reserved																					UART_Tx	55
23	GND																					UART_CTS	54
24	Reserved																					UART_WAKE	53
25	Reserved																					SDIO_CLK	52
26	GND																					SDIO_CMD	51
27	SUSCLK(32kHz)																					SDIO_DAT0	50
28	W_DISABLE#1																					SDIO_DAT1	49
GND (G2)		PEWake#	CLKREQ#	PERST#	GND	REFCLKNO	REFCLKPO	GND	PETn0	PETp0	GND	PEFn0	PERp0	GND	Reserved	Reserved	Reserved	SDIO_Reset	SDIO_Wake	SDIO_DAT3	SDIO_DAT2	GND (G3)	
		29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48		

Top View

- Platform pin-out definition:

GND (G1)		GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND (G4)	
1	UIM_Power_In/GPIO1																						GND	76
2	UIM_Power_Out																						GND	75
3	UIM_SWP																						GND	74
4	3.3V																						3.3V	73
5	3.3V																						3.3V	72
6	GND																						GND	71
7	Reserved																						USB_D+	70
8	ALERT#																						USB_D-	69
9	I2C_CLK																						GND	68
10	I2C_DATA																						Reserved	67
11	COEX1																						Reserved	66
12	COEX2																						LED#1	65
13	COEX3																						LED#2	64
14	SYSCLK/GNSS0																						W_DISABLE#2	63
15	TX_Blinking/GNSS1																						GND	62
16	Reserved																						PCMCLK	61
17	GND																						BT_PCMIN	60
18	Reserved																						PCMOUT	59
19	Reserved																						PCMF1	58
20	GND																						UART_CTS	57
21	Reserved																						UART_Tx	56
22	Reserved																						UART_Rx	55
23	GND																						UART_RTS	54
24	Reserved																						UART_WAKE	53
25	Reserved																						SDIO_CLK	52
26	GND																						SDIO_CMD	51
27	SUSCLK(32kHz)																						SDIO_DAT0	50
28	W_DISABLE#1																						SDIO_DAT1	49
GND (G2)		PEWake#	CLKREQ#	PERST#	GND	REFCLKNO	REFCLKPO	GND	PERn0	PERp0	GND	PETn0	PETp0	GND	Vendor Defined	Vendor Defined	Vendor Defined	SDIO_Reset	SDIO_Wake	SDIO_DAT3	SDIO_DAT2	GND (G3)		
		29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48			

Top View

- **Pin definition (Module point of view):**

Pin No.	Pin Name	Type	Description	Voltage
1-3	UIM_power_in/GPIO1 UIM_power_out UIM_SWP	I/O	No connection	-
4-5	3.3V	Power	Power voltage input pin	3.3V
6	GND	GND	Ground	-
7	Reserved	I/O	Reserved LGA pin	-
8*(a)	ALERT	-	No connection	-
9*(a)	I2C CLK	-	No connection	-
10*(a)	I2C DATA	-	No connection	-
11*(a)	COEX1	-	No connection	-
12*(a)	COEX2	-	No connection	-
13*(a)	COEX3	-	No connection	-
14-15	SYSCLK/GNSS0 TX_Blanking/GNSS1	-	Reserved LGA pin, No connection	
16	Reserved	I/O	Reserved LGA pin, No connection	
17	GND	GND	Ground	
18-19	Reserved	-	Reserved LGA pin, No connection	
20	GND	GND	Ground	
21-22	Reserved	-	Reserved LGA pin, No connection	
23	GND	GND	Ground	
24-25	Reserved	-	Reserved LGA pin, No connection	
26	GND	GND	Ground	
27	SUSCLK (32KHz)	I	32.768 kHz clock supply input that is provided by PCH to reduce power and cost for the module. SUSCLK will have a duty cycle that can be as low as 30% or as high as 70%. 200ppm.	3.3V
28	W_DISABLE#1	I	No connection	
29*(a)	PEWAKE#	-	No connection	-
30*(a)	CLKREQ#	-	No connection	-
31*(a)	PERST#	-	No connection	-
32	GND	GND	Ground	
33*(a)	REFCLKN0	-	No connection	-
34*(a)	REFCLKP0	-	No connection	-
35	GND	GND	Ground	
36*(a)	PETn0	-	No connection	-
37*(a)	PETp0	-	No connection	-
38	GND	GND	Ground	
39*(a)	PERn0	-	No connection	-
40*(a)	PERp0	-	No connection	-
41	GND	GND	Ground	
42-44	Reserved	-	Reserved LGA pin, No connection	
45	SDIO Reset	I	SDIO sideband GPIO pin to enable/disable (reset) the WiFi function. Platform firmware is required to assert/de-assert this pin on every boot (warm and cold). The WiFi device may use 0.5 to 1 mW in reset, Active Low	1.8V
46	SDIO Wake	O	SDIO Host Wake. Note in band SDIO wake is not used for non-active modes, Active Low. Require pull up on the host side (recommended 15K to 100K)	1.8V
47	SDIO DATA3	I/O	4 lines for SDIO data exchange	1.8V
48	SDIO DATA2	I/O	4 lines for SDIO data exchange	1.8V



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Preliminary Datasheet

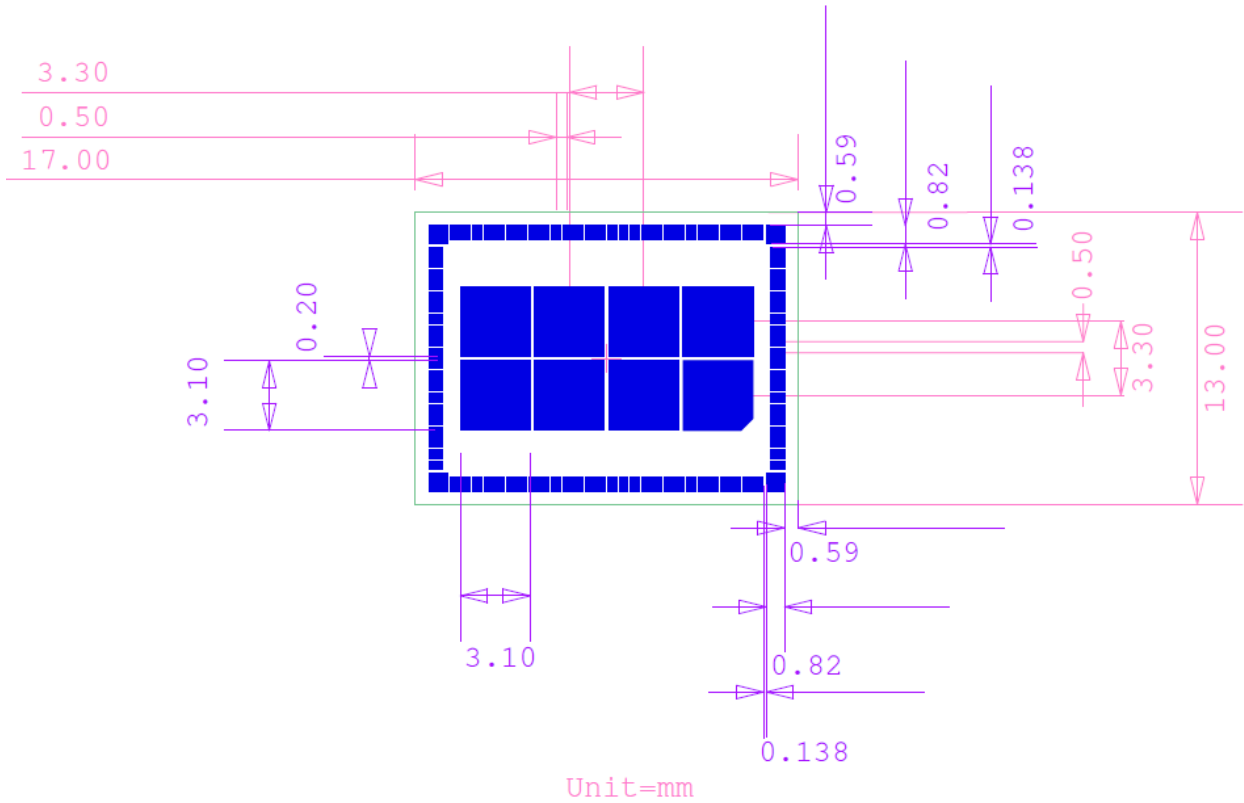
49	SDIO DATA1	I/O	4 lines for SDIO data exchange	1.8V
50	SDIO DATA0	I/O	4 lines for SDIO data exchange	1.8V
51	SDIO CMD	I/O	SDIO Command Interface	1.8V
52	SDIO CLK	I	SDIO 3.0 Clock	1.8V
53	UART WAKE	O	Bluetooth host Wake. Active Low	3.3V
54	UART CTS	I	UART Clear To Send, Active low, connected to UART RTS on the platform.	1.8V
55	UART TX	O	UART Transmit Data, connected to UART RX on the platform.	1.8V
56	UART RX	I	UART Receive Data, connected to UART TX on the platform.	1.8V
58	UART RTS	O	UART Request To Send, Active low, connected to UART CTS on the platform.	1.8V
58	PCM FR1	I/O	PCM Synchronous data sync/ I2S Word Select	1.8V
59	PCMIN	I	PCM Synchronous data input/ I2S Serial Data IN	1.8V
60	PCMOUT	O	PCM Synchronous data output/ I2S Serial Data OUT	1.8V
61	PCMCLK	I/O	PCM Clock/ I2S Continuous Serial Clock (SCK)	1.8V
62	GND	GND	Ground	
63	W_DISABLE#2	I	Active low, debounced signal when applied by the platform it will disable BT radio operation	3.3V
64	LED#2	-	No connection	-
65	LED#1	-	No connection	-
66-67	Reserved	-	Reserved LGA pin	
68	GND	GND	Ground	
69*(a)	USB_D-	-	No connection	-
70*(a)	USB_D+	-	No connection	-
71	GND	GND	Ground	
72-73	3.3V	Power	Power voltage input pin	3.3V
74-76, others	GND	GND	Ground	

The pin-out definition is following NGFF1216 standard, but removed partial functions such as PCIe, BT_USB. All of function used is following the BCM4324 application.

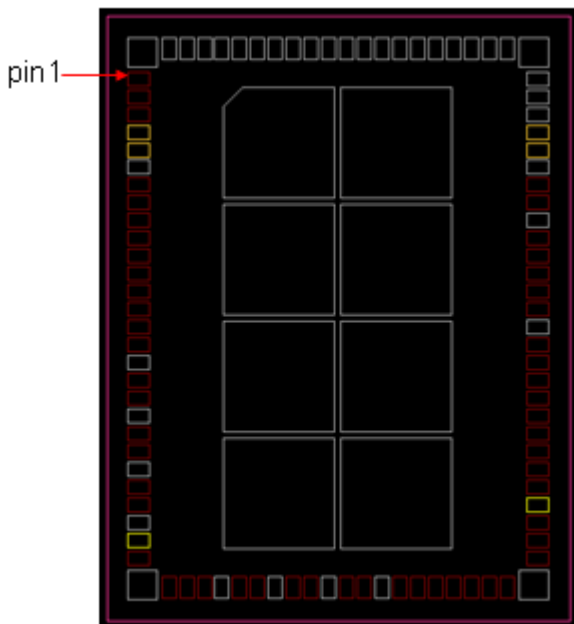
*(a) FOXCONN remove the function according to BCM4324 function.

4. Mechanical Drawing

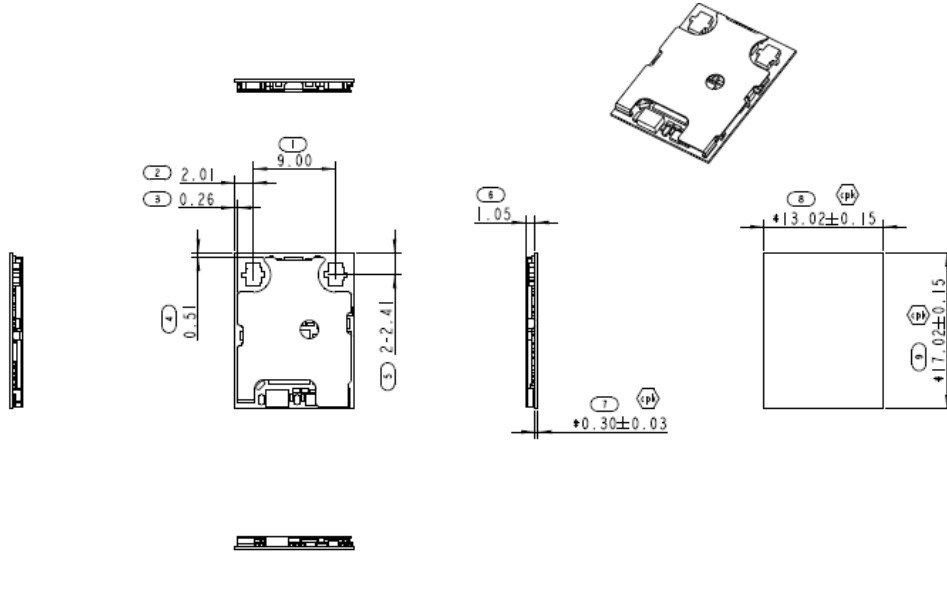
Foxconn can provide the DXF file for detail dimension as following drawing.
 (Dimension Tolerance is ± 0.15 mm)



Footprint (Top view)



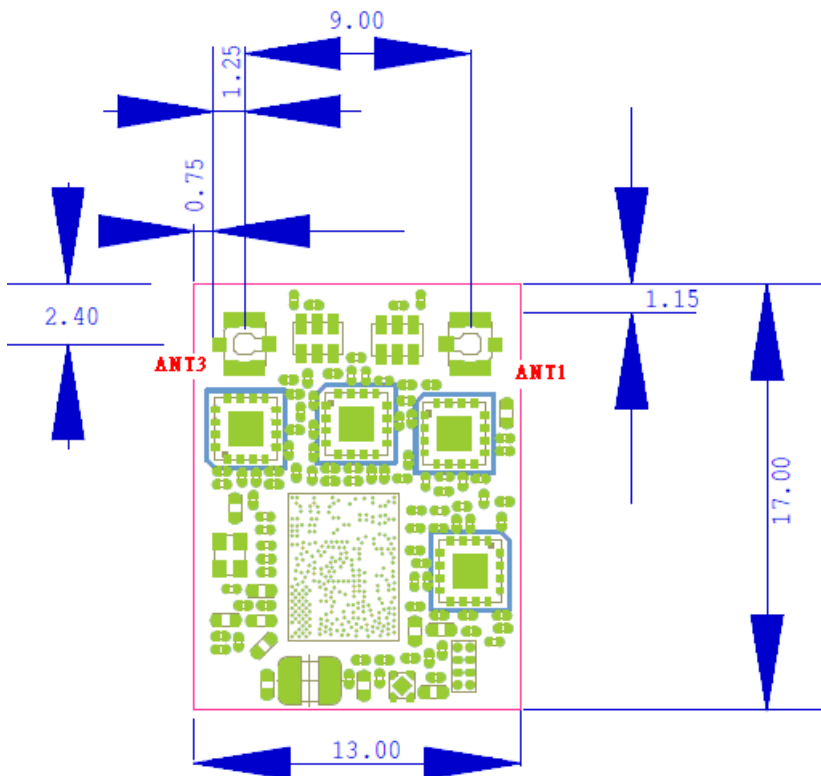
T77H506.00 035 Shielding Cover



SCALE 1.000

MATERIAL (SPEC.)		SCALE	FOXCONN		
FINISH		1:1	HON HAI PRECISION IND. CO., LTD		
Select +		SHEET	CNSBG		
0.5 0.05 0.05 0.10		1/1	PART NAME		
0.30 0.10 0.15 0.20 0.50 0.50 3.00		UNIT	outline drawing		
30-120 0.15 0.20 0.30 0.50 0.50 2.00 0.25 0.15		mm	MODEL	T77H506.00	PART NO.
120-300 0.20 0.30 0.40 1.00 1.00 0.05 3.00 0.30 0.15		SIZE	N/A		
300-450 0.25 0.40 0.50 2.00 2.00 0.05 5.00 0.50 0.20		A4	CUSTOMER MODEL	DESIGNED	
450-600 0.30 0.50 0.60 3.00 3.00 0.05 5.00 0.80 0.20			T77H506.00		plngcoo liang
DIMENSION DESIGNATOR			CUSTOMER PART NO.	APPROVED	
DRAFT TOLERANCE ±0.21 CRITICAL DIM. MARK *			N/A		Jon zhou

Antenna port location as following drawing ((Dimension Tolerance is +-0.15 mm))



RF connector:

- New RF connector is needed, for lower z-height and smaller footprint
- Same RF Receptacle on module supports either 0.81mm or 1.13mm diameter cable
- Cable diameter determines mated height: 1.2mm mated height with 0.81mm cable

4.3 Example of IPEX RF connector

IPEX P/N: 20449-001E (MHF4)

IPEX P/N: 20448-001R-081

[Material and Finish]

Parts	Material	Plating / Color
HOUSING	LCP UL94V-0	BLACK
CONTACT	COPPER ALLOY	Au
GROUND CONTACT	COPPER ALLOY	Au

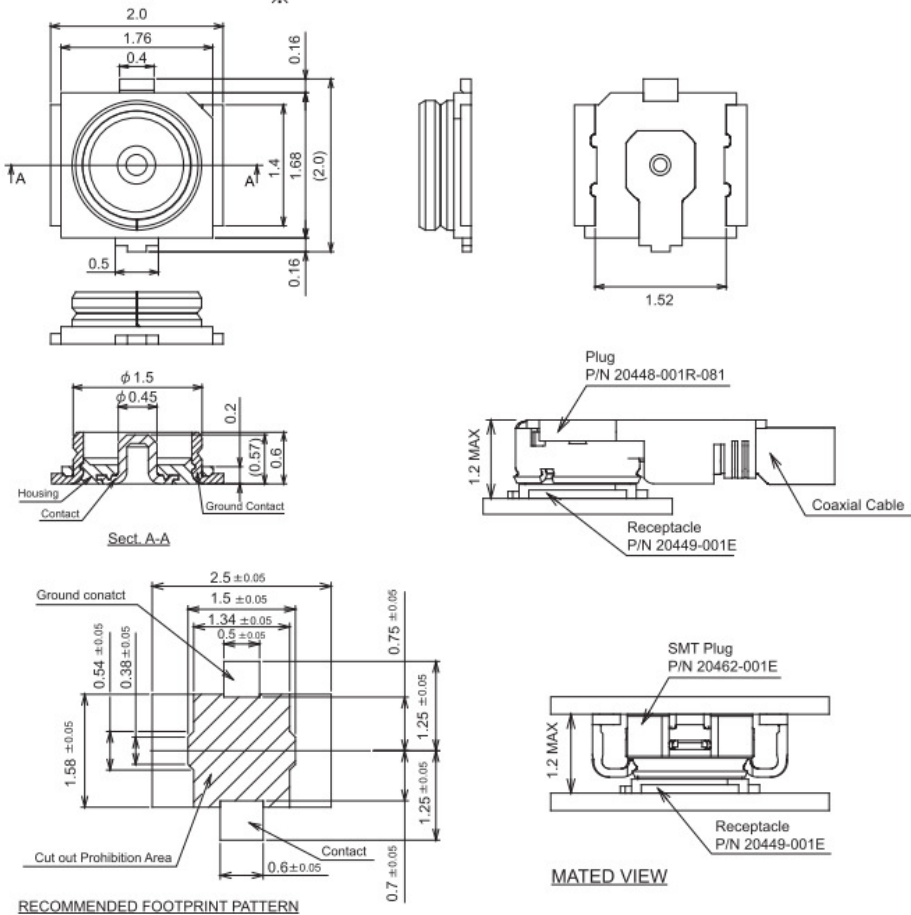
MHF[®] 4 Receptacle

[Part Number]

Ground Contact : Au Plating

20449-001 E

※ Packing : Emboss Tape
1reel : 10,000pcs





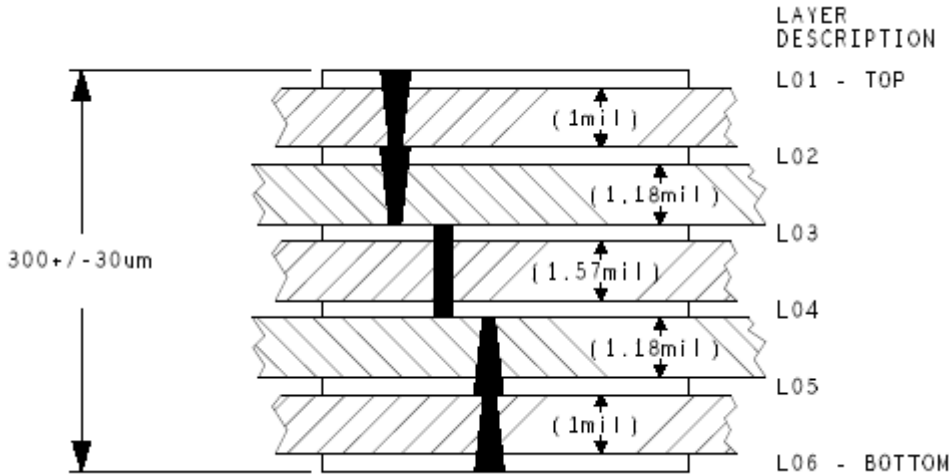
COMPANY CONFIDENTIAL
Preliminary Datasheet



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6. PCB Layout

6-layer, 2 order HDI FR-4 (Halogen Free) design, total thickness 0.3+/-0.03mm



7. Software Requirement

- Operating System Support
 - Windows 8
 - Windows Blue or later Android 4.3 and above
- WLAN Feature Support
 - WiFi Direct
 - WiFi Display
 - Wi-Fi Miracast (Intel will support WiDi with Miracast interoperability)
- WLAN Security Support
 - WPA/WPA2 Enterprise
 - CCX Lite or higher
 - WMM/AES/TKIP/CKIP
- WLAN Transmit Power Reduction
 - Software control to meet FCC SAR requirement
 - Capability to disable 5GHz operation
- Bluetooth Profile Support
 - A2DP-Sink
 - A2DP-Source
 - AVRCP-Target
 - DUN-DT
 - FTP-Client
 - FTP-Server
 - HCRP-Client
 - HID-Host
 - MCAP
 - OPP-Client
 - OPP-Server
 - PAN-User



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Preliminary Datasheet

- SDP
 - Serial-DevA
 - Serial-DevB
- BLE (Bluetooth Low Energy) Support
- Windows 8
 - Windows Blue or later

8. Regulatory

- USA : FCC P15B / FCC P15C / FCC P15E
- Canada : IC RSS-210
- Japan : TELEC
- EU : EN300328 V1.8.1 , EN301893 V1.6.1 , EN301489-1/-17 , EN 60950-1 2nd

BCM4324 module 5GHz power table

Typical power (limit is +/-2dB) (unit:dBm)

HT20		CH36	CH48	CH52	CH64	CH100	CH120	CH140	CH149	CH165
6Mbps	ANT0	16	16	17.5	17.5	17.5	17.5	17	17.5	17.5
	ANT1	16	16	17.5	17.5	17.5	17.5	17	17.5	17.5
MCS8	2Tx	16	16	19.5	19.5	19.5	19.5	19.5	19.5	19.5

HT40		CH38	CH64	CH54	CH62	CH102	CH134	CH151	CH159
MCS8	2Tx	16	16	19.5	18	19	19.5	19.5	19.5

BCM4324 module 2.4GHz power table

Typical power (limit is +/-2dB) (unit:dBm)

HT20		CH1	CH2	CH3	CH4	CH5	CH6	CH7	CH8	CH9	CH10	CH11
1Mbps	ANT0	15.5	15.5	15.5	15.5	15.5	15.5	15.5	15.5	15.5	15.5	15.5
	ANT1	18	18	18	18	18	18	18	18	18	18	18
6Mbps	ANT0	17	17	17	17	17	17	17	17	17	17	17
	ANT1	17	17	17	17	17	17	17	17	17	17	17
MCS8	2Tx	18.5	19.5	19.5	19.5	19.5	19.5	19.5	19.5	19.5	19.5	17.5

9. Package reliability test conditions

Sine Vibration	1.Frequency = 5 ~ 500 ~ 5 HZ 2.Acceleration = 2 Grms 3.Each of x, y, z axis/ 30 min
Shock Test	1.Sine wave ,230G , 3msec 2.Test : +/- x, y, z axes

10. Environmental Requirements and Specifications

10.1 Temperature

9.1.1 Operating Temperature Conditions

The product shall be capable of continuous reliable operation when operating in ambient temperature of 0 °C to +70 °C.

10.1.2 Non-Operating Temperature Conditions

Neither subassemblies shall be damaged nor shall the operational performance be degraded when restored to the operating temperature when exposed to storage temperature in the range of -10 °C to +85 °C.

10.2 PCB bending

The PCB bending spec shall be keep planeness under 0.1mm for both Foxconn and end assembly customer.

10.3 Handling environment

ESD

There are semiconductors on the module, please handle the module under ESD protected and well-controlled environment (<100V).

Terminals Handling Notice

The product is mounted with motherboard through Land Grid Array. In order to prevent poor soldering, please do not touch LGA portion by hand.

Notes: As a rule, baking the components in accordance with condition mentioned above, because tape and real for packaging materials have no heat resistance, please bake the components moved into another container such as heat resistance trays.

Others

1. Please make sure to avoid mechanical shock and vibration for this module.
2. Please do not drop the module.
3. Please do not clean the module.

10.4 Storage Condition

1. Moisture barrier bag must be stored under 40 °C , humidity under 90% RH, when the moisture barrier bag is sealed by Foxconn.
2. The calculated shelf life for the dry packed product shall be a 12 months from the bag seal date.
3. If Moisture barrier bag is open, the component must be stored in an environment of $25 \pm 5^{\circ}\text{C}$ /10%RH
4. Please keep the module at 30°C/70% RH.

10.5 Baking Condition

If below two conditions happens:

- a) Humidity indicator cards read >30%
- b) Temp < 30 °C, Humidity <70%RH, moisture barrier bag open over 96 hours

Products require baking before mounting

Baking condition: 90 °C, 12-22 hours

Baking times: Max. 2 times

10.6 Soldering and reflow condition

1) Heating method

Conventional Convection or IR/convection

2) Temperature measurement

Thermocouple d=0.1mm ~ 0.2mm CA (K) or CC (T) at soldering portion or equivalent method.

3) Solder paste composition

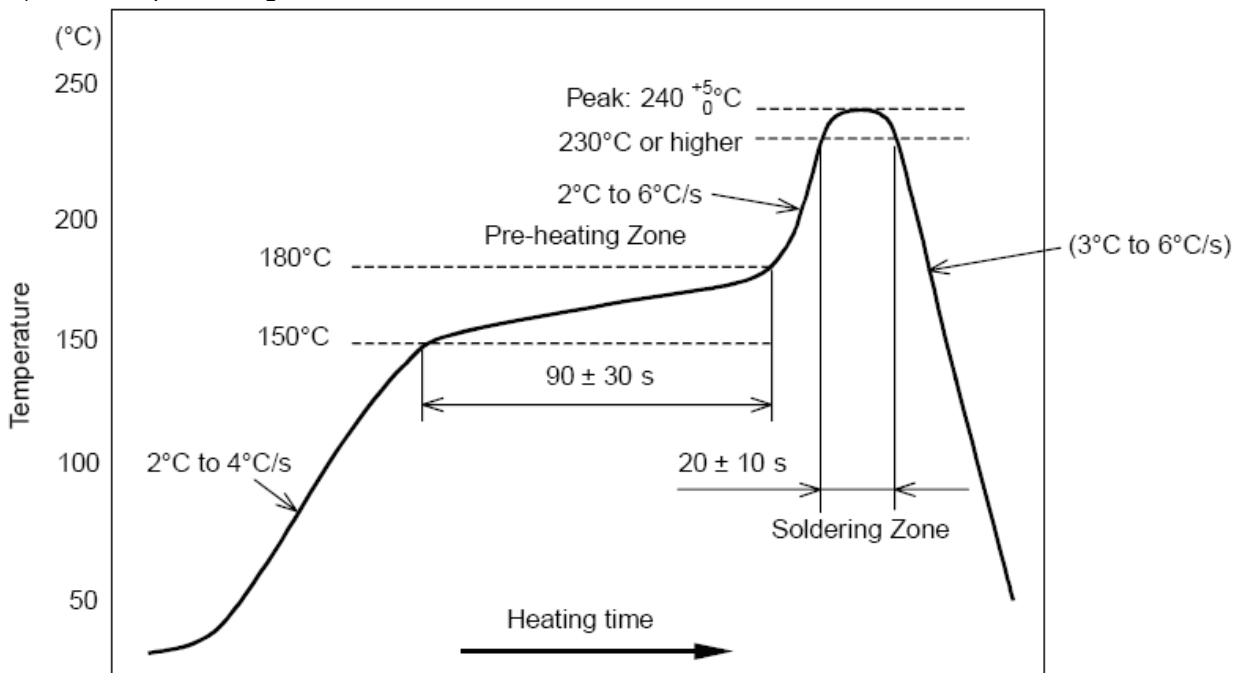
Sn/3.0Ag/0.5Cu

4) Allowable reflow soldering times: 2 times, based on the below reflow soldering profile

5) Temperature profile

Reflow soldering shall be done according to the below temperature profile.

6) Peak temp: 245 degree C



Temperature profile for evaluation of solder heat resistance of a component (at solder joint)

11 Package information

TBD



COMPANY CONFIDENTIAL

Federal Communication Commission Interference Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Caution: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Operations in the 5.15-5.25GHz band are restricted to indoor usage only.

Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

This device is intended only for OEM integrators under the following conditions:

- 1) The antenna must be installed such that 20 cm is maintained between the antenna and users, and
- 2) The transmitter module may not be co-located with any other transmitter or antenna.

As long as 2 conditions above are met, further transmitter test will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed

IMPORTANT NOTE: In the event that these conditions can not be met (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID can not be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

End Product Labeling

This transmitter module is authorized only for use in device where the antenna may be installed such that 20 cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following: "Contains FCC ID: MCLT77H506". The grantee's FCC ID can be used only when all FCC compliance requirements are met.

Manual Information To the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.