

Revisions

Date	Rev	Description
June 28, 2005	0.0	Initial Release

P01_COVER_PAGE
P01_COVER_PAGE.sch

P02_POWER
P02_POWER.sch

- VREF
- PWR_RSTn
- DIN

P03_RTL8671_208
P03_RTL8671_208.sch

- RSTn
- EECS
- EEDO
- EEDI
- EESK
- NICLED2
- NICLED3
- MDIO
- MDC
- RXD3
- RXD2
- RXD1
- RXD0
- RXDV
- RXCLK
- TXCLK
- TXEN
- TXD0
- TXD1
- TXD2
- TXD3
- COL
- AD[0..31]
- PCL_CLK
- STOPBn
- INTABn
- PCL_RSTn
- IDSEL0
- GNTB0
- REQB0
- PAR
- DEVSELBn
- TRDYBn
- IRDYBn
- FRAMEBn
- BE3n
- BE2n
- BE1n
- BE0n
- PWR_RSTn
- SDRAM_CLK
- WEn
- CASn
- RASn
- MCS0n
- AA20
- MCS2n
- MCS3n
- DD[0..15]
- AA[0..20]
- PMEB
- TX3_AFE
- TX2_AFE
- TX1_AFE
- TX0_AFE
- CTRLIN_AFE
- CLKM_AFE
- CLWD_AFE
- RX3_AFE
- RX2_AFE
- RX1_AFE
- RX0_AFE
- POWDN_AFE
- VREF

P04_MEMORY&SETTING
P04_MEMORY&SETTING.sch

- MCS0n
- MCS2n
- NICLED3
- TX0_AFE
- NICLED2
- TX2_AFE
- CASn
- CTRLIN_AFE
- DD[0..15]
- SDRAM_CLK
- AA[0..20]
- RASn
- MCS3n
- AA20
- TX3_AFE
- WEn
- POWDN_AFE

P05_RTL8271_64
P05_RTL8271_64.sch

- CLKM_AFE
- RX2_AFE
- CLWD_AFE
- RX3_AFE
- TX3_AFE
- RSTn
- POWDN_AFE
- TX1_AFE
- TX2_AFE
- TX0_AFE
- RX0_AFE
- RX1_AFE
- CTRLIN_AFE
- DIN
- VREF

P06_8305SC
P06_8305SC.sch

- RSTn
- RDIN3
- RDIP3
- TDOP3
- TDON3
- RDIN2
- RDIP2
- TDOP2
- TDON2
- RDIN1
- RDIP1
- TDOP1
- TDON1
- RDIN0
- RDIP0
- TDOP0
- TDON0
- MDC
- MDIO
- TXD0
- TXD1
- TXD2
- TXD3
- TXEN
- TXCLK
- COL
- RXD0
- RXD1
- RXD2
- RXD3
- RXDV
- RXCLK
- ACT3_EN48P1
- ACT0_BCINDROP
- ACT1_GYSPD
- ACT2_P4ANEG

P07_8305SIX
P07_8305SIX.sch

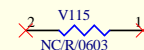
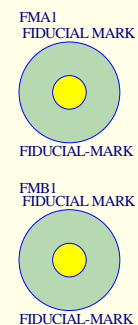
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- TDON1
- RDIP1
- RDIP3
- TDIN0
- TDON0
- TDOP2
- RDIP2
- TDIP0
- RDIN2
- TDOP3
- TDOP1
- RDIN1
- TDOP0
- TDON1
- RDIN0
- RDIP0
- TDOP1
- ACT3_EN48P1
- ACT0_BCINDROP
- RDIN3
- TDON2
- ACT1_GYSPD
- ACT2_P4ANEG
- NICLED2
- NICLED3

P08_RTL8185
P08_RTL8185.sch

- DEVSELBn
- TRDYBn
- IRDYBn
- BE3n
- IDSEL0
- REQB0
- AD[0..31]
- STOPBn
- BE1n
- FRAMEBn
- BE0n
- PAR
- PCL_CLK
- PCL_RSTn
- BE2n
- INTABn
- GNTB0
- PMEB
- EEDO
- EESK
- EECS
- EEDI
- TRSW-
- TRSW+
- CALEN
- RIFSD
- RIFSC
- RFLD
- TXQ+
- TXQ-
- TXI+
- TXI-
- RXI+
- RXQ+
- RXQ-
- RSSI
- TSSIO
- A_PAPE
- 40MHzOSC

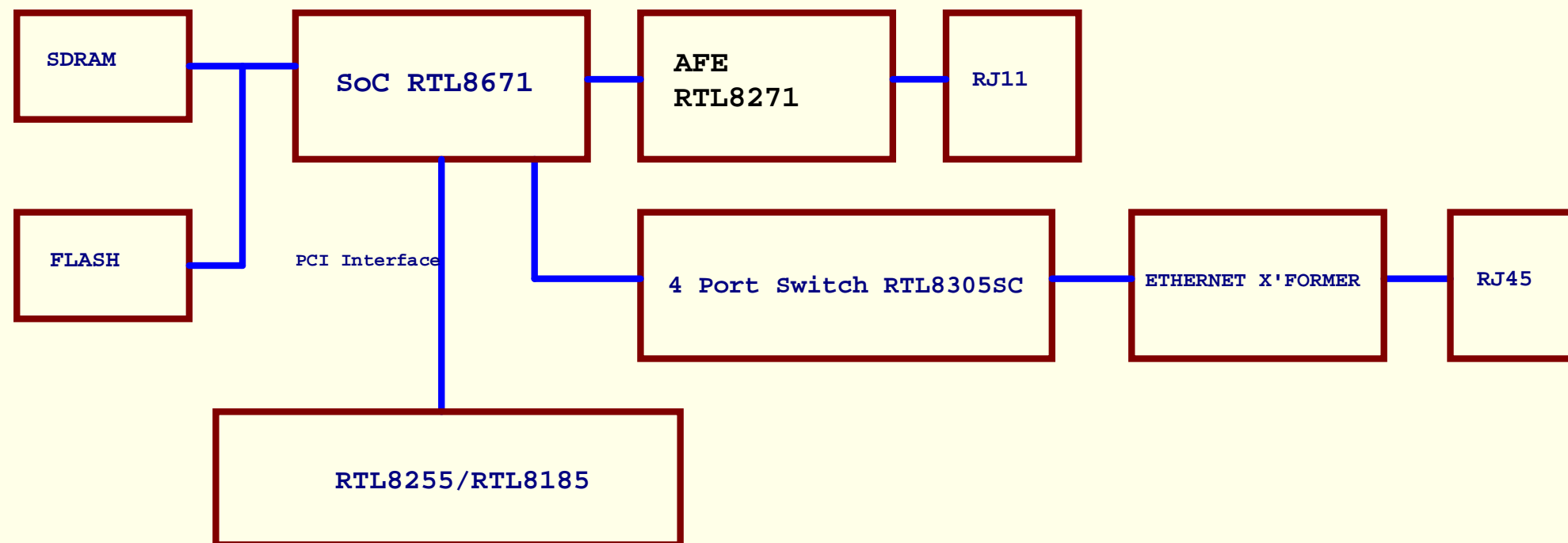
P09_RTL8225
P09_RTL8225.sch

- TRSW-
- TRSW+
- A_PAPE
- TSSIO
- CALEN
- RIFSD
- RIFSC
- RFLD
- 40MHzOSC



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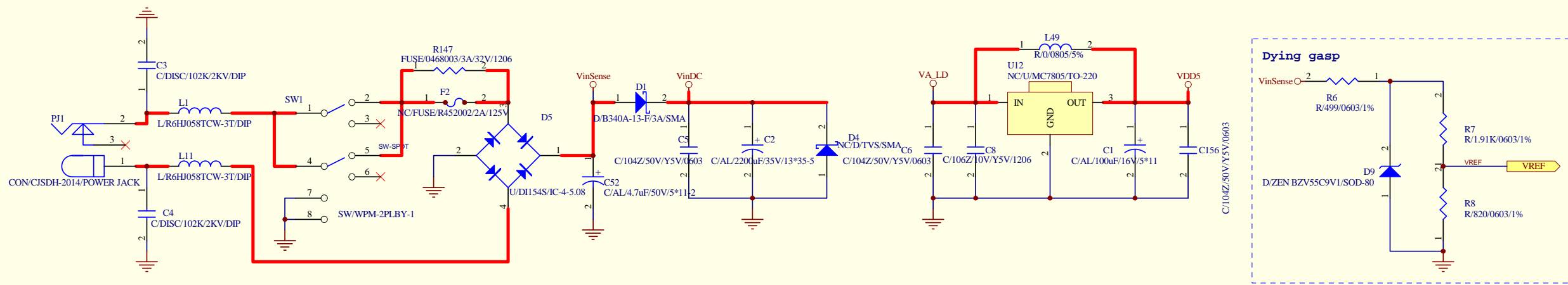
ADSL2+_4 Port Switch_WLAN



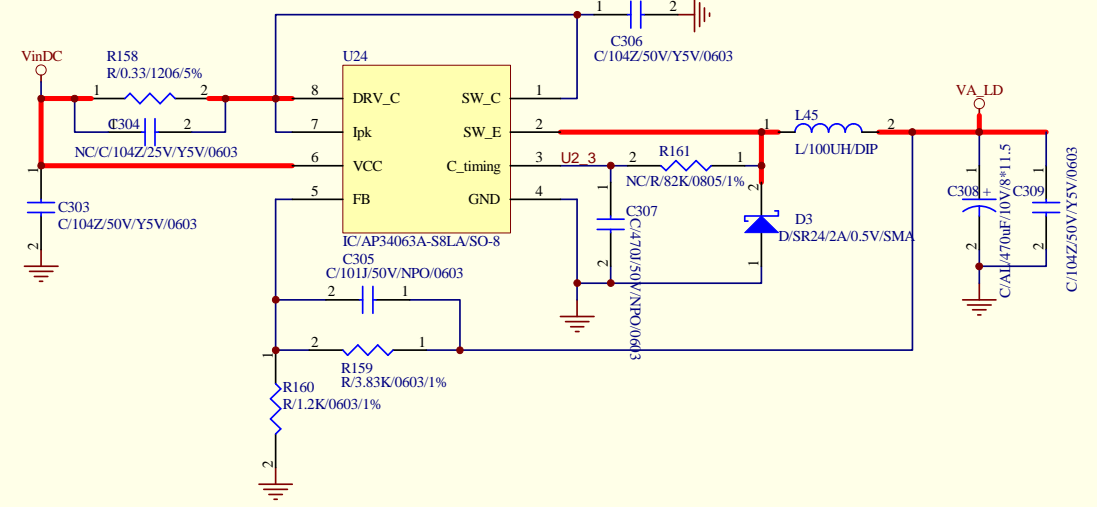
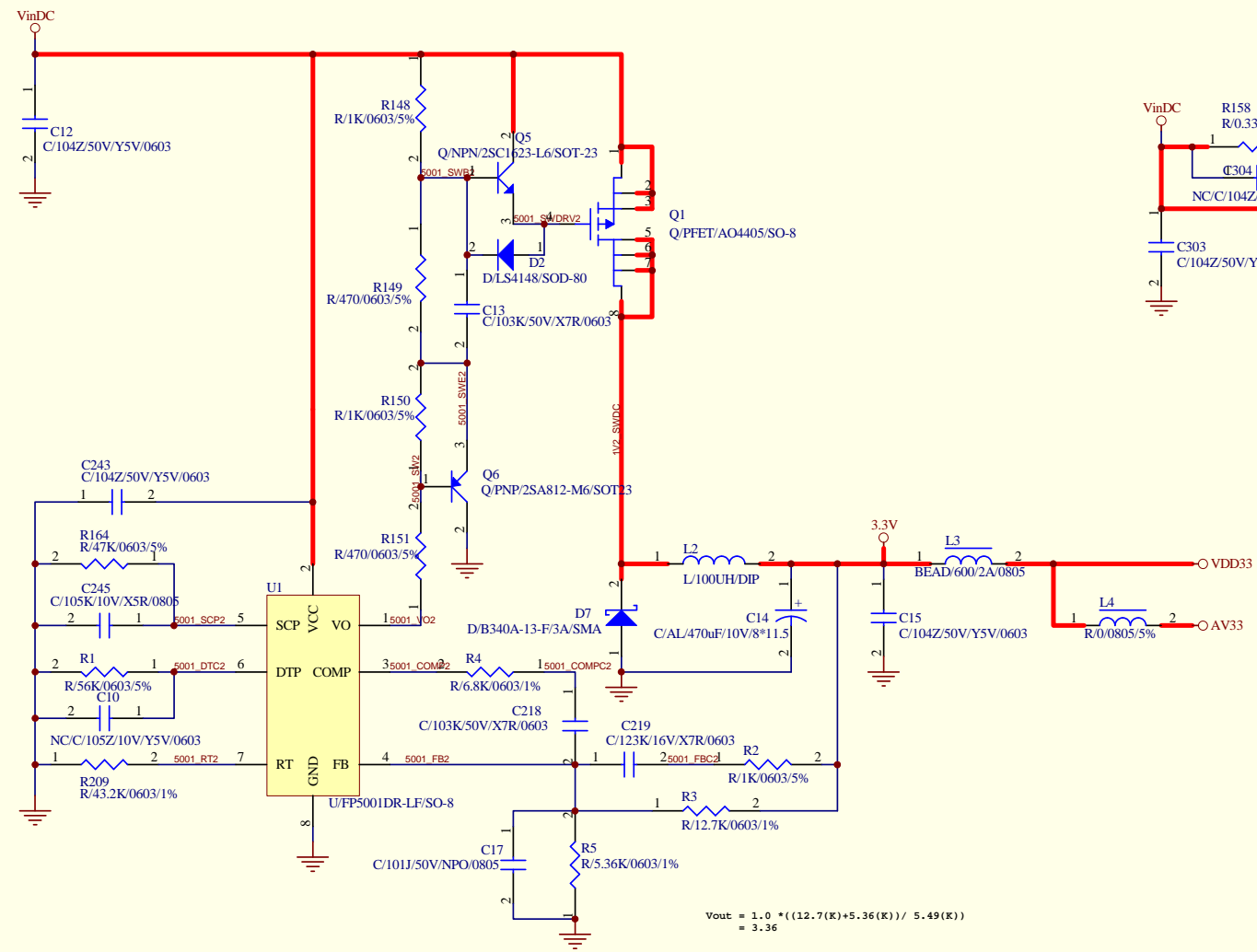
POWER

5V;3.3v;2.5V: for RTL8271
 3.3V;1.5V: for RTL8671

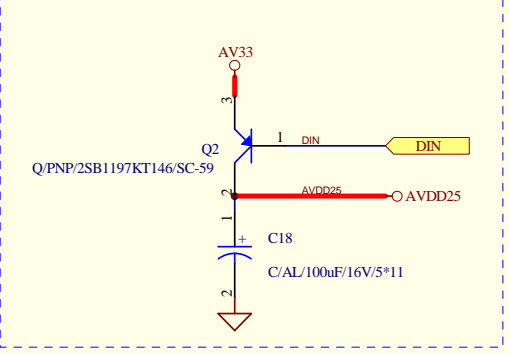
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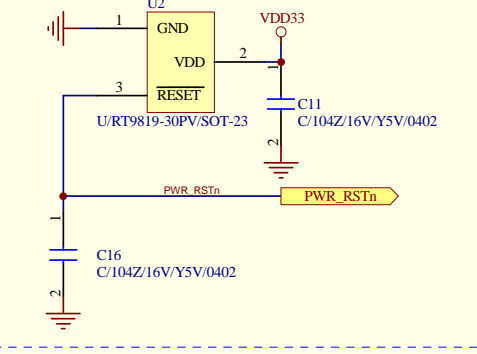
Power of System 3.3 V



Power of System 2.5V

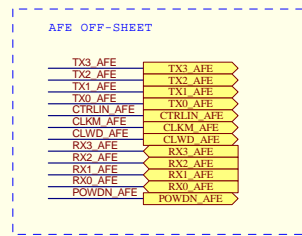
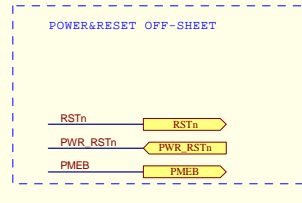
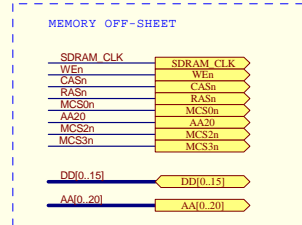
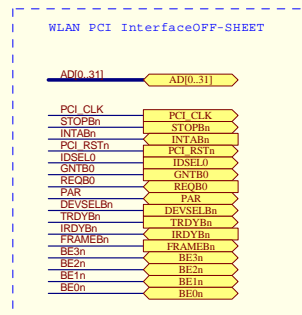
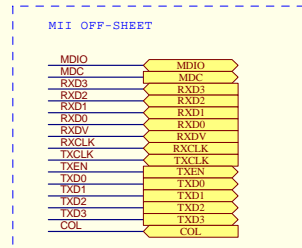
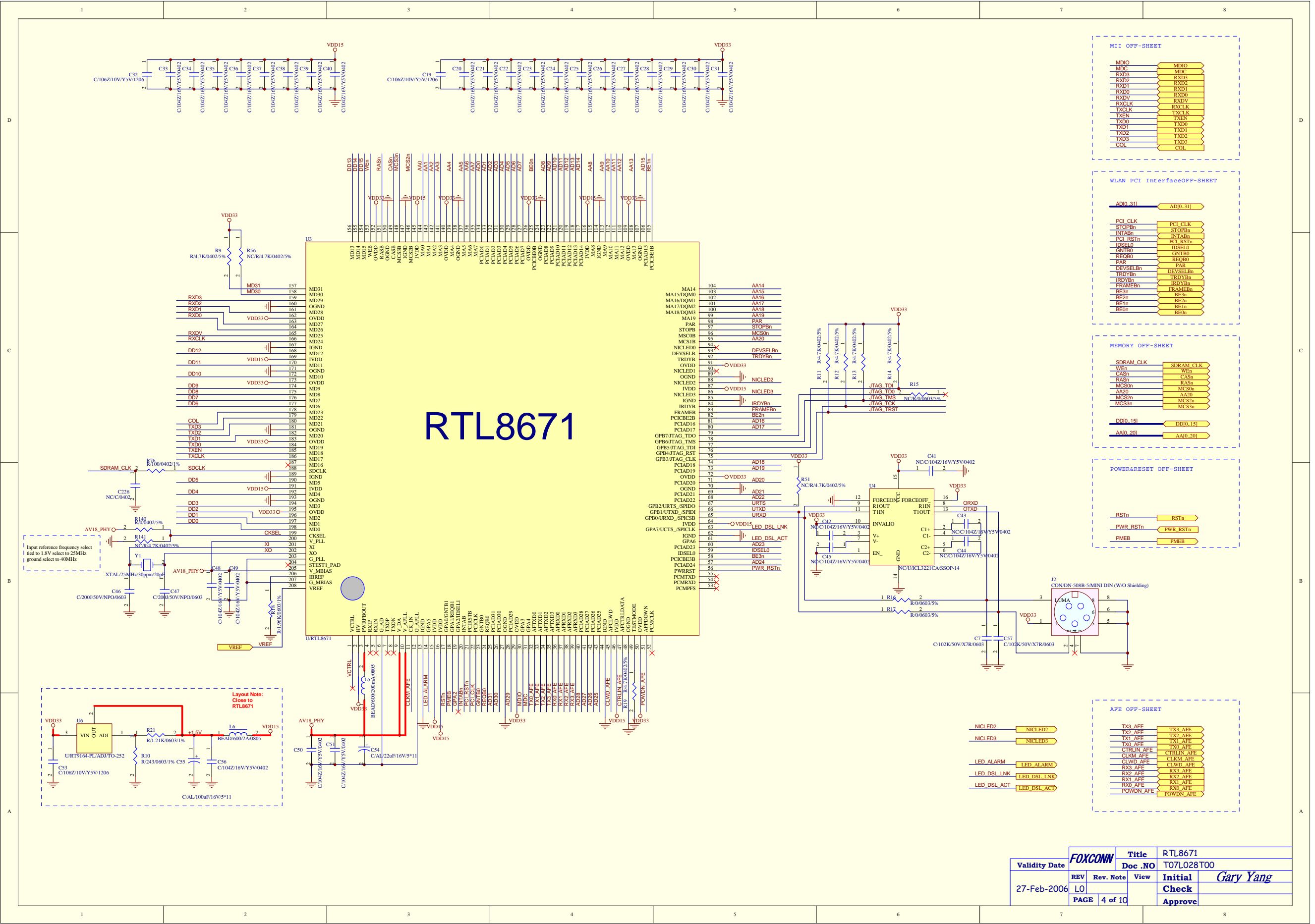


RESET#

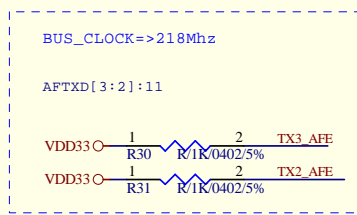
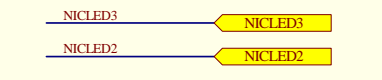
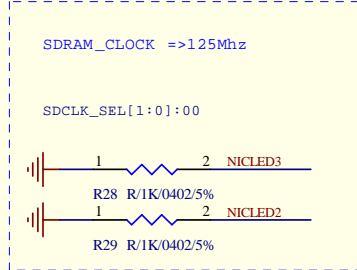
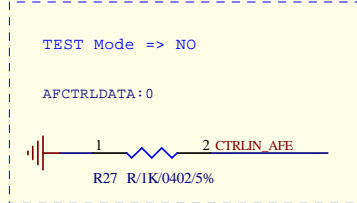
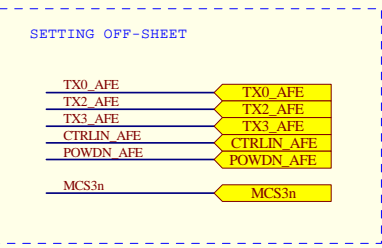
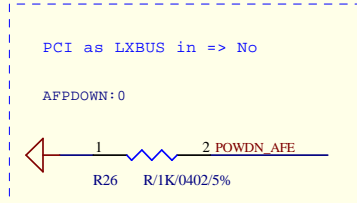
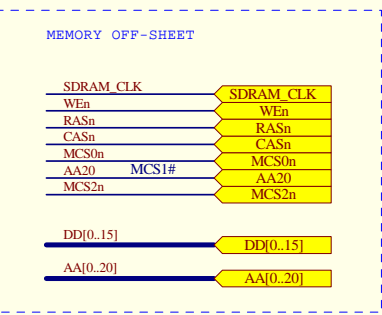
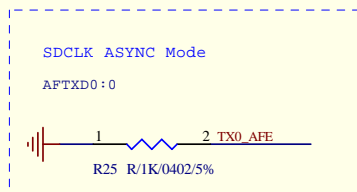
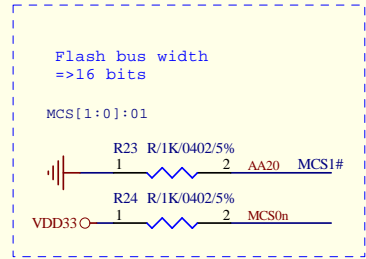
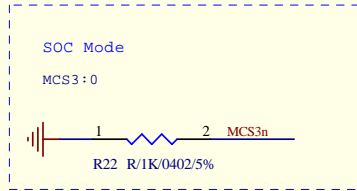
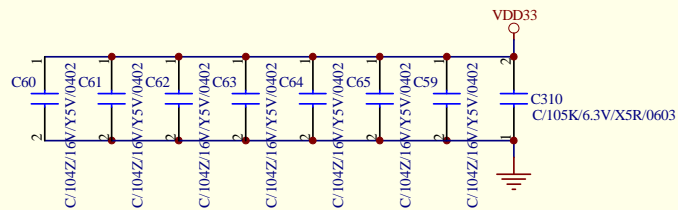
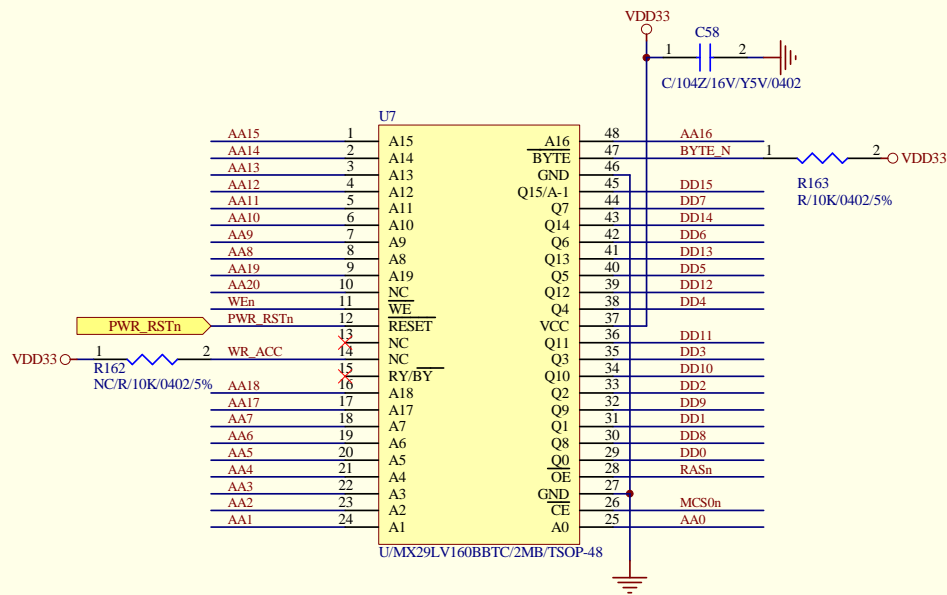
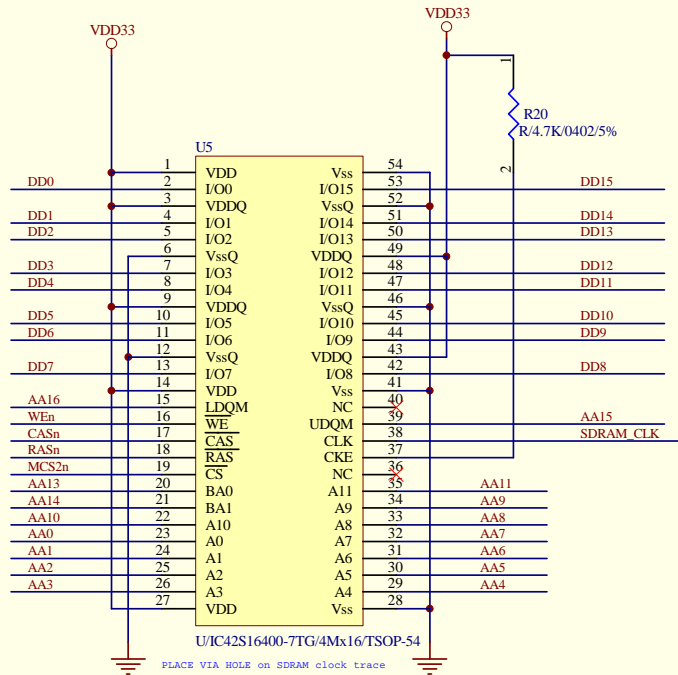


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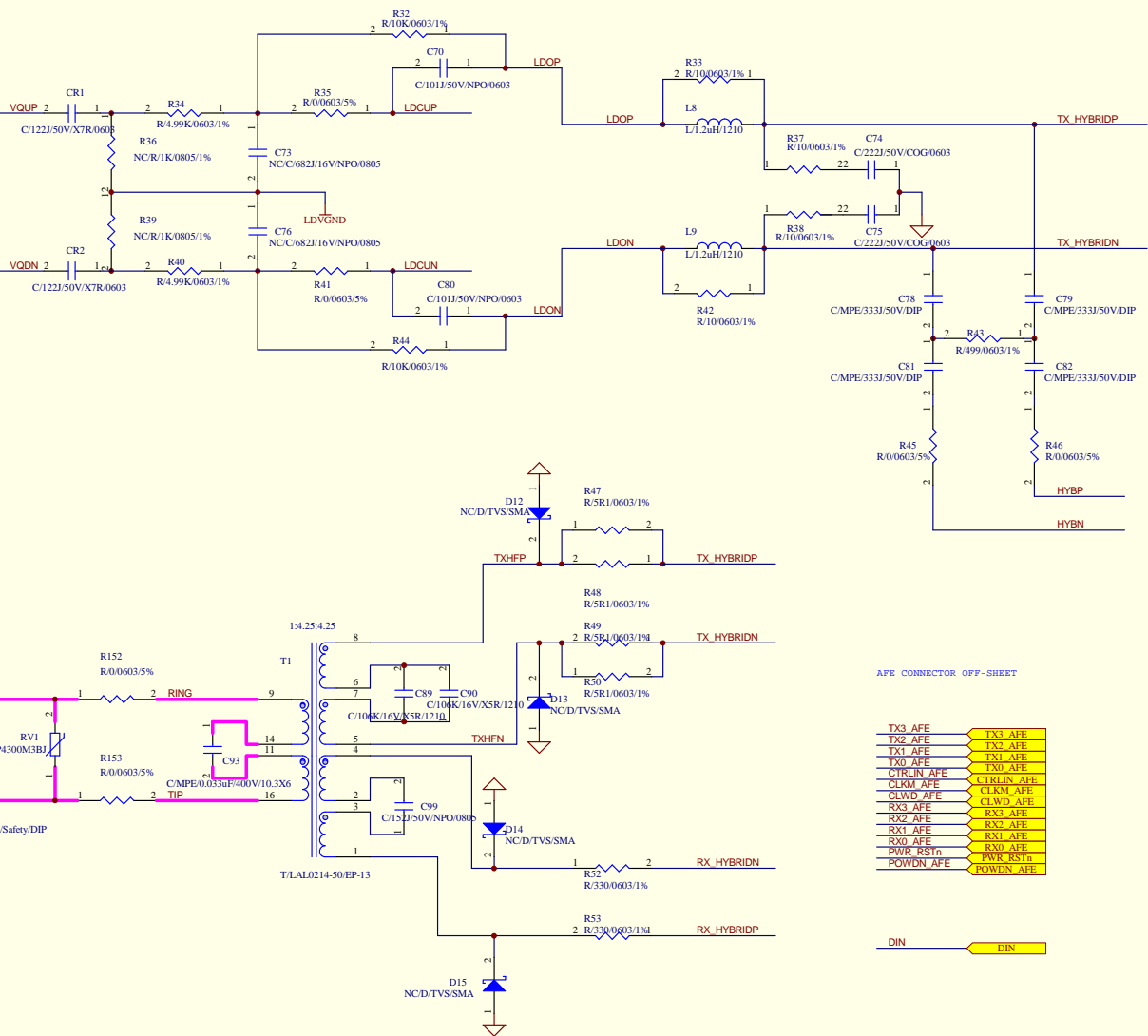
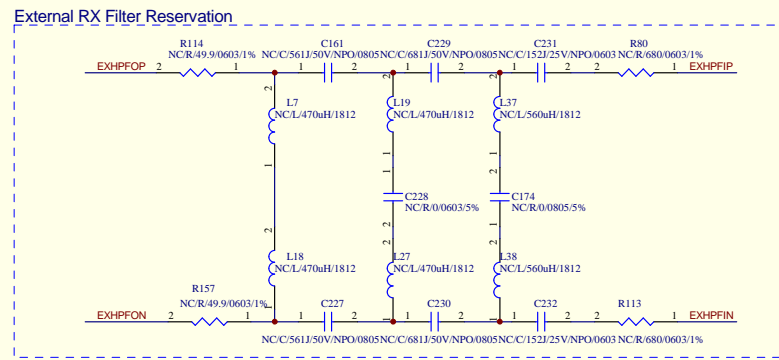
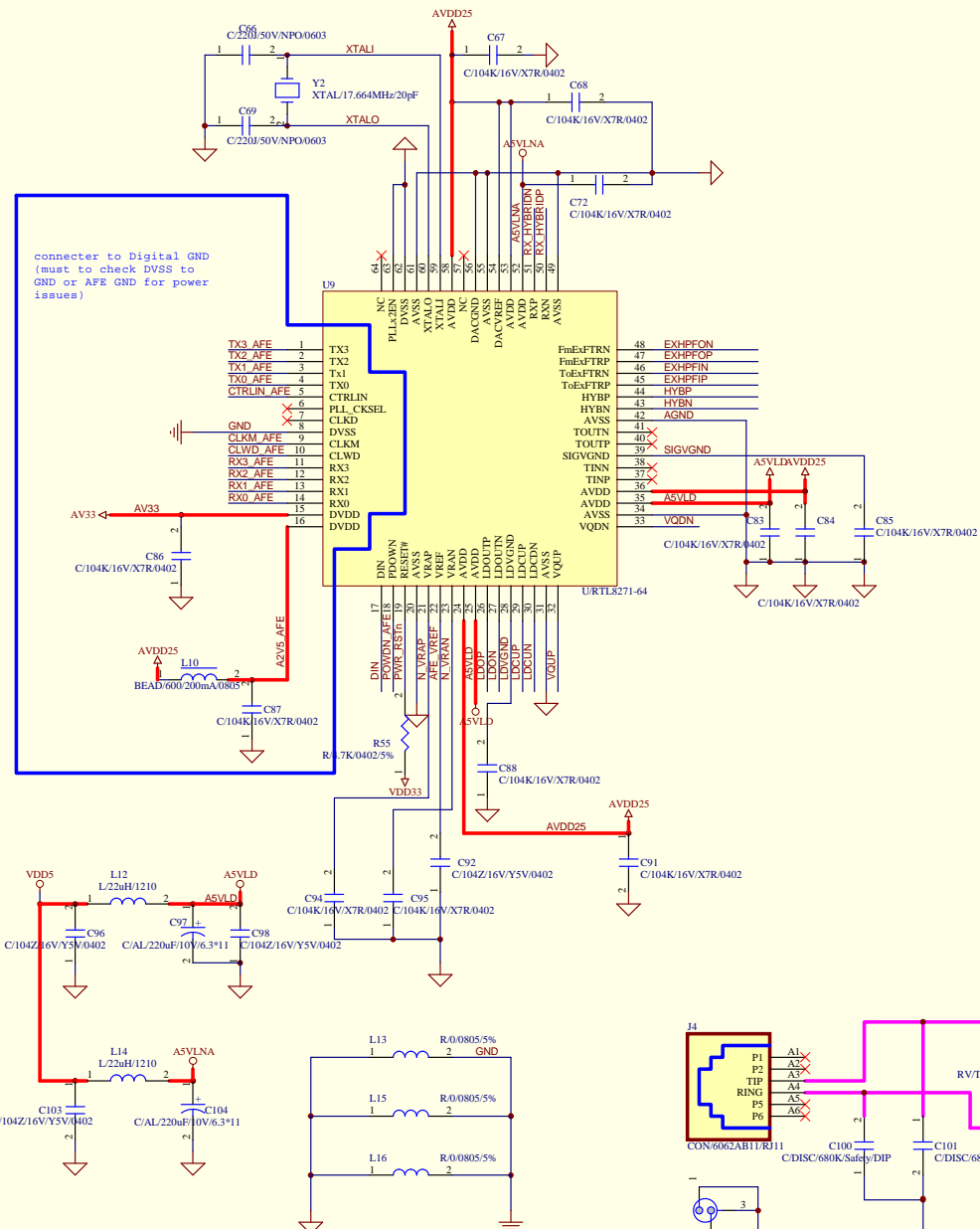
RTL8671



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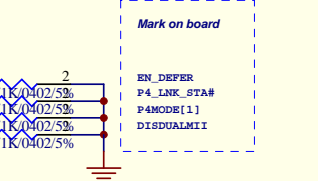
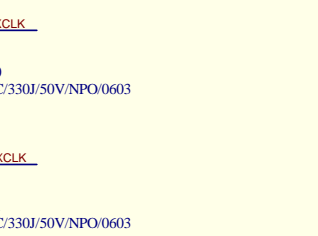
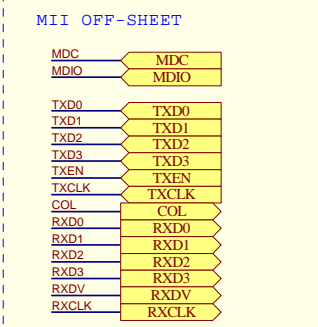
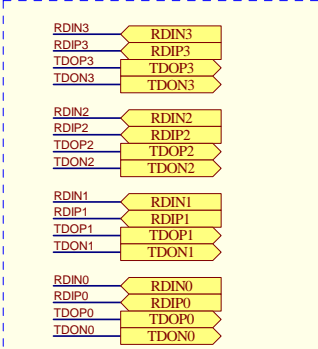
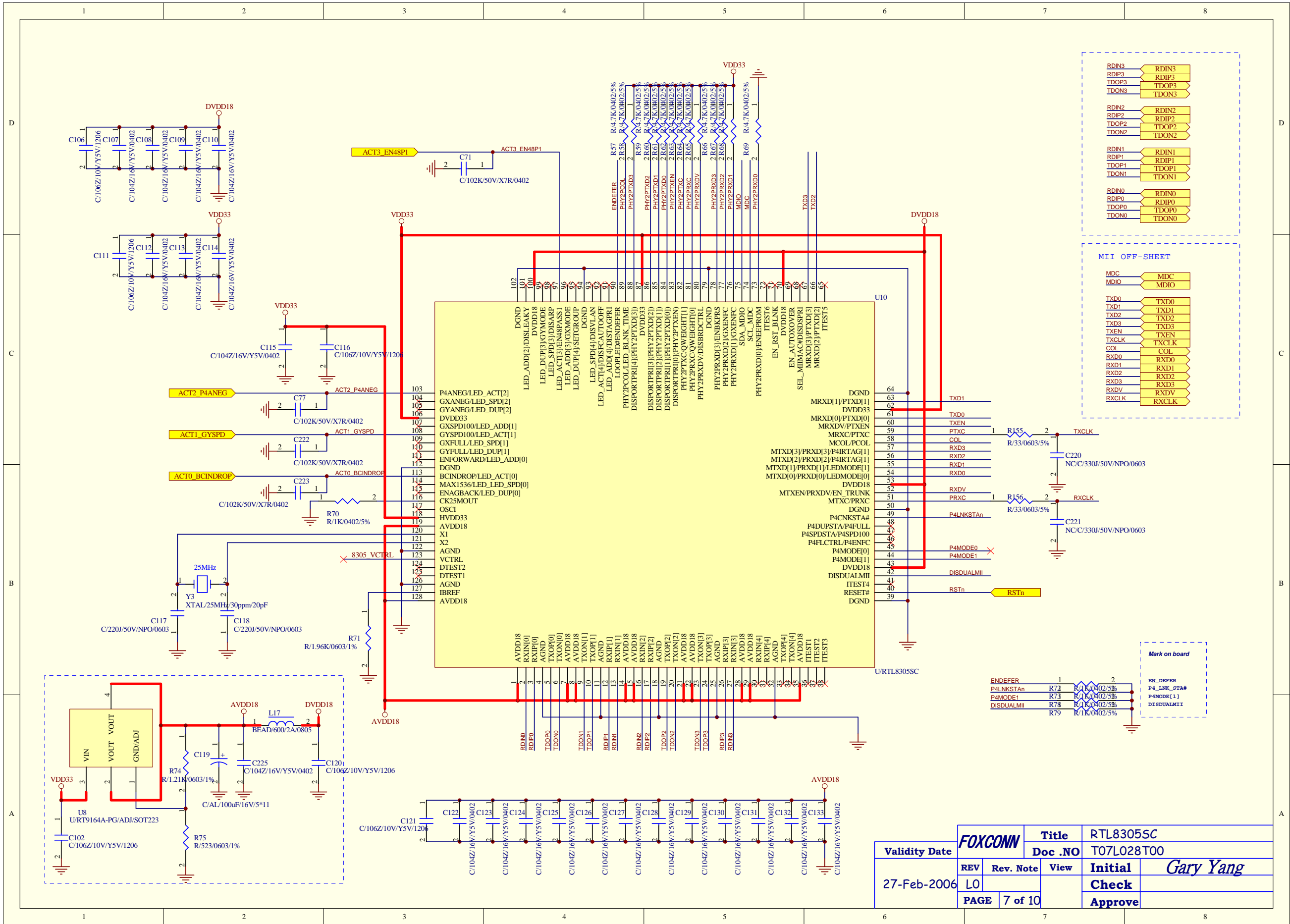


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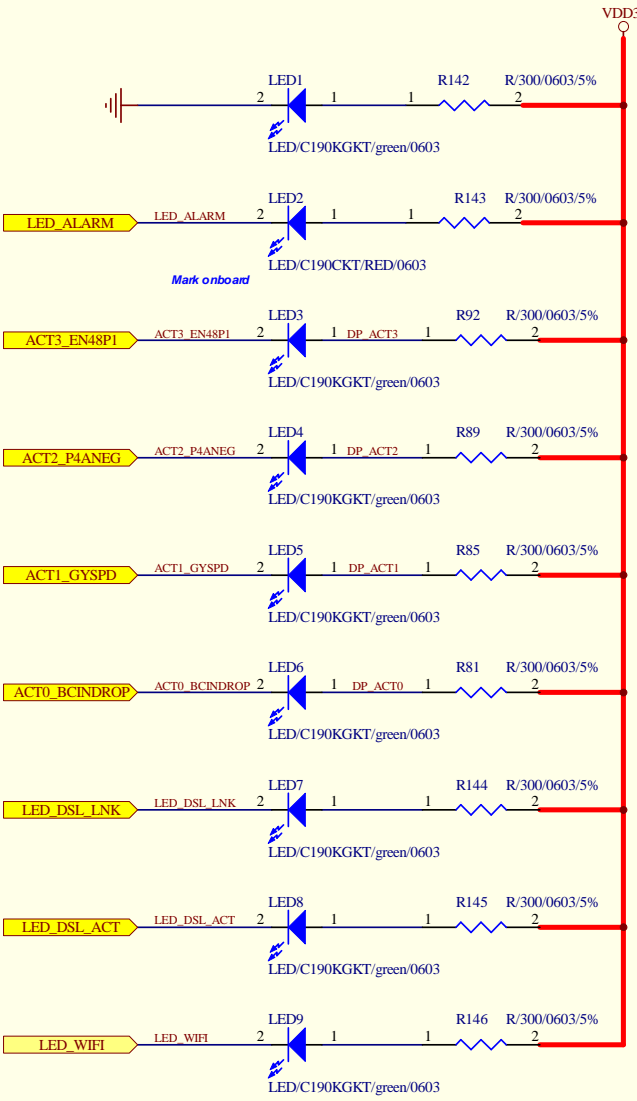
- AFE CONNECTOR OFF-SHEET
- | | |
|------------|------------|
| TX3_AFE | TX3_AFE |
| TX2_AFE | TX2_AFE |
| TX1_AFE | TX1_AFE |
| TX0_AFE | TX0_AFE |
| CTRLIN_AFE | CTRLIN_AFE |
| CLKM_AFE | CLKM_AFE |
| CLWD_AFE | CLWD_AFE |
| RX3_AFE | RX3_AFE |
| RX2_AFE | RX2_AFE |
| RX1_AFE | RX1_AFE |
| RX0_AFE | RX0_AFE |
| PWR_RSTn | PWR_RSTn |
| POWEN_AFE | POWEN_AFE |
- DIN

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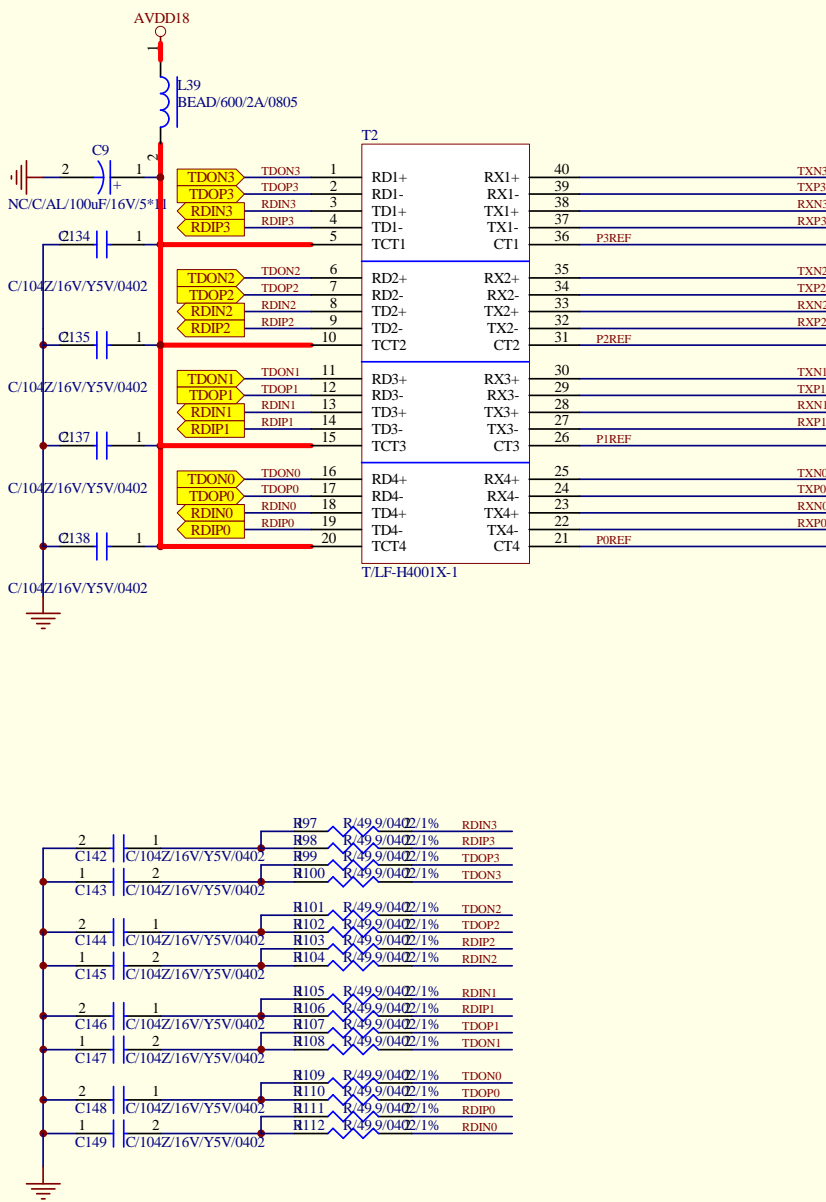


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		Approve	

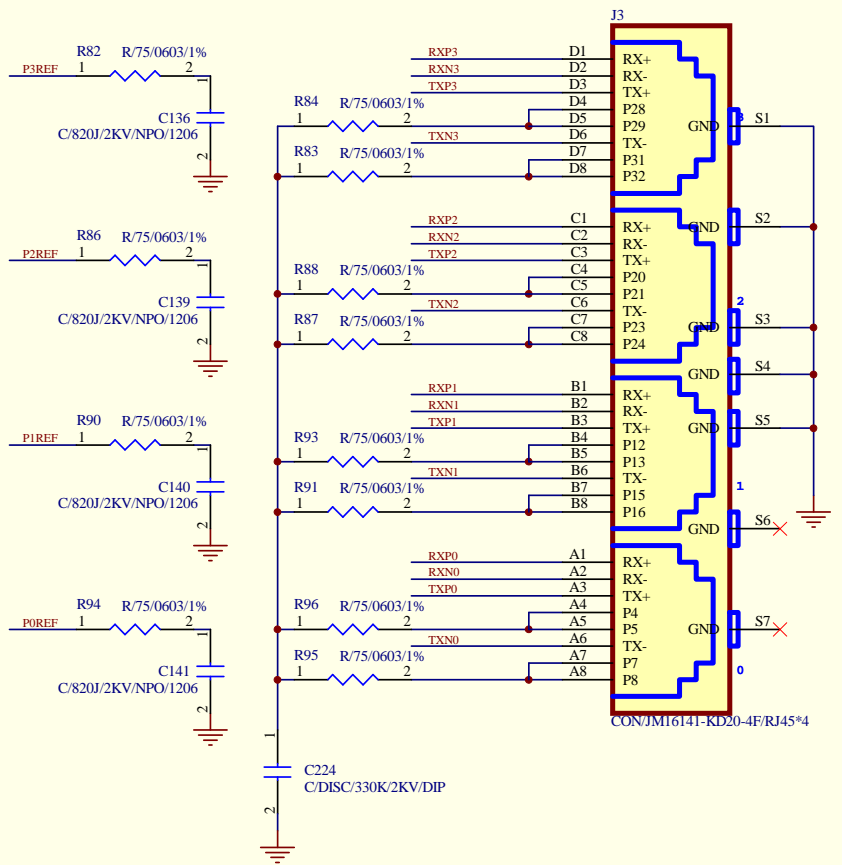
LED



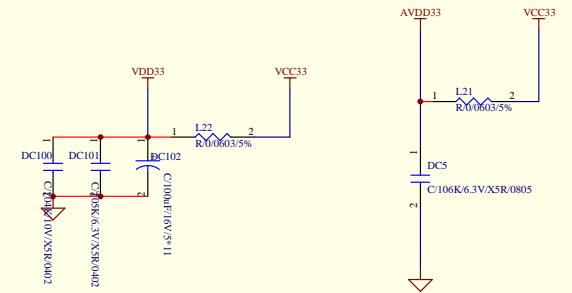
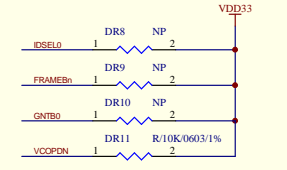
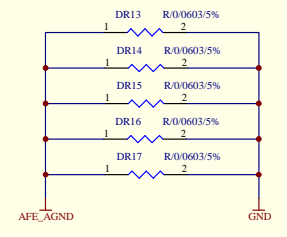
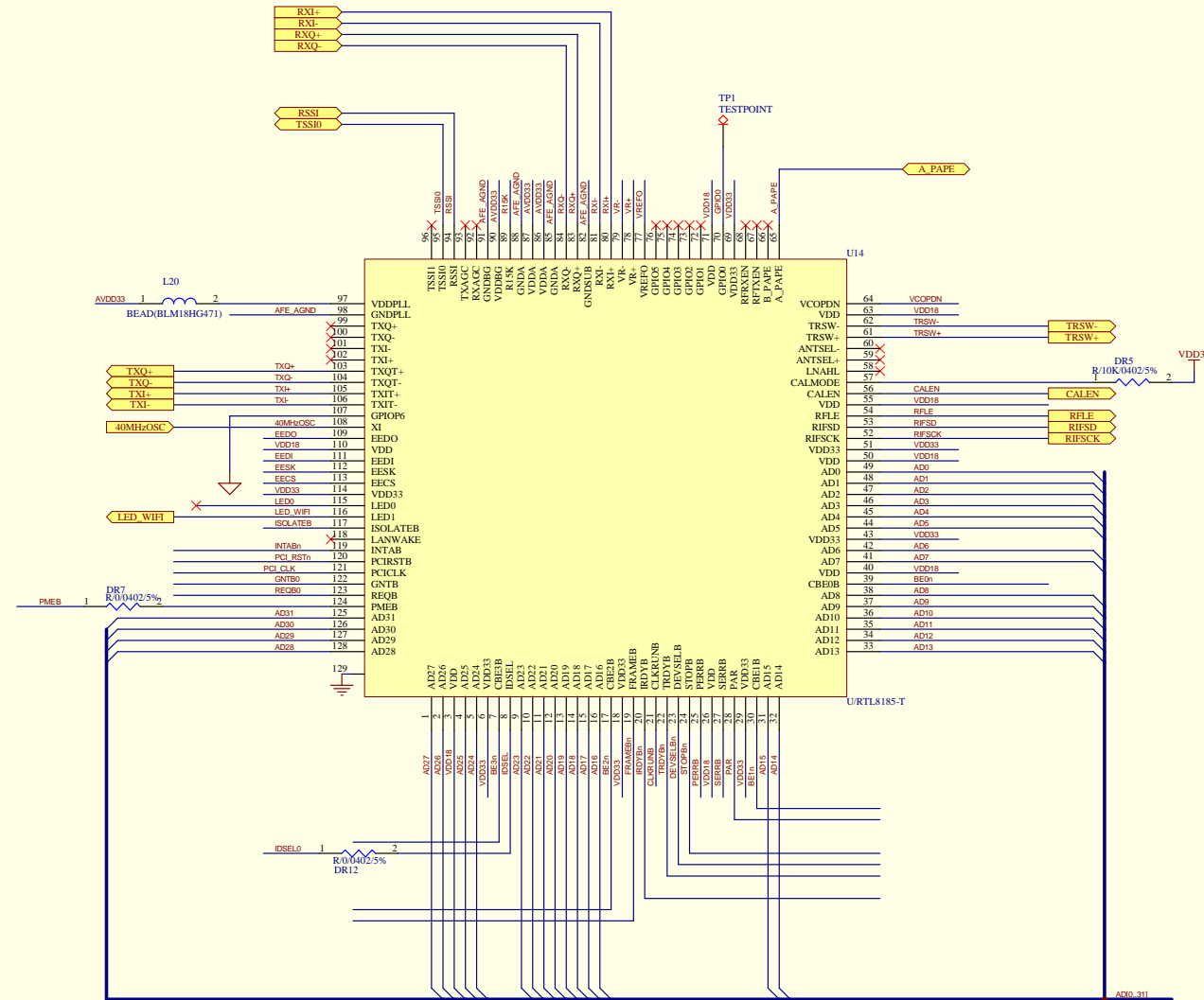
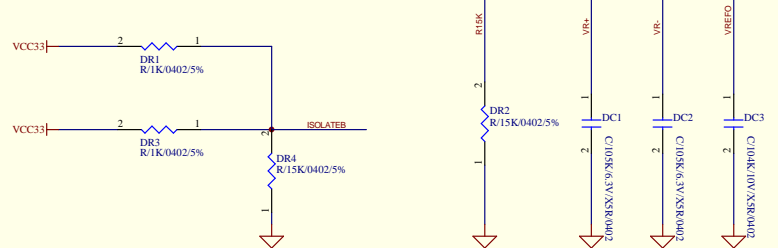
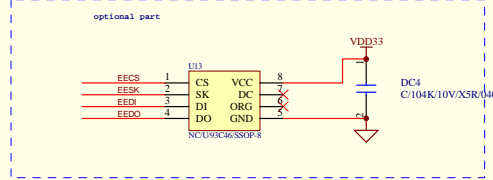
TRANSFORMER



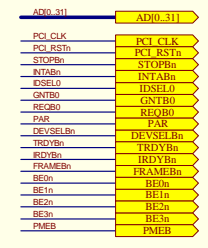
FRAME SIDE



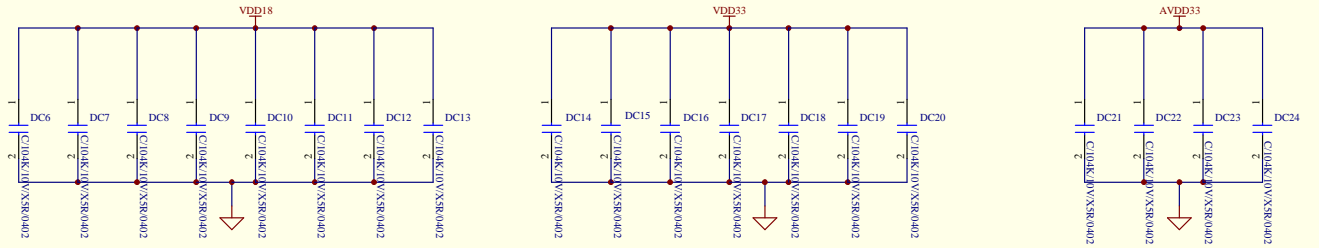
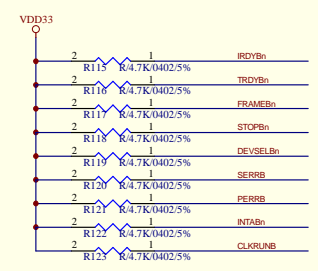
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WLAN PCI OFF-SHEET



Control Pin pull high



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