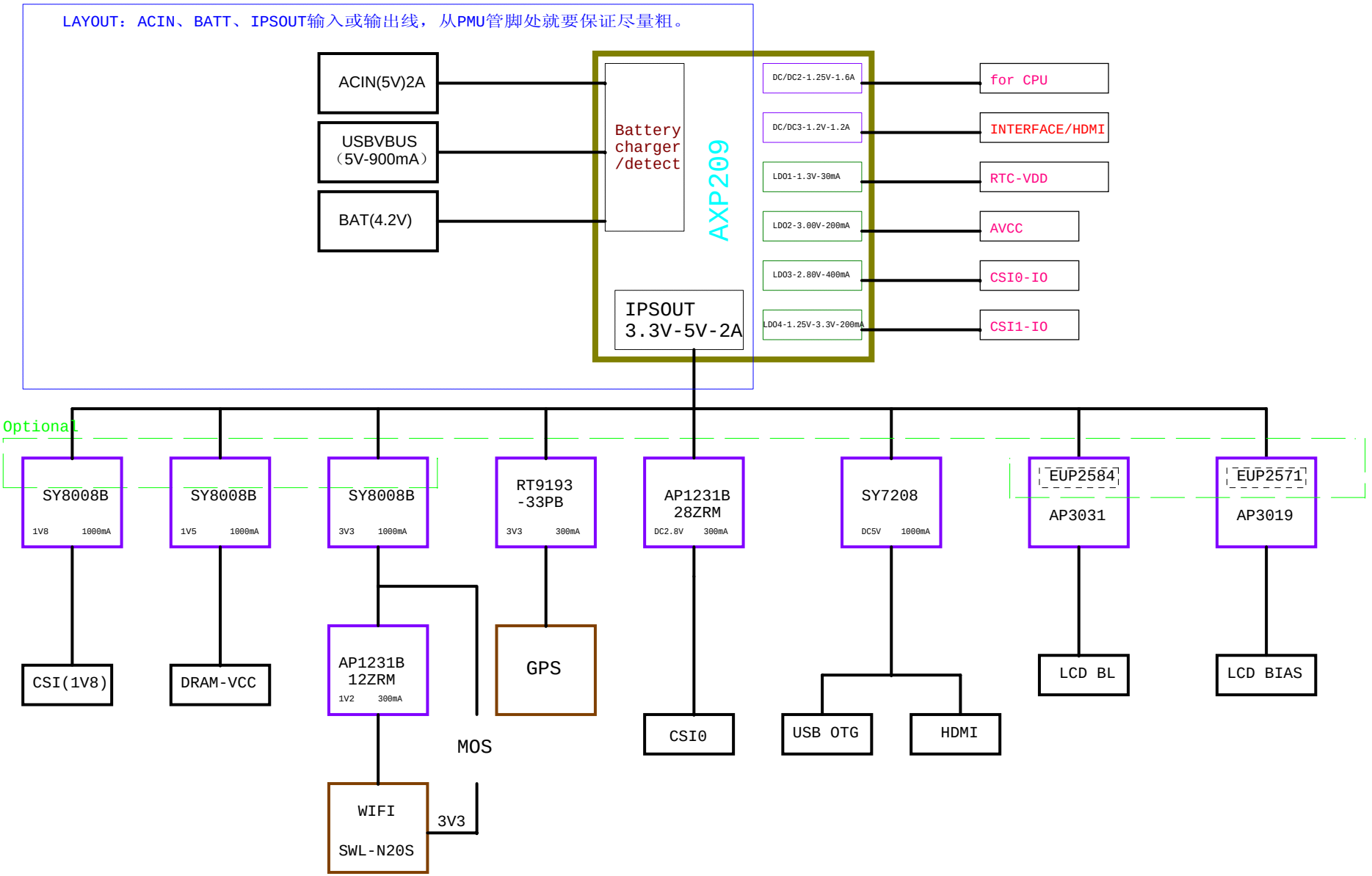
POWER TREE



MAINCHIP_PAD_DDR3		
Title: POWER TREE		
Size: A3	Document Number: <V1.0>	Rev:
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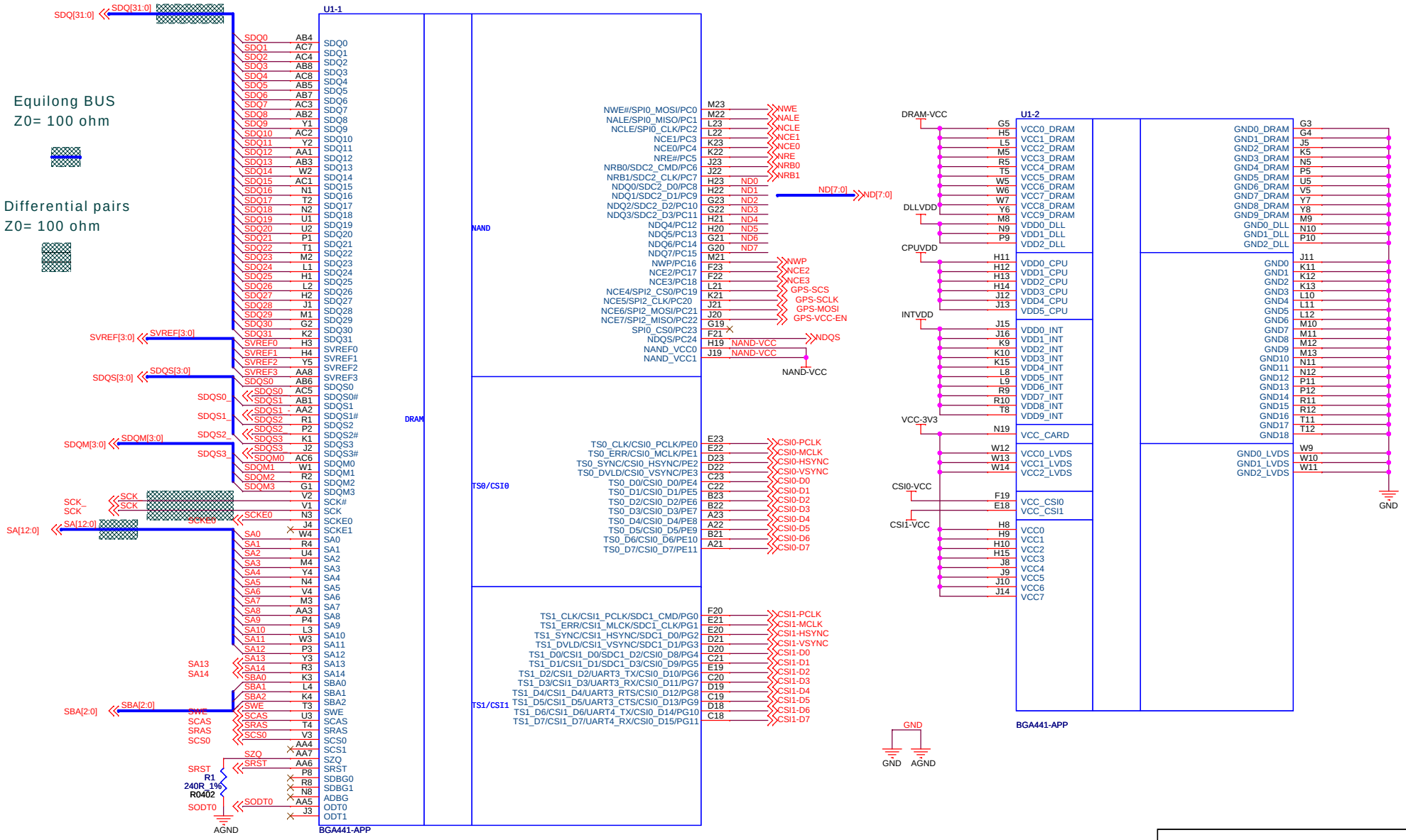
CPU1



Title	MAINCHIP_PAD_DDR3		
	CPU1		
Size A3	Document Number <V1.0>		Rev
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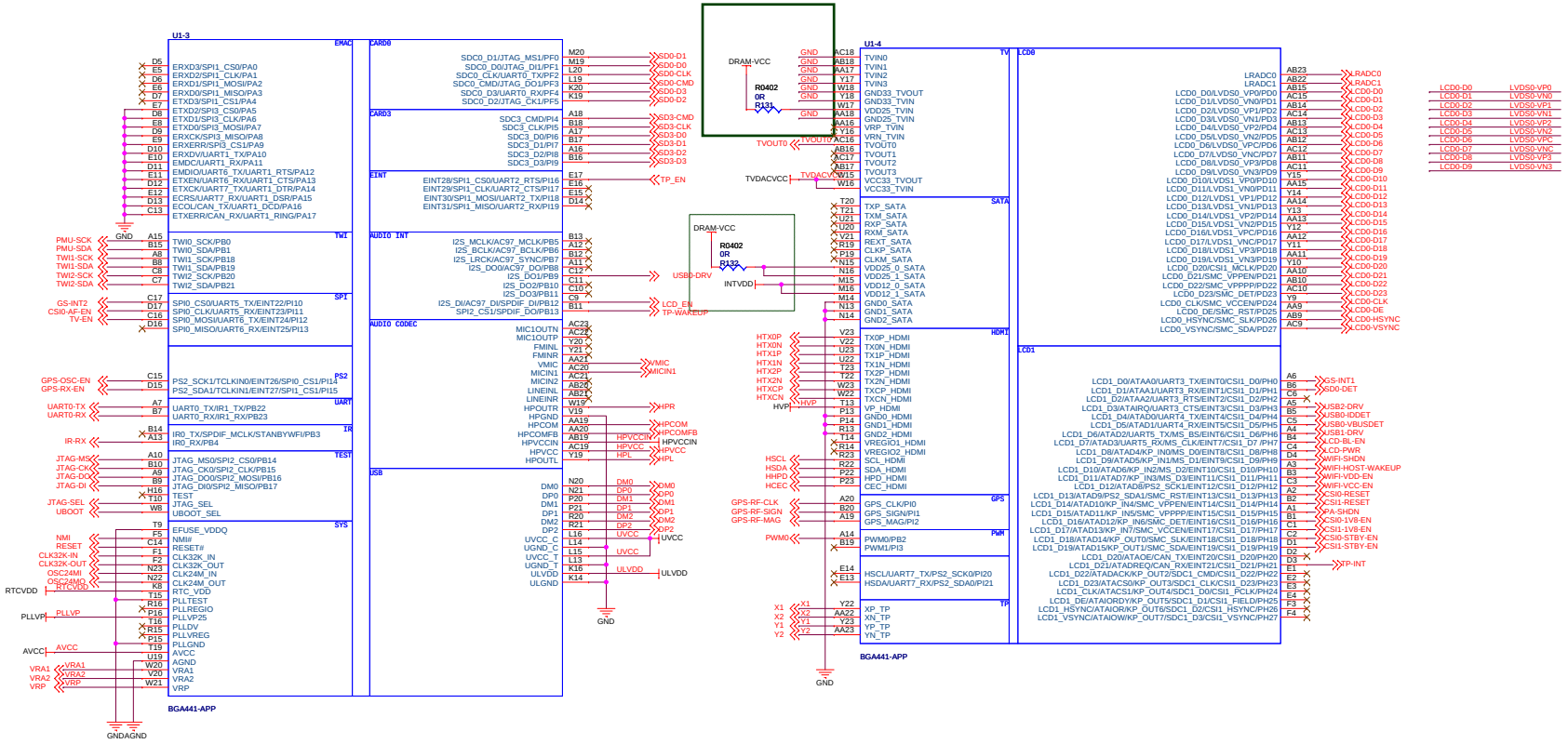
CPU2



Title	MAINCHIP_PAD_DDR3
	CPU2
Size	A2
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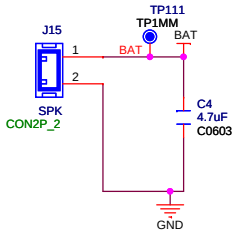
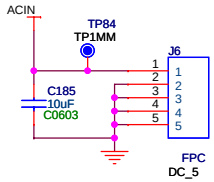
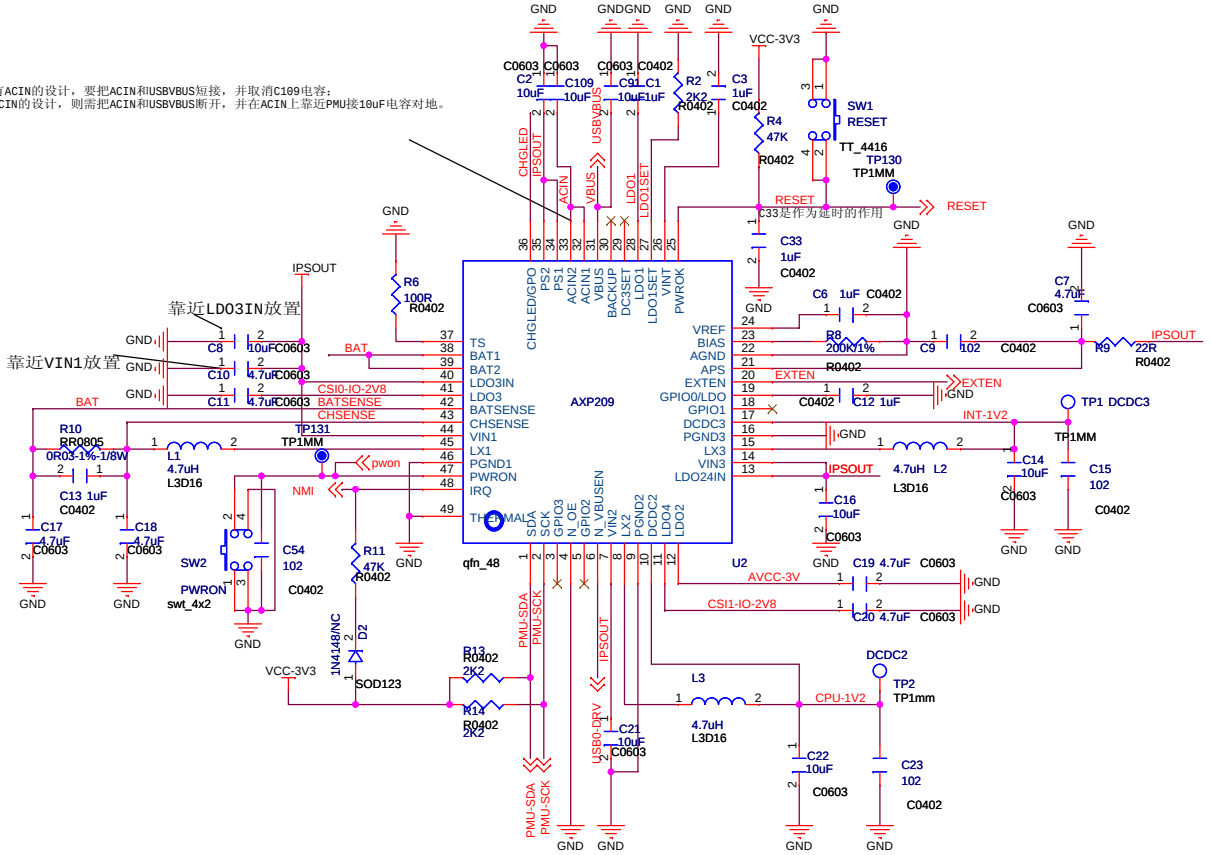
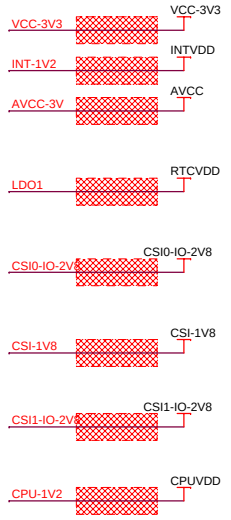
POWER-PMU



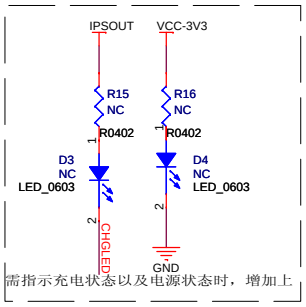
POWER INPUT







POWER LINE:Width>=60mil



PACK OK

MAINCHIP_PAD_DDR3		
Title: POWER-PMU		
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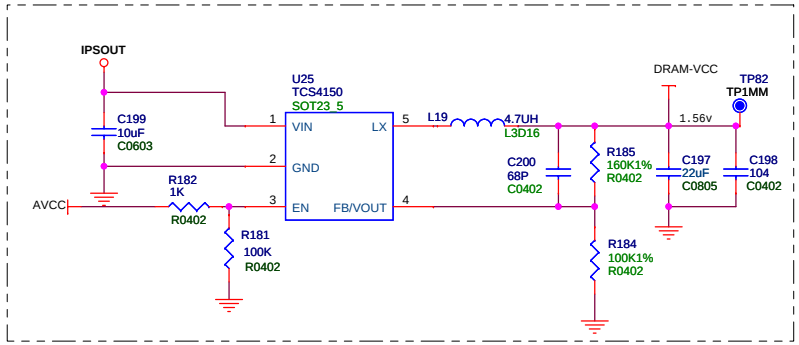
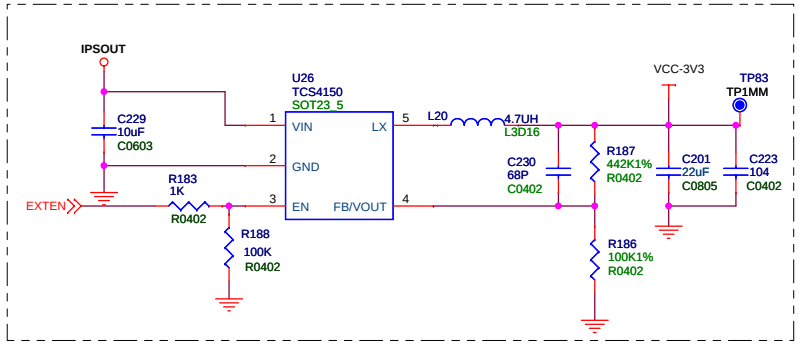
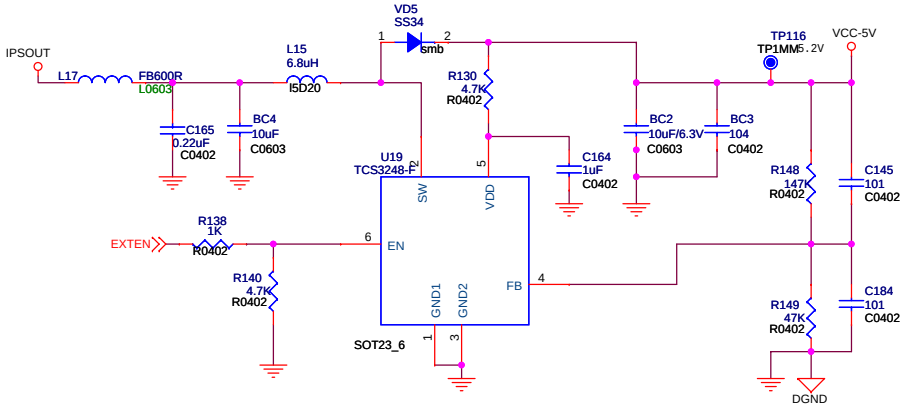
POWER-DC/DC







MAINCHIP_PAD_DDR3		
Title: POWER-DC/DC		
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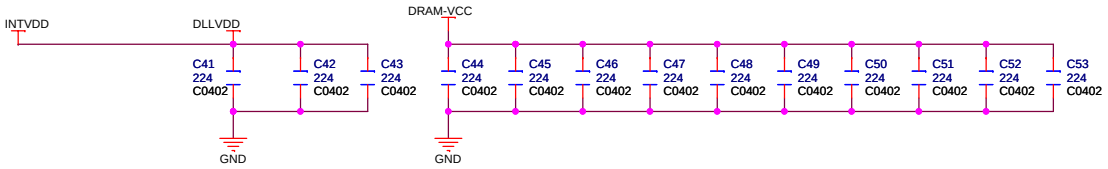
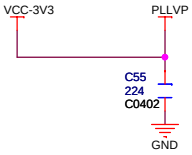
BESIDE CPU

DRAM

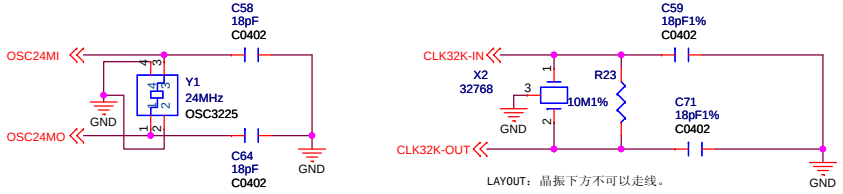
LAYOUT：按照一个PIN，放一个电容，并且尽量靠近PIN摆放。



PLL



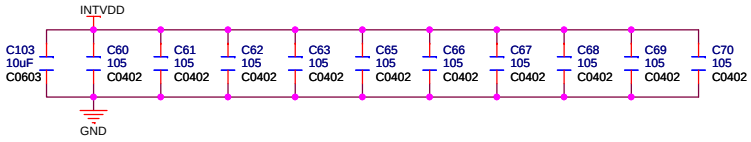
Crystal



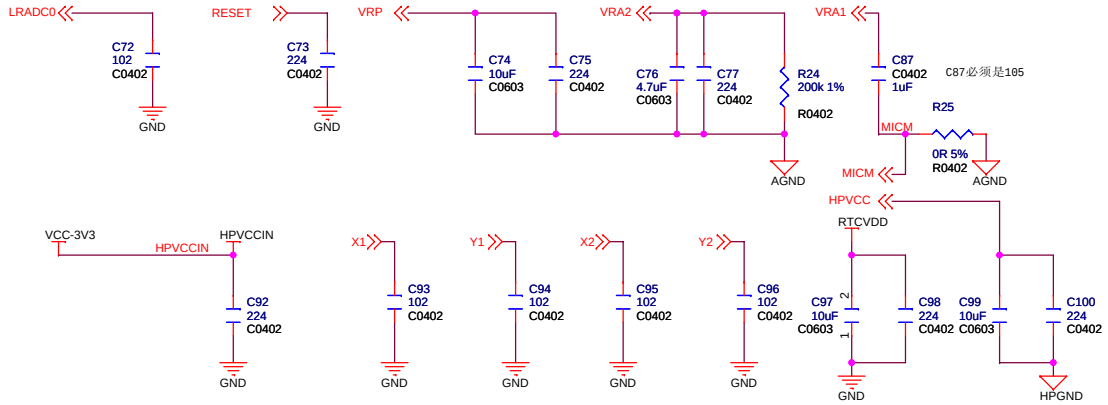
LAYOUT：晶振下方不可以走线。

CORE

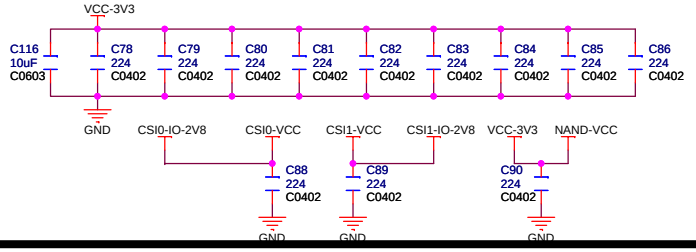
LAYOUT：按照一个PIN，放一个电容，并且尽量靠近PIN摆放。



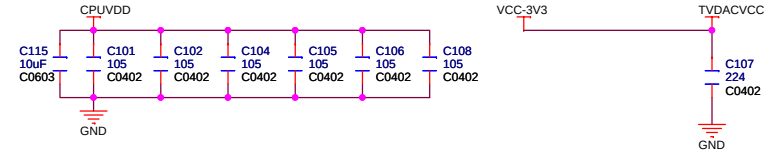
AUDIO&SYS&TP



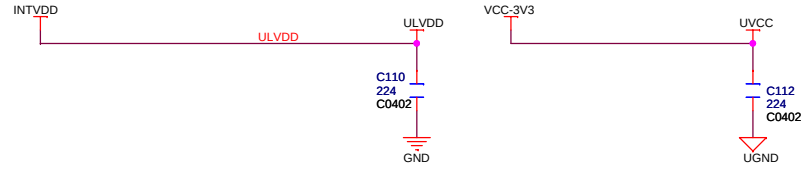
PIO-INTFACE



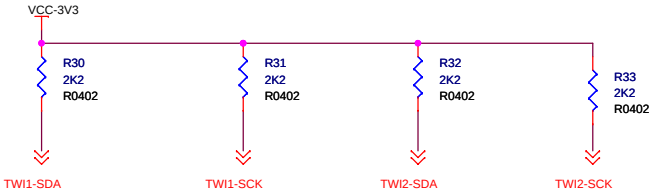
CPU&TV



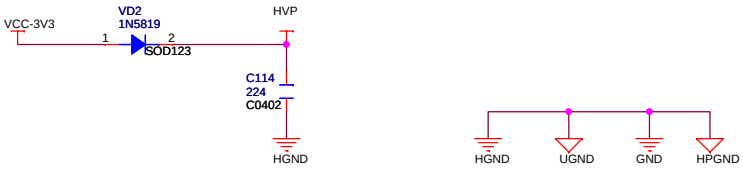
USB



TWI-PULLUP



HDMI



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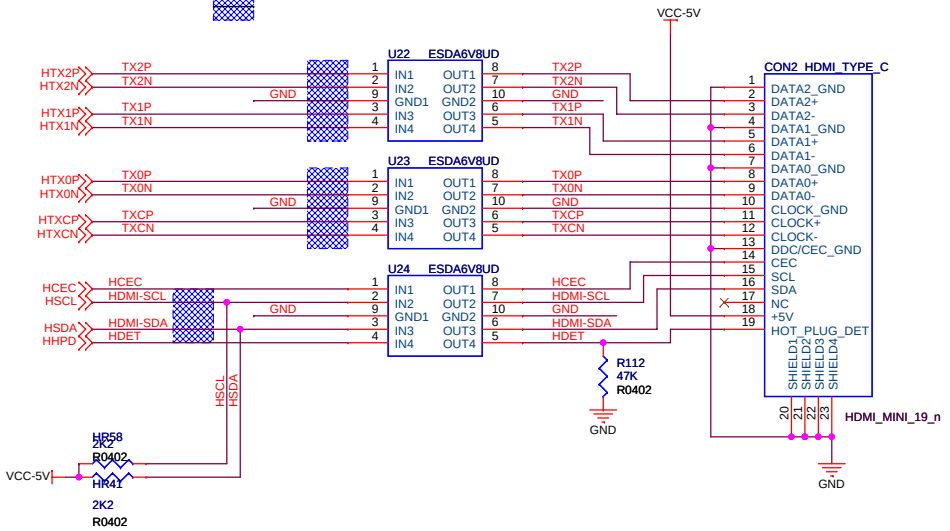
HDMI-CSI0

HDMI

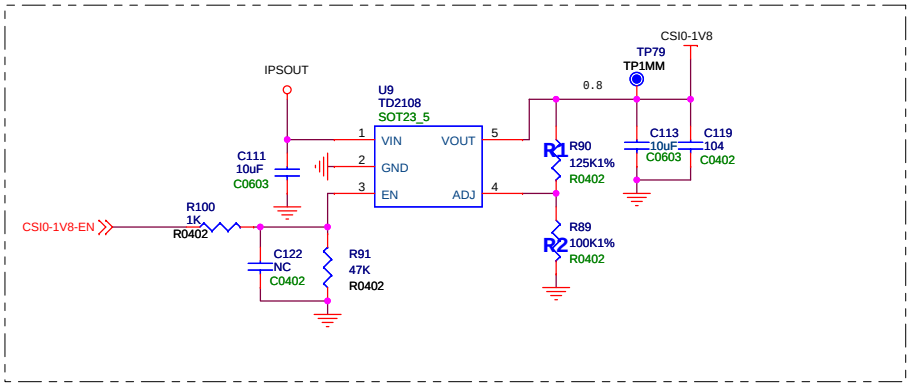
HDMI

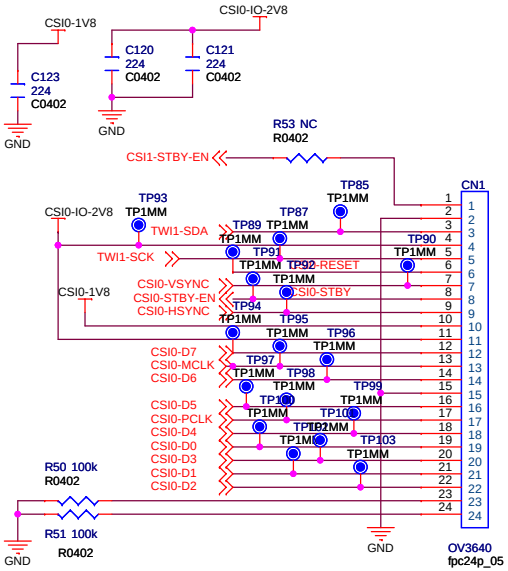
Differential pairs
Z0= 100 ohm

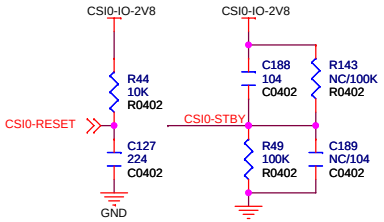


CSI1

前置低分辨率







请根据实际摄像头的要求选择上拉方式！

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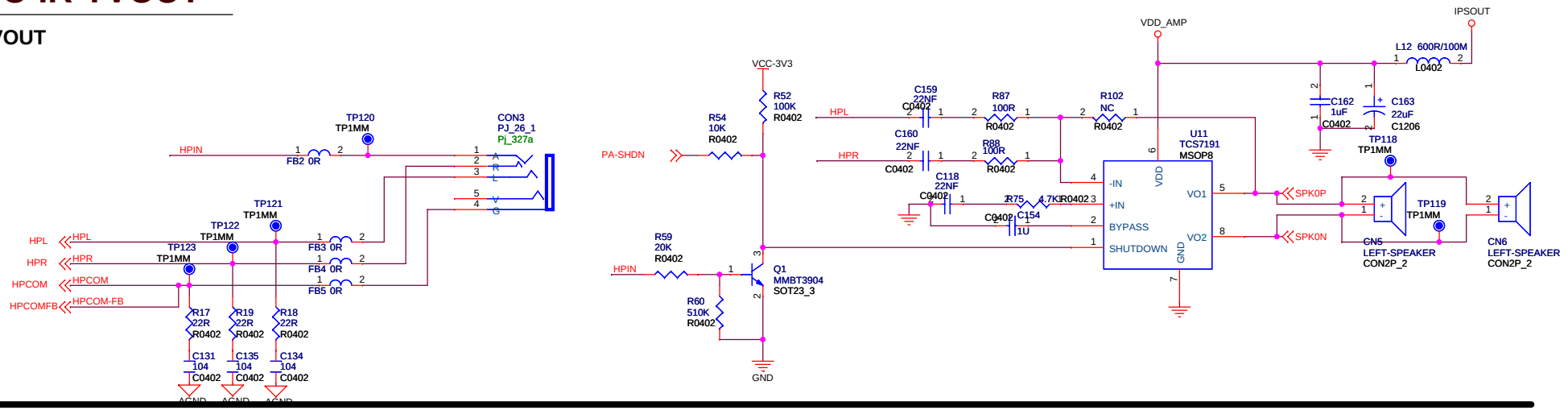
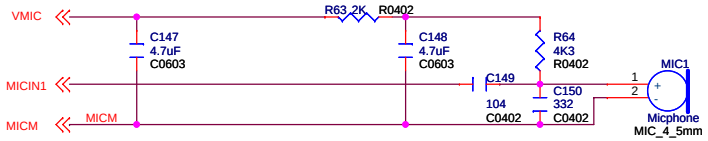
HP-KEY-MIC-IR-TVOUT

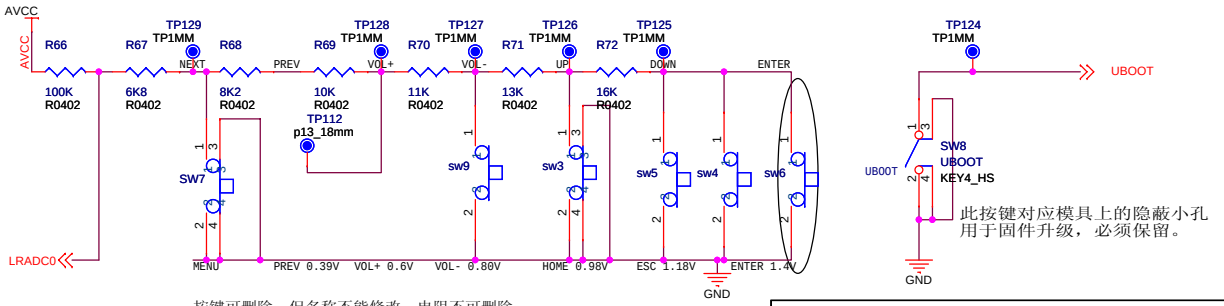
Head Phone & TVOUT



Microphone



KEY



此按键对应模具上的隐蔽小孔用于固件升级，必须保留。

按键可删除，但名称不能修改，电阻不可删除。

Title	MAINCHIP_PAD_DDR3	
	HP-KEY-MIC-IR-TVOUT	
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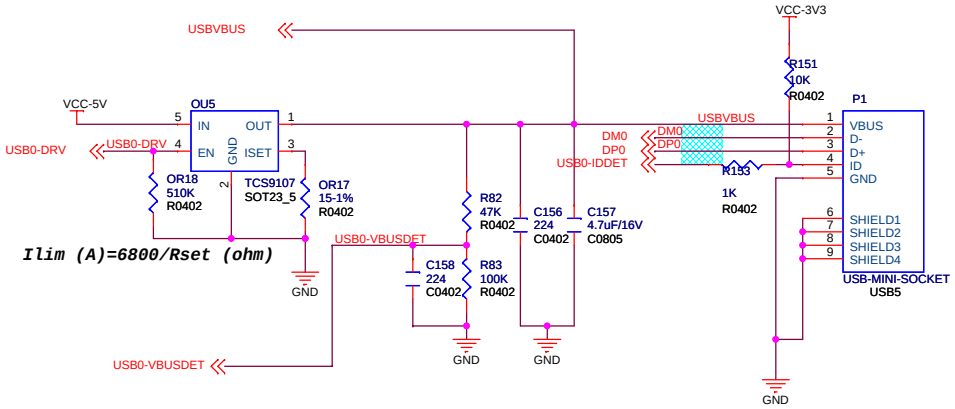
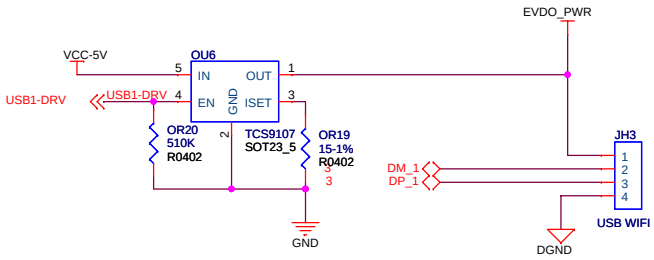
USB-CARD

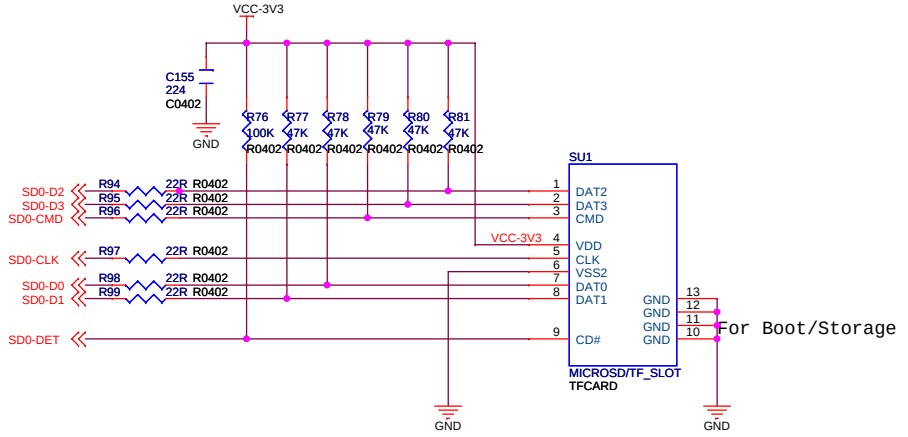
USB

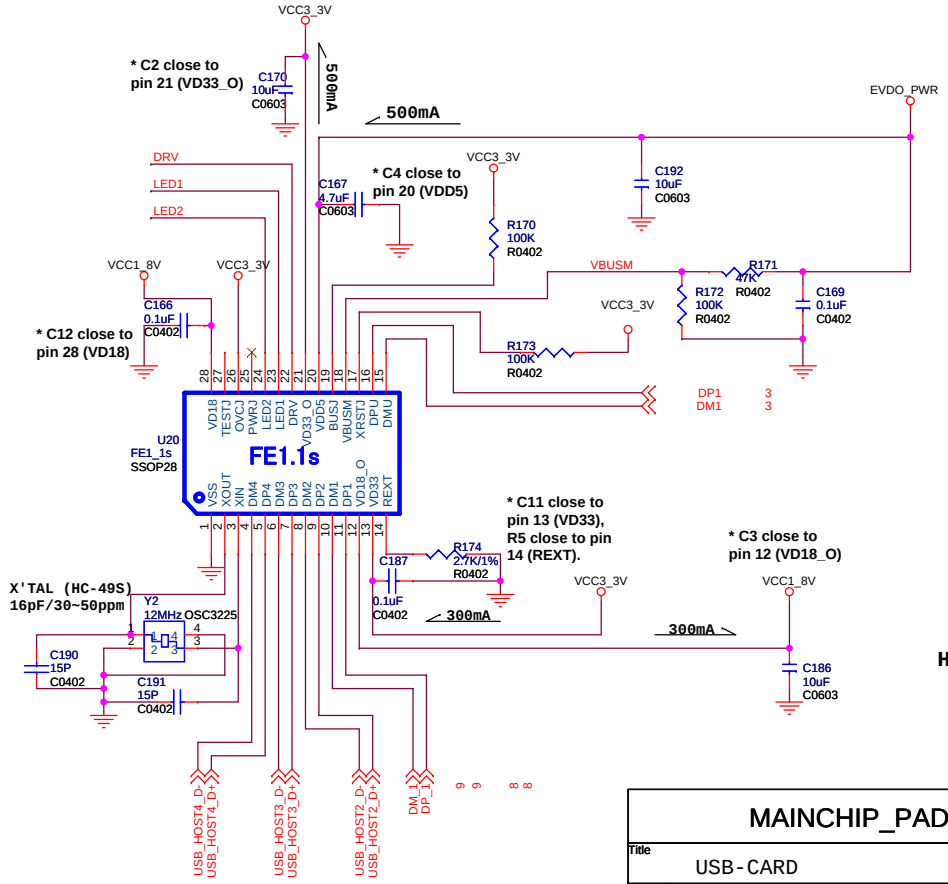


Differential pairs
Z0= 90 ohm



CARD0





HUB

MAINCHIP_PAD_DDR3		
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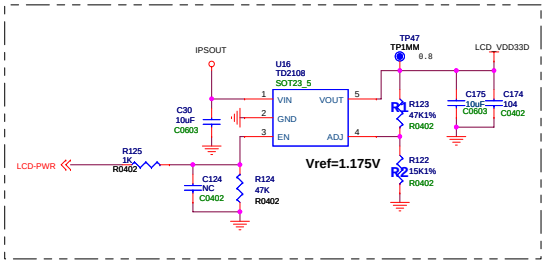
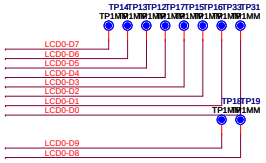
RGB888(24BIT) 7" WVGA LCD I/F

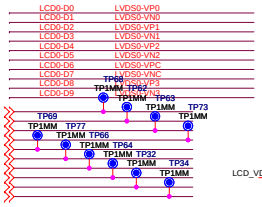




RGB888(24bit)	
LPD[7:0] LPD[15:8] LPD[23:16]	Blue Green Red

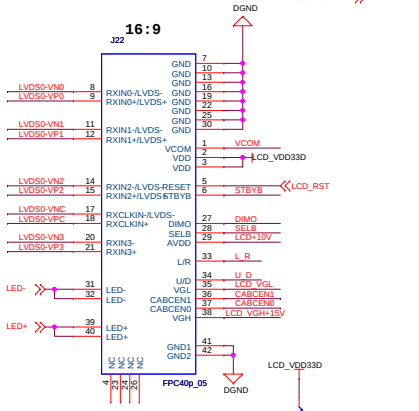
RGB666(18bit)	
LPD[5:0] LPD[11:6] LPD[17:12]	Blue Green Red

RGB565(16bit)	
LPD[4:0] LPD[10:5] LPD[15:11]	Blue Green Red

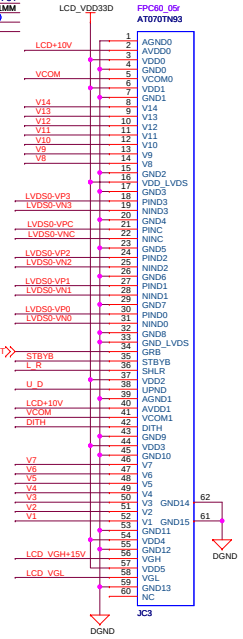


7''高清

16:9

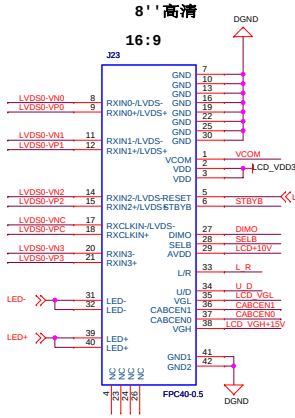


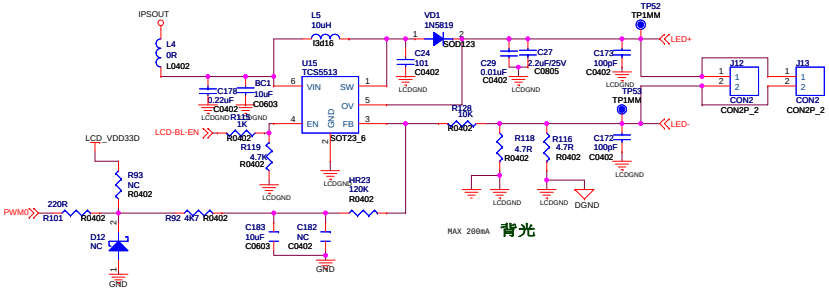
10.1''高清

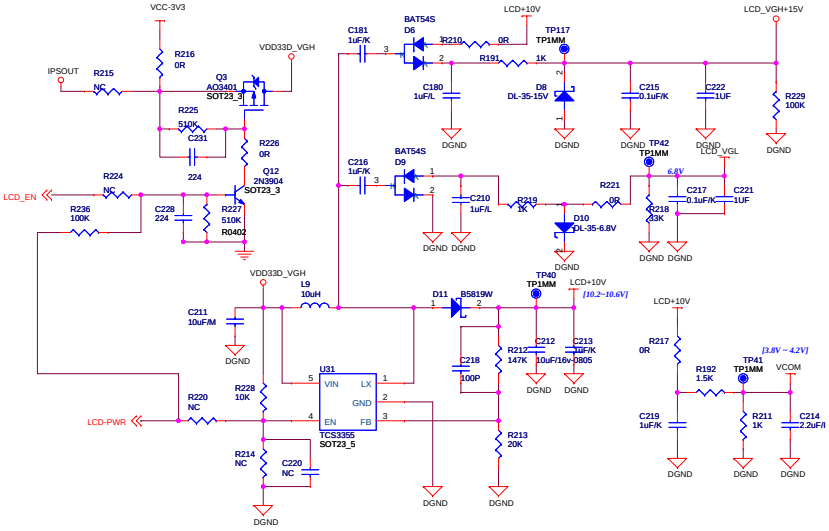


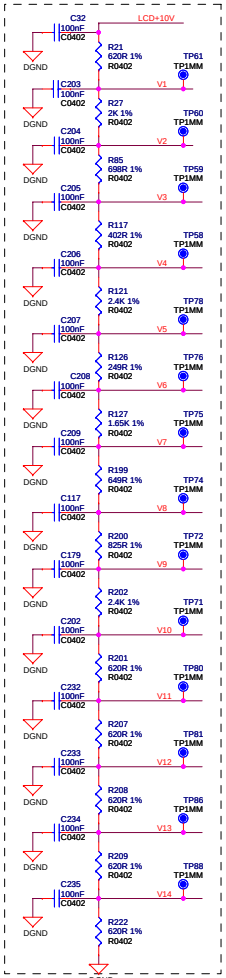
8''高清

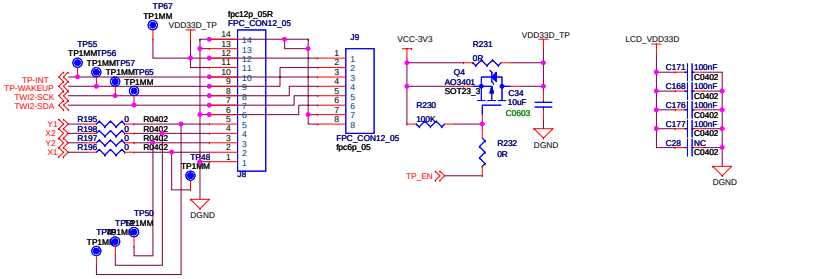
16:9

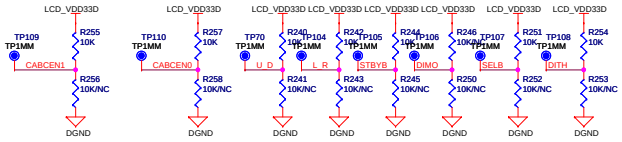












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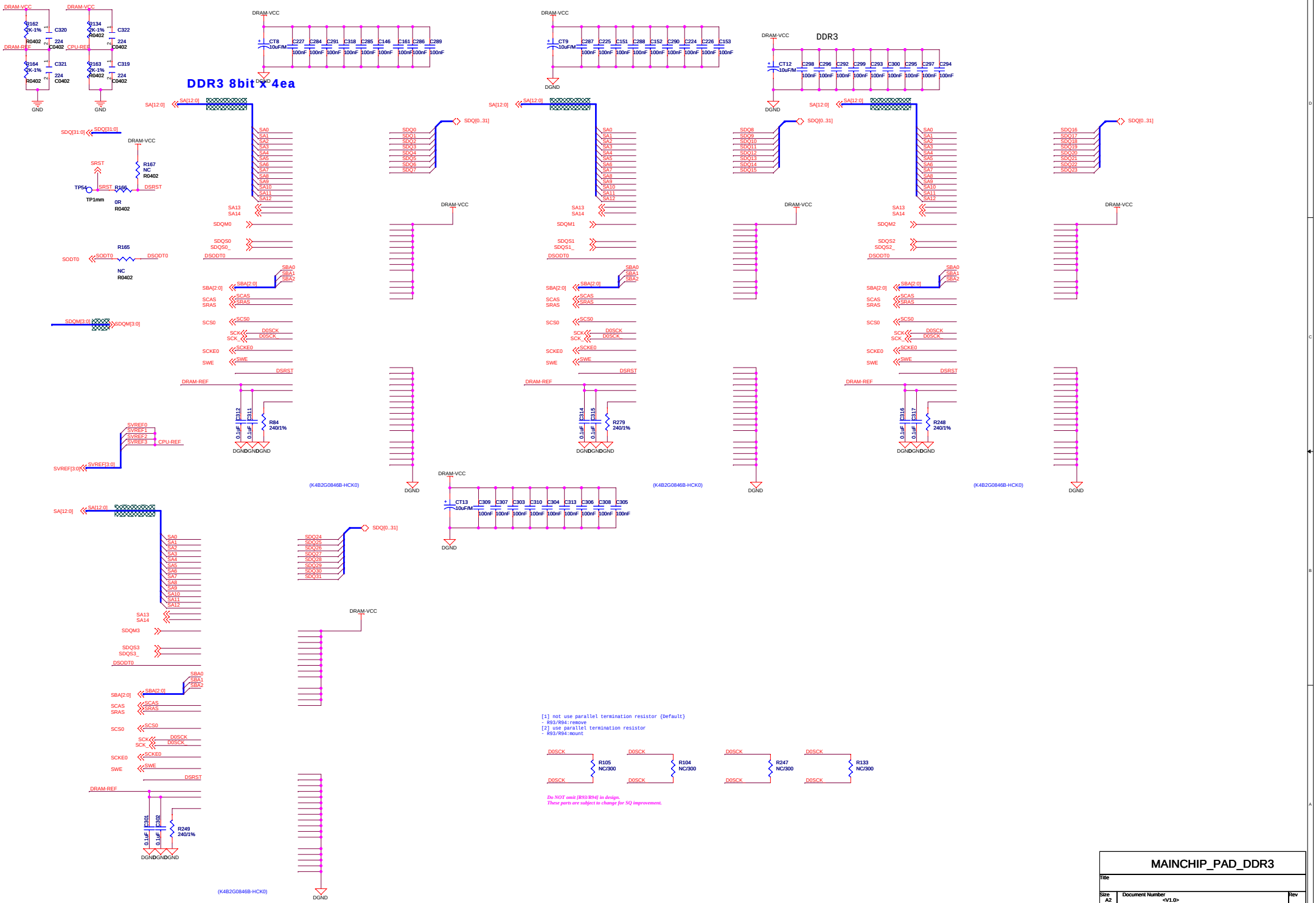
DDR3



Title	MAINCHIP_PAD_DDR3		
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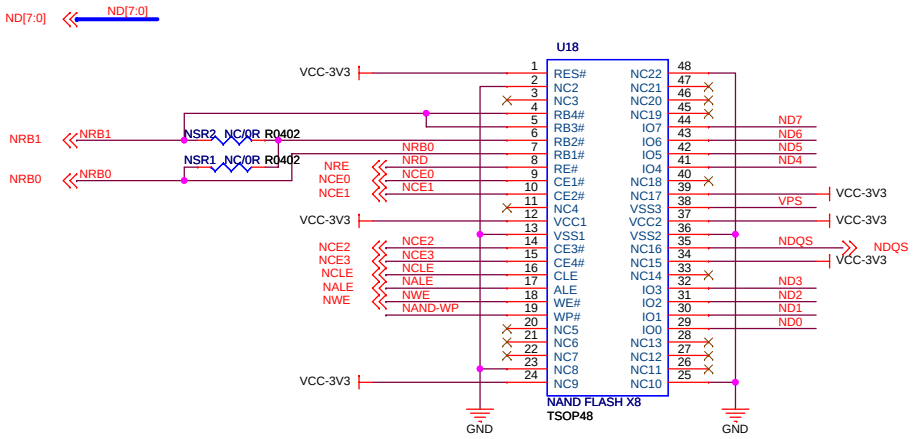
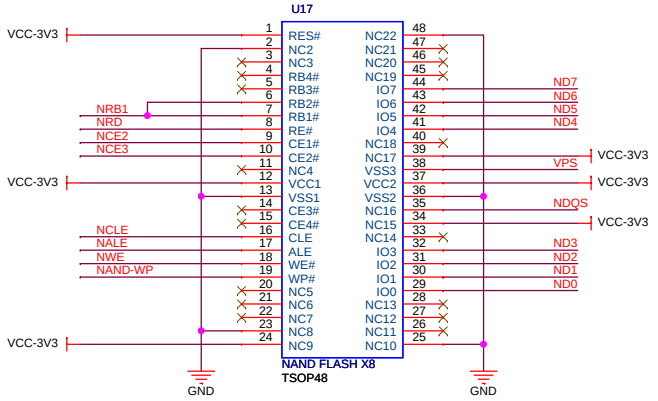
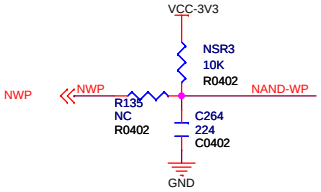
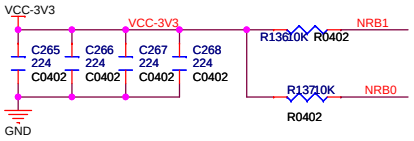
NAND Flash

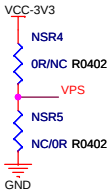
TSOP-48 Nand











pack ok

(1) 接1片单片选Nand 时，NSR2、NSR1断开
(2) 接1片双片选Nand 时，连接NSR2，断开NSR1
(3) 接1片四片选Nand 时，连接NSR1，断开NSR2
(4) 接2片单片选或接2片双片选Nand时，连接NSR1，断开NSR2
(5) 接Intel、Toshiba、Samsung 2xnm TSOP Nand时， NSR4连接，NSR5断开；其它的NSR4断开，NSR5连接

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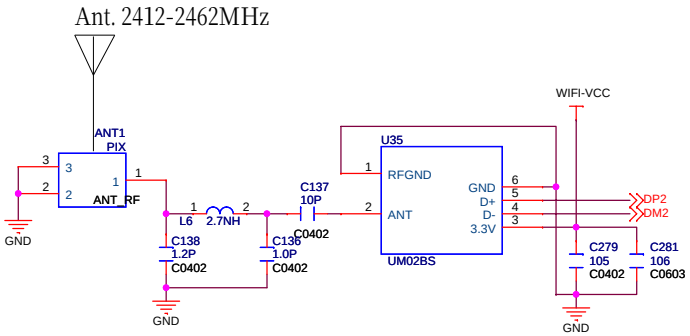
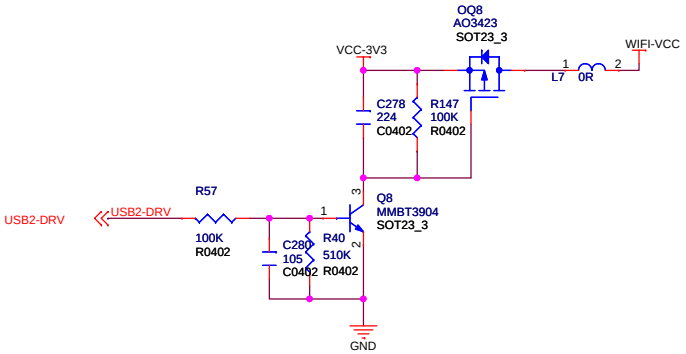
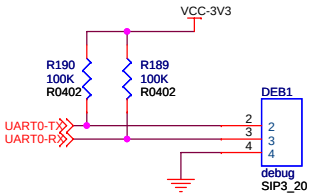
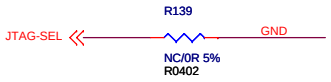
WIFI-GSENSOR

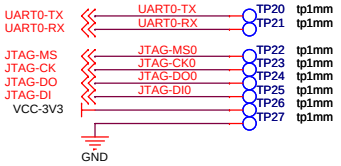




DEBUG

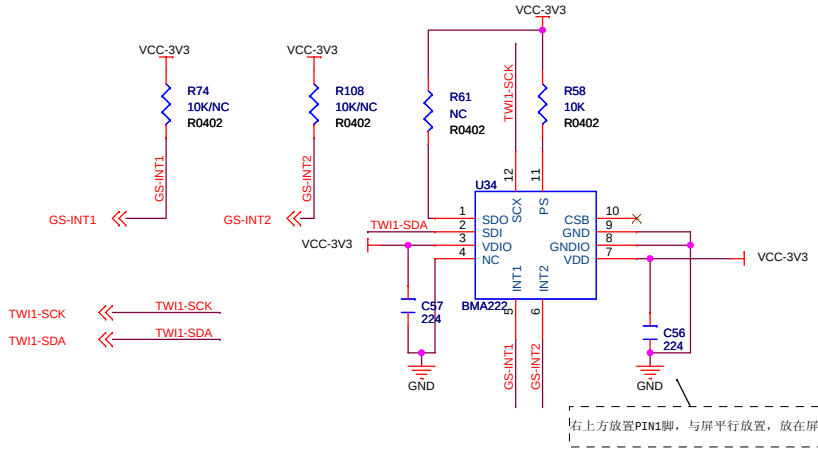






预留DEBUG测试点，以备调试使用

G-SENSOR



MAINCHIP_PAD_DDR3		
Title: WIFI-GSENSOR		
Size: A3	Document Number: <V1.0>	Rev:
Date: Monday, February 27, 2012	Sheet 16 of 16	