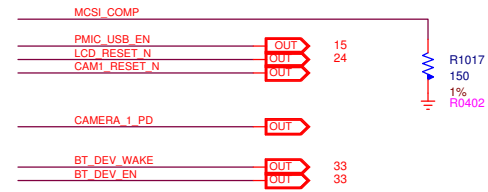
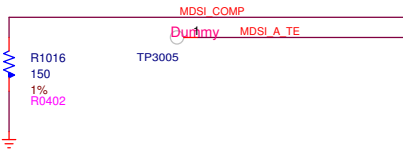
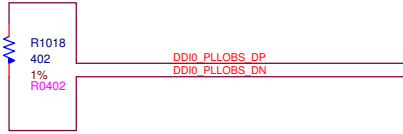
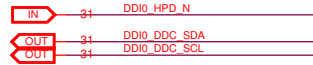
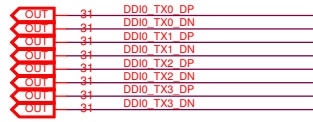
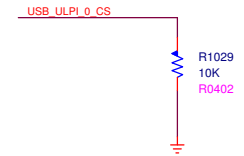
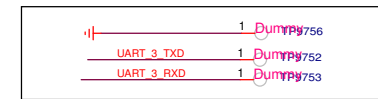
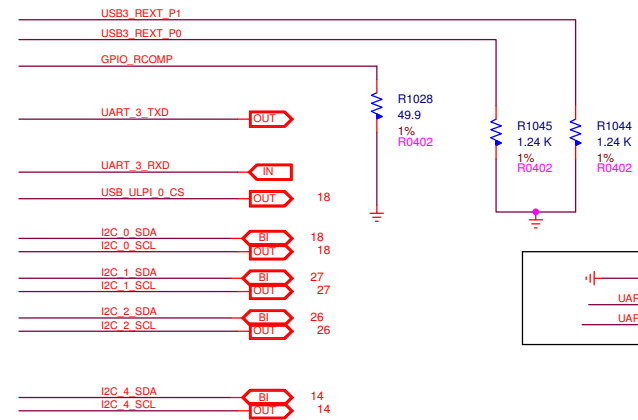
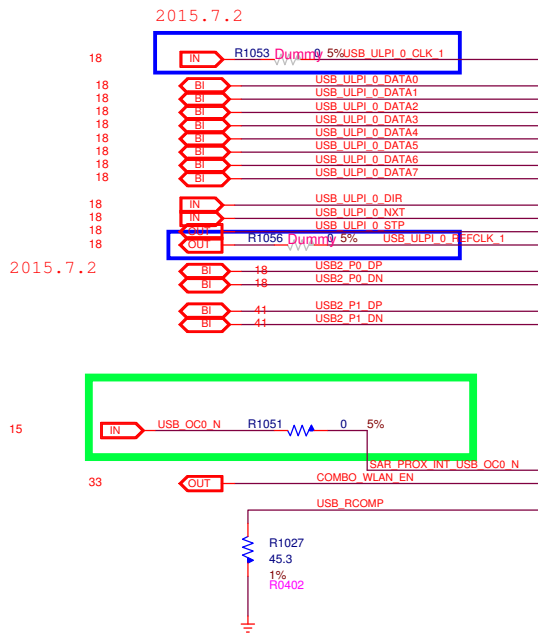


SOC:DISPLAY&CAMERA



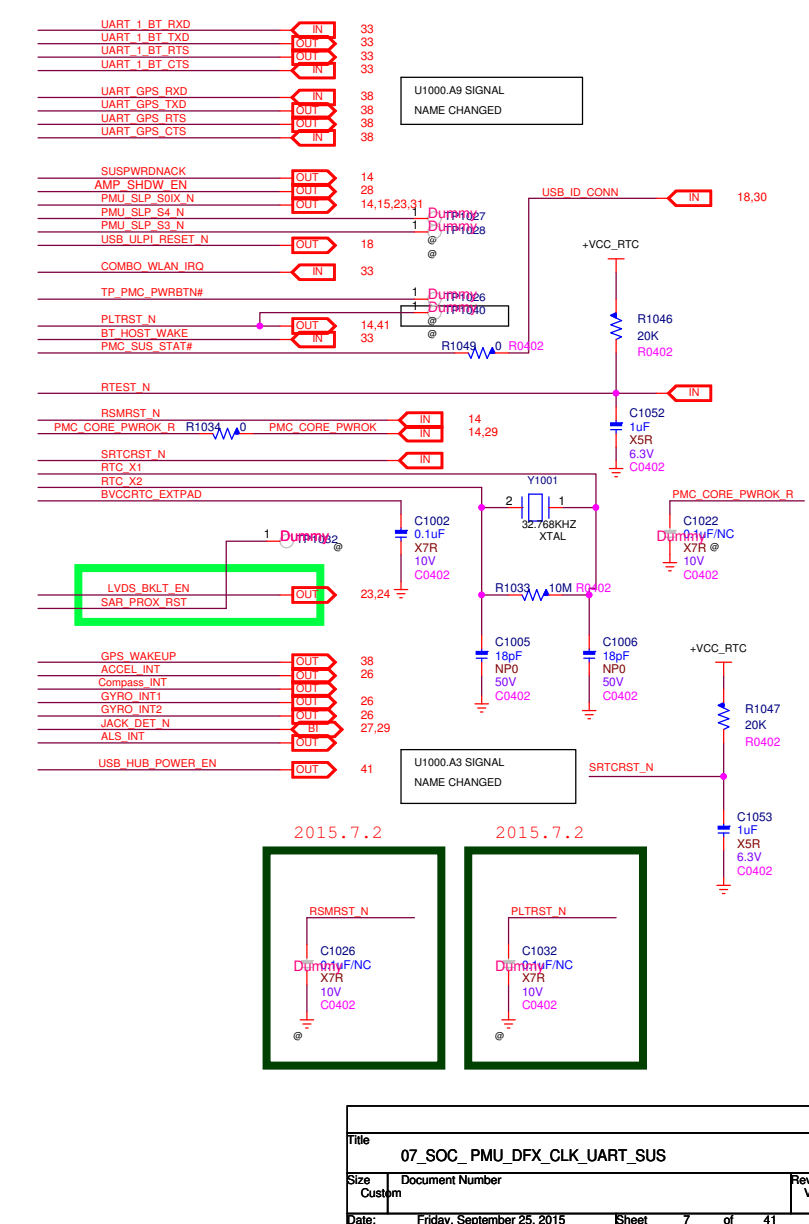
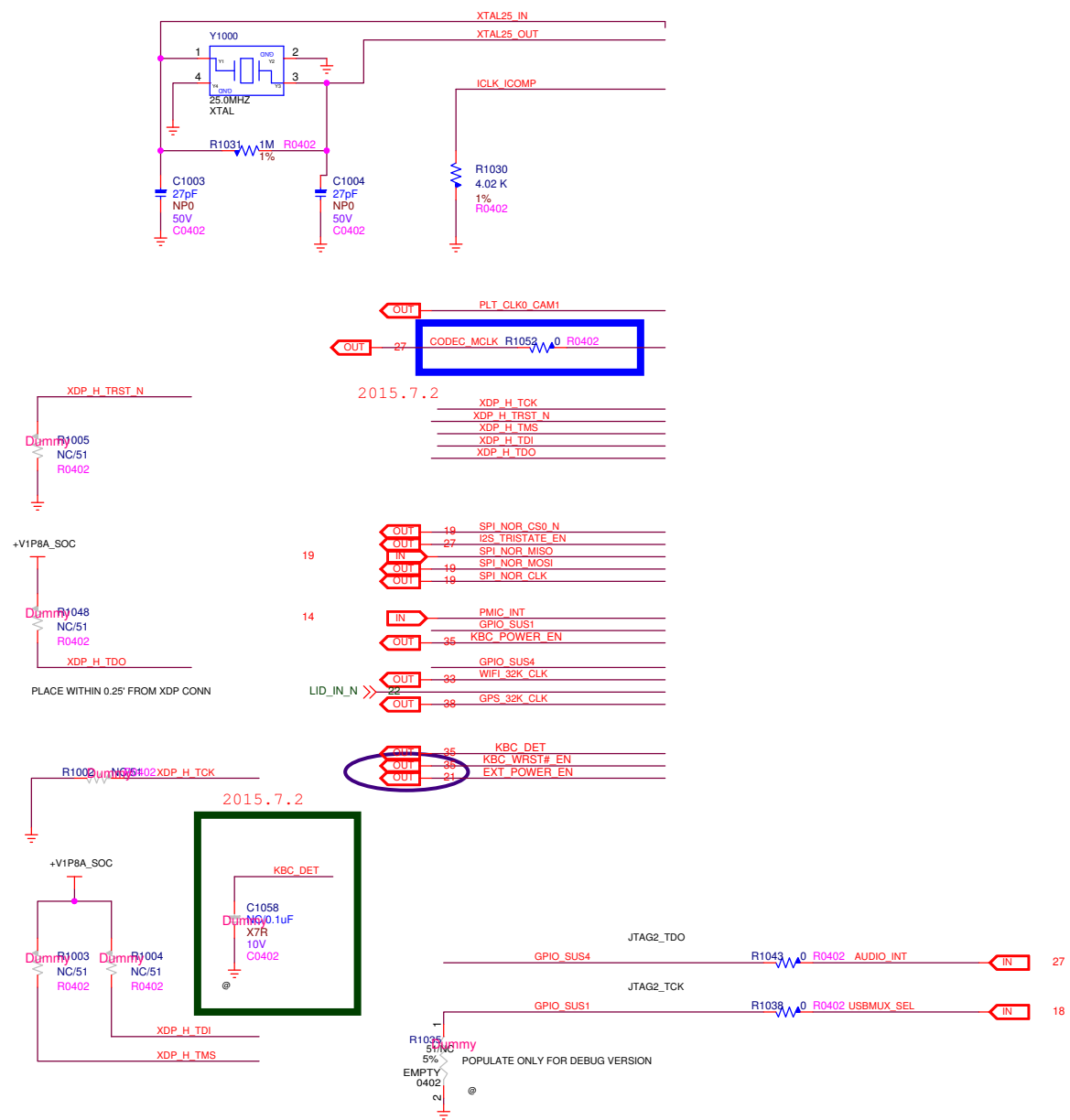
Title		
04_SOC_DISPLAY & CAMERA		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 4 of 41





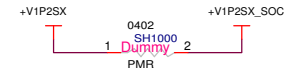
Title		
06_SOC_USB & I2C		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 6 of 41

SOC:PMU/DFX/CLOCK/UART/SUS



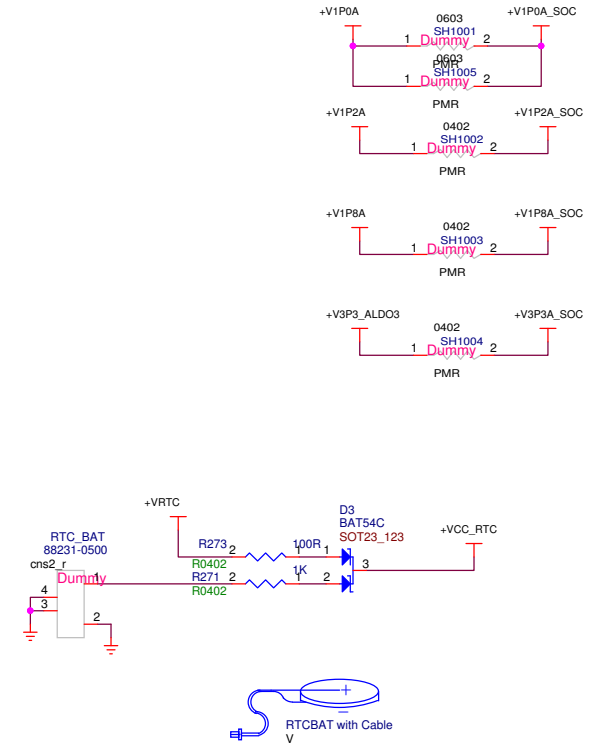
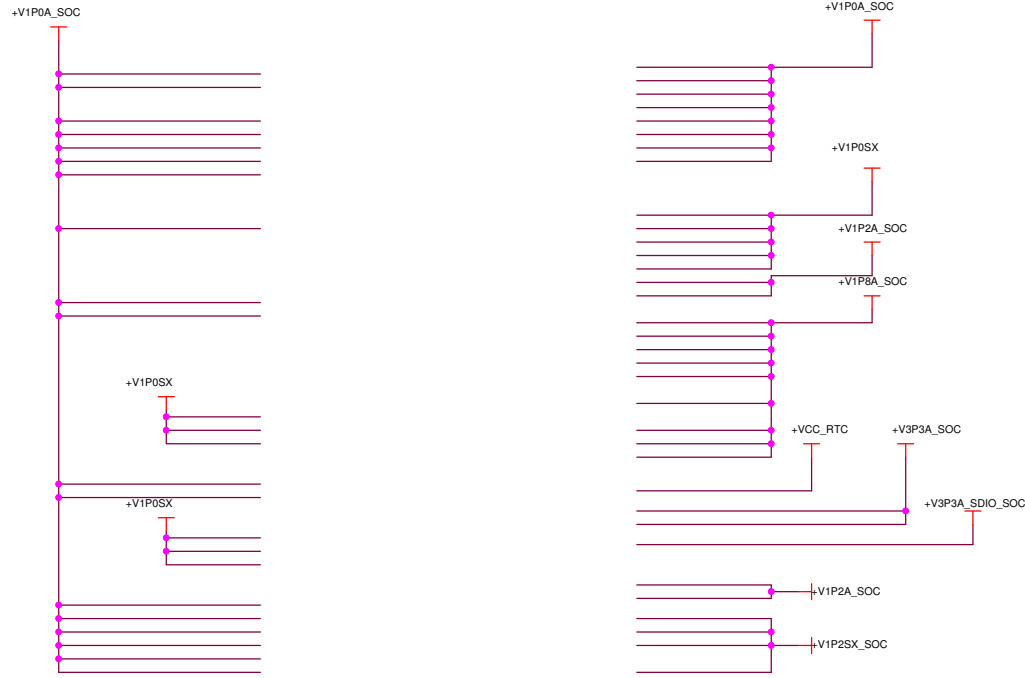
Title		
07_SOC_PMU_DFX_CLK_UART_SUS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 7 of 41

SOC:POWER 1



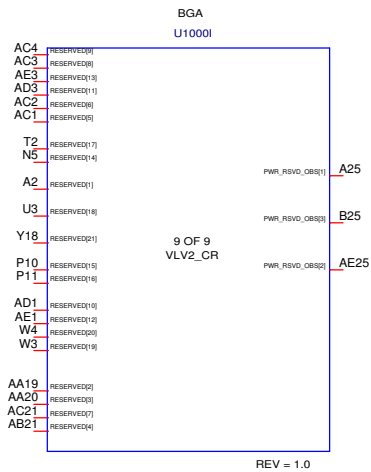
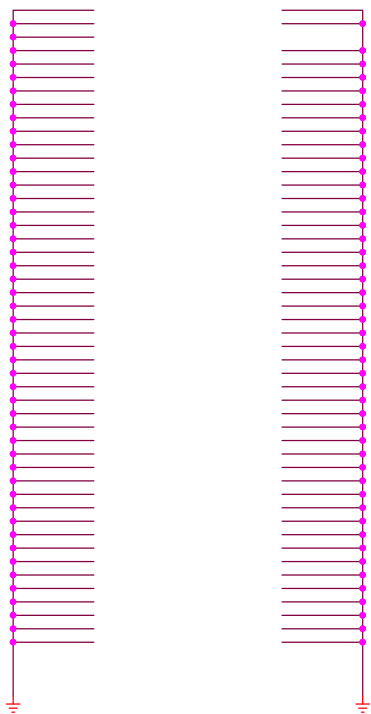
Title		
08_SOC_POWER 1		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 8 of 41

# SOC:POWER 2



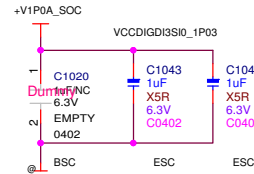
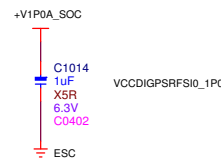
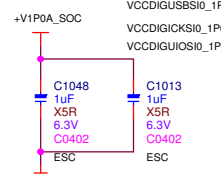
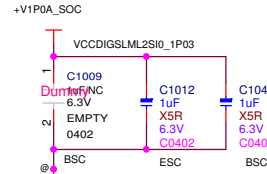
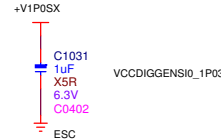
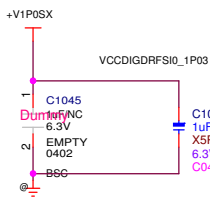
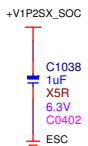
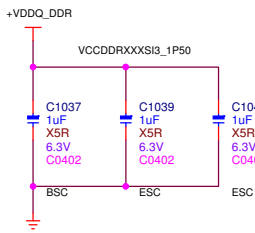
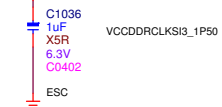
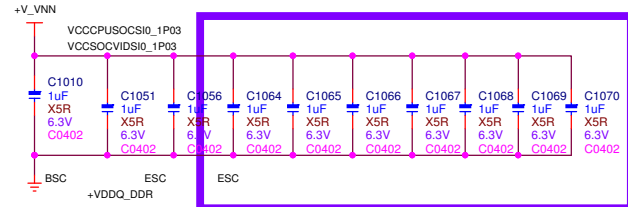
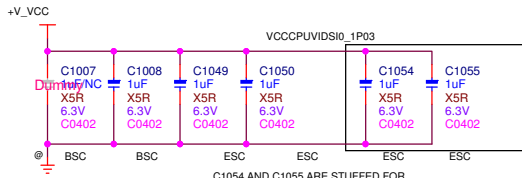
Title		
09_SOC_POWER 2		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 9 of 41

SOC:GND

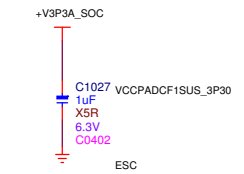
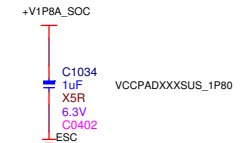
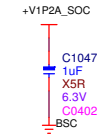
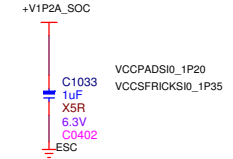
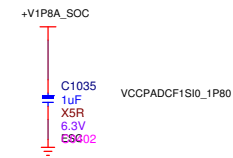
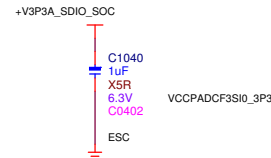
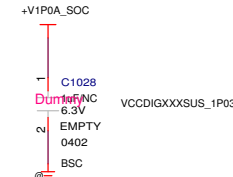
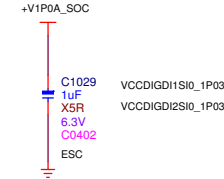
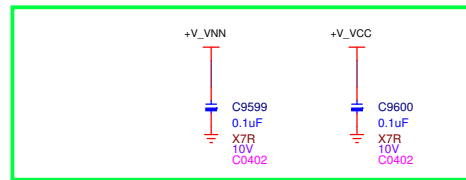


Title			
10_SOC_GND			
Size	Document Number	Rev	
Custom		V1.0	
Date:	Friday, September 25, 2015	Sheet	10 of 41

SOC:DECOUPLING



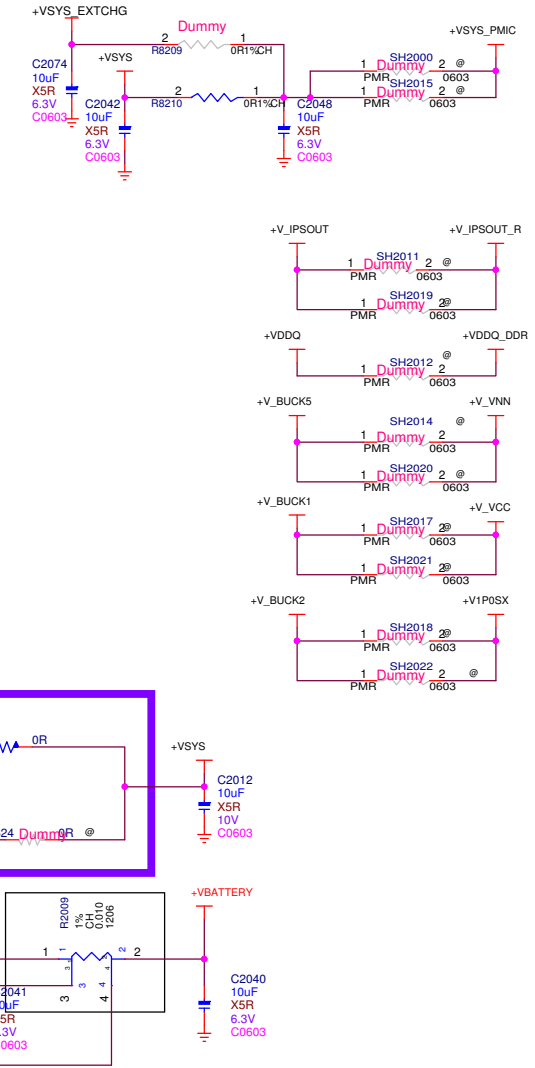
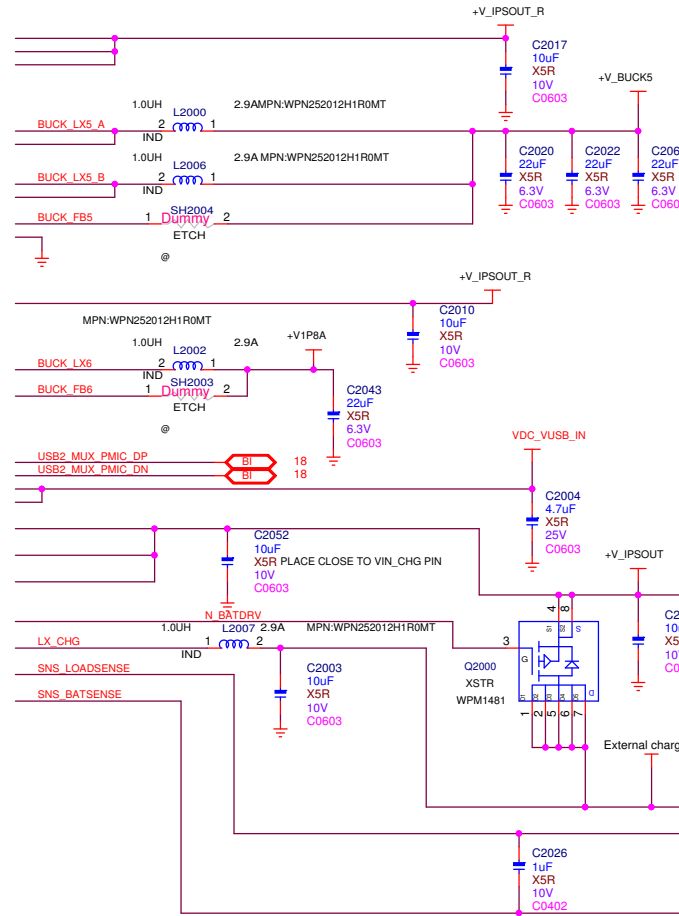
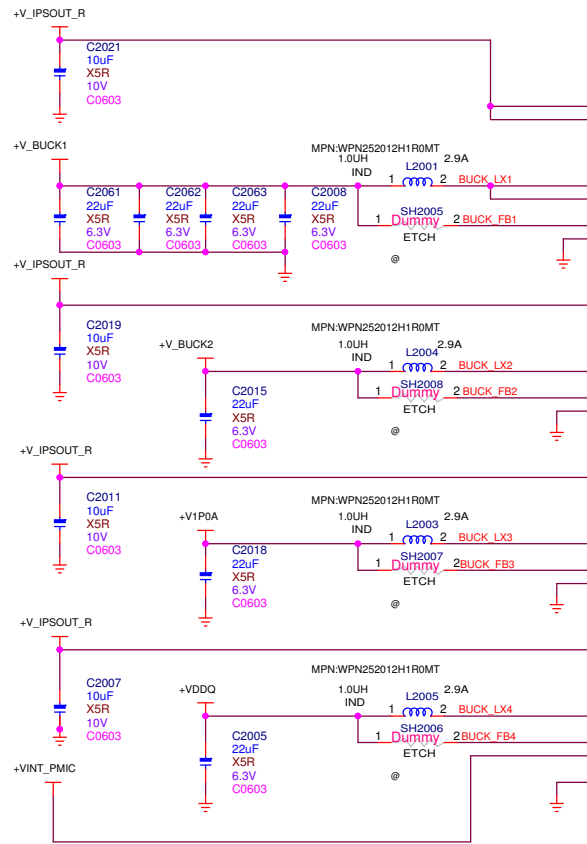
For EMI and SI



Title		11_SOC_DECOUPLING	
Size	Custom	Document Number	Rev V1.0
Date:	Friday, September 25, 2015	Sheet	11 of 41



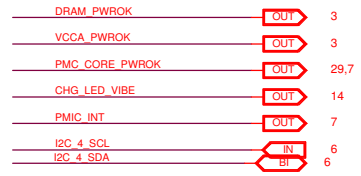
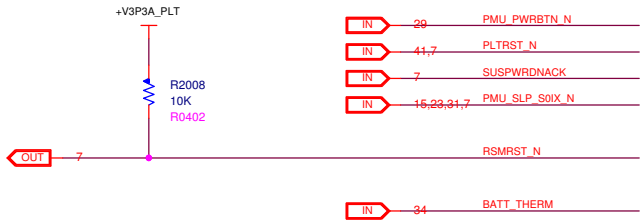
# PMIC: SWITCHING REGULATORS



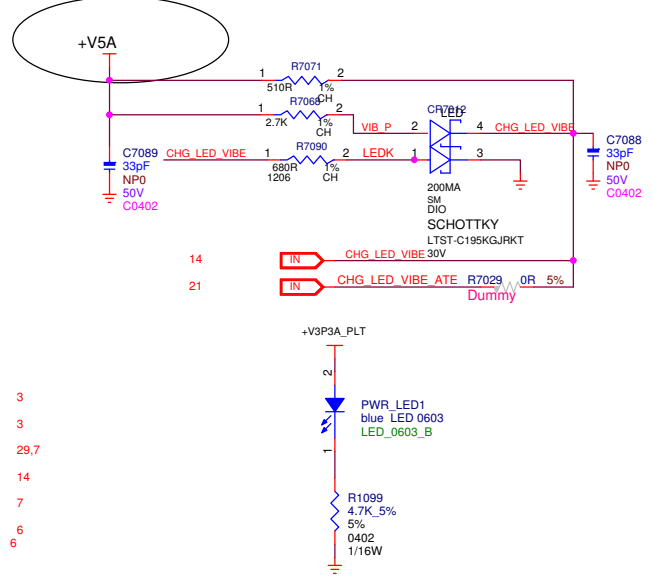
Title		
12_PMIC_SWITCHING REGULATORS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 12 of 41



PMIC: HOST & MISC INTERFACES

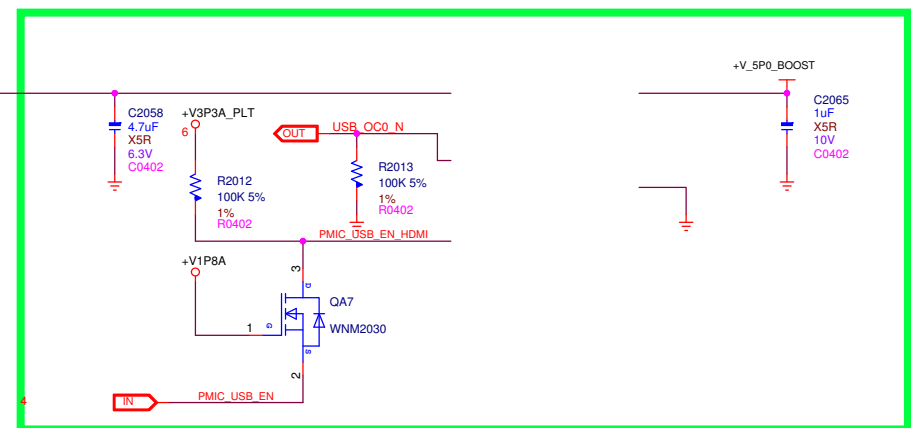
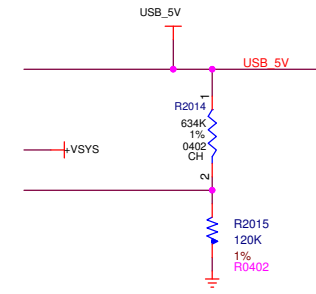
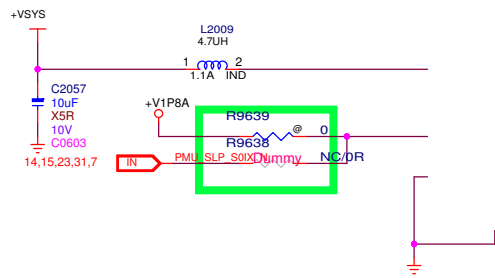
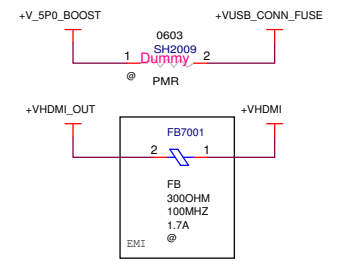
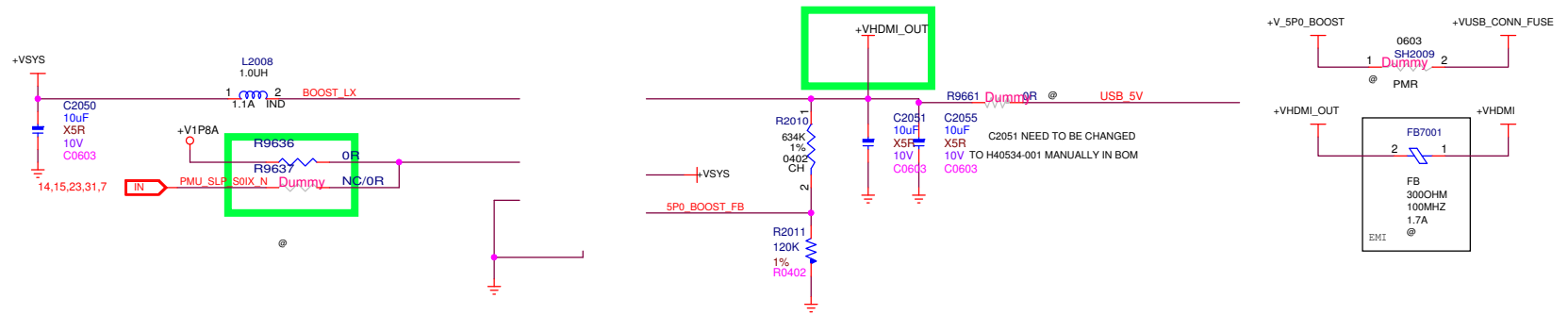


change from +vsys to +v5a 2015.08.18



Title		14_PMIC_HOST & MISC INTERFACES	
Size	Document Number	Rev V1.0	
Date:	Friday, September 25, 2015	Sheet	14 of 41

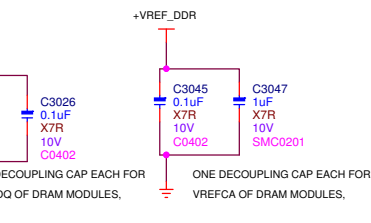
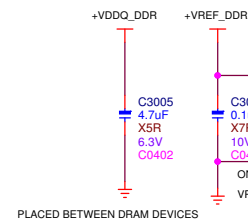
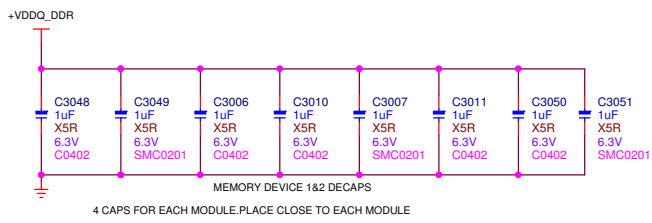
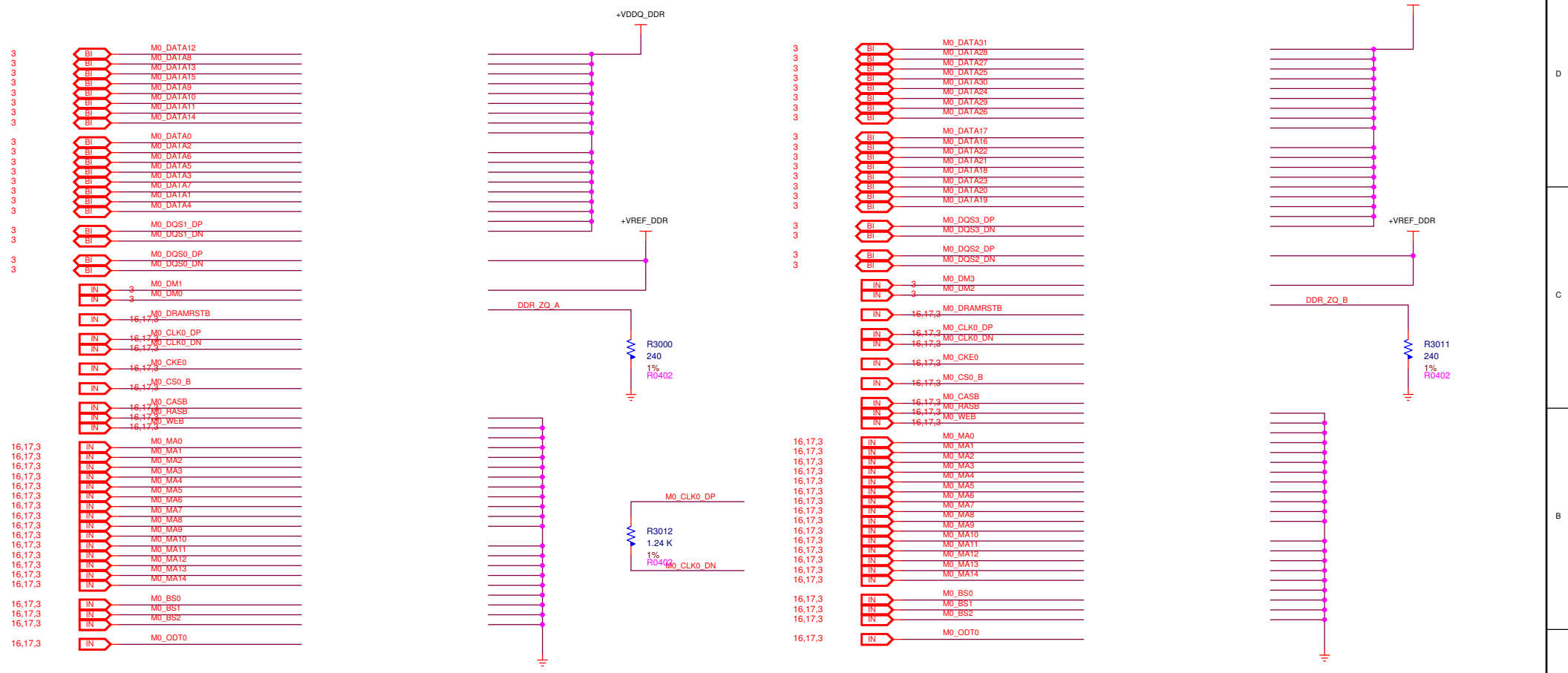
PMIC: EXTERNAL REGULATORS



Title		
15_PMIC_EXTERNAL REGULATORS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 15 of 41

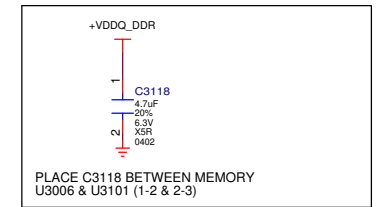
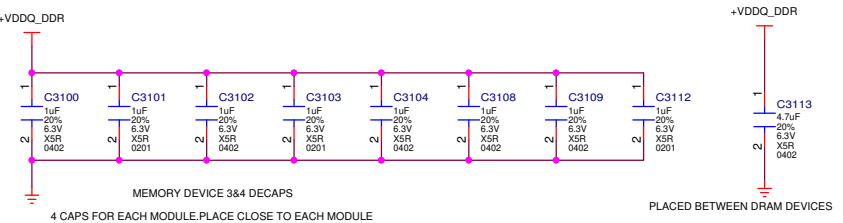
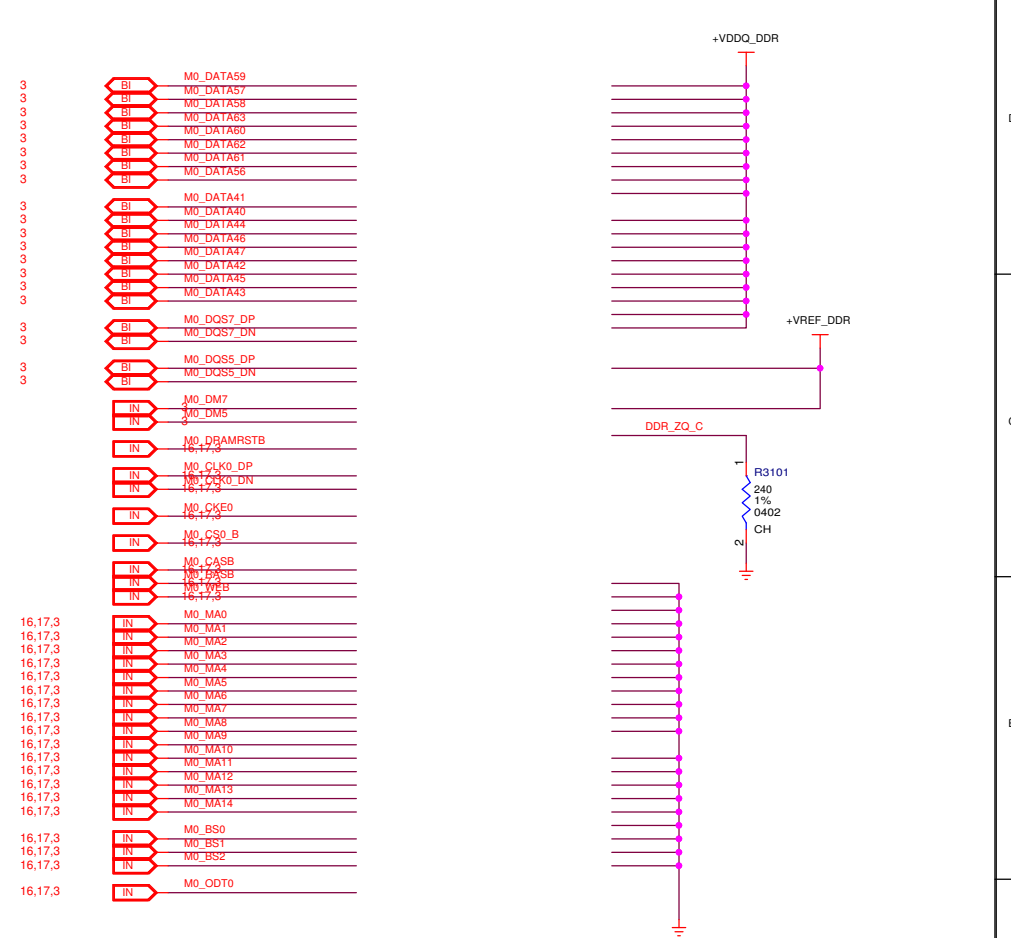
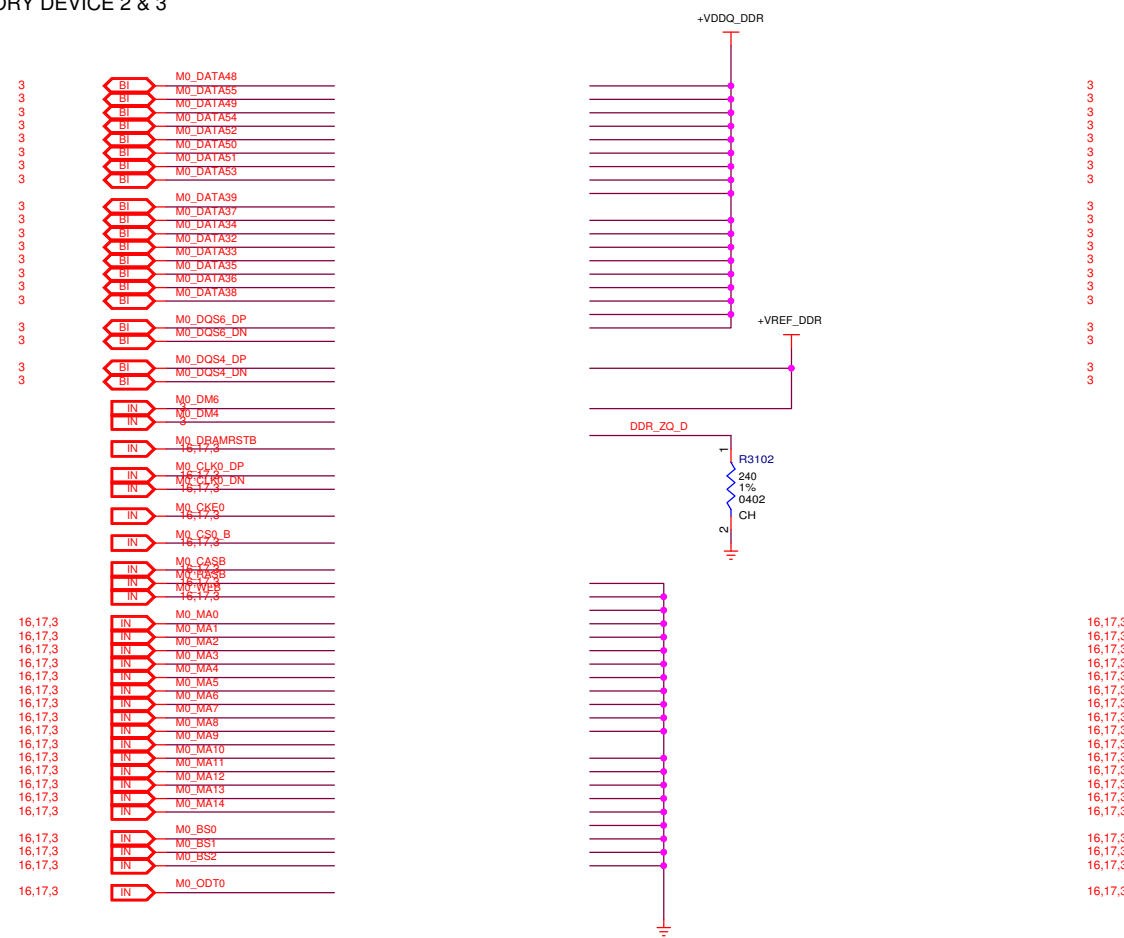
MEMORY DEVICE 0 & 1

PART USED IN BOM - HYNIX #H5TC4G63AFR-PBA(IPN IS G96268-001)



Title		
16_MEMORY DEVICE 0 & 1		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 16 of 41

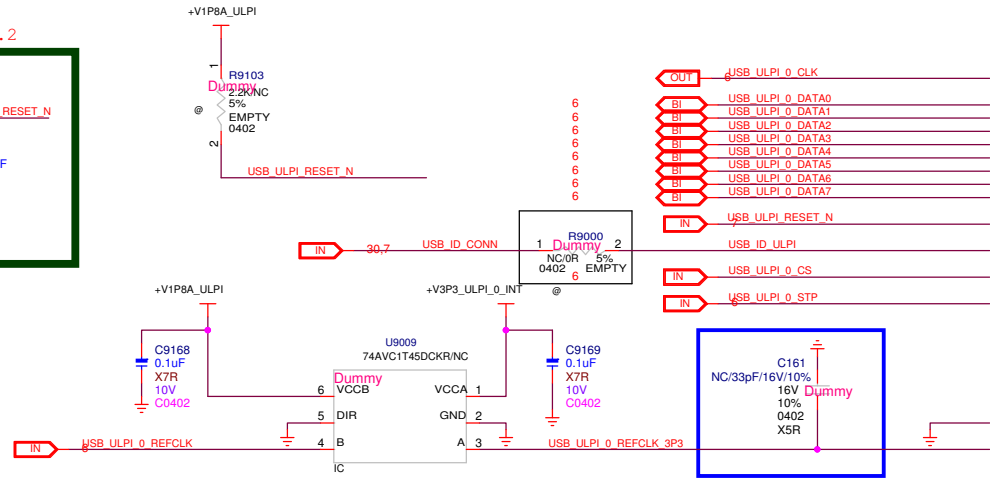
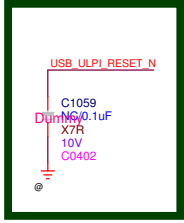
D  
C  
B  
A



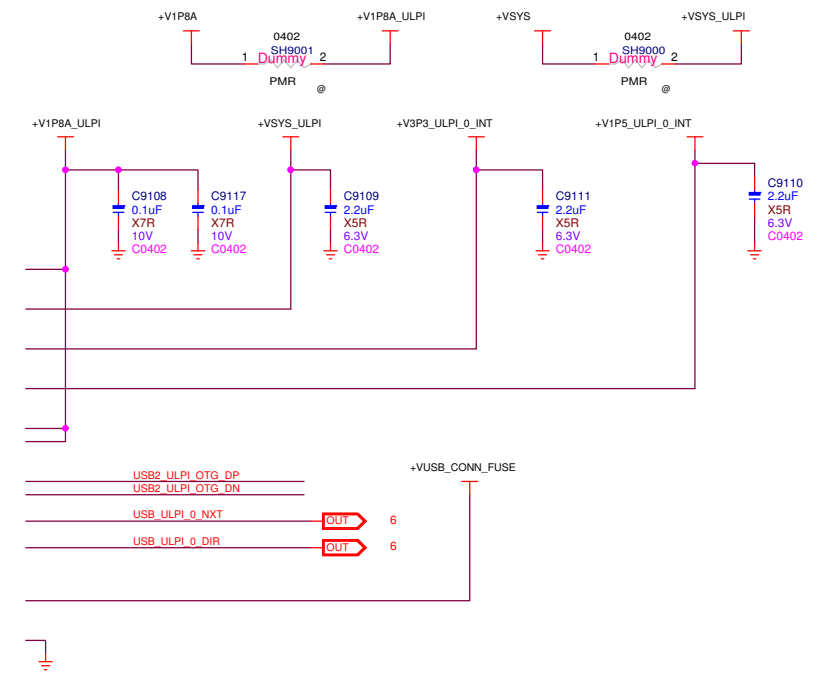
Title		
17_BLANK PAGE		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 17 of 41

# USB PHY & MUX

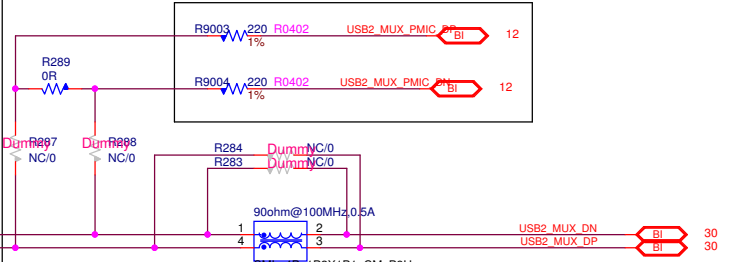
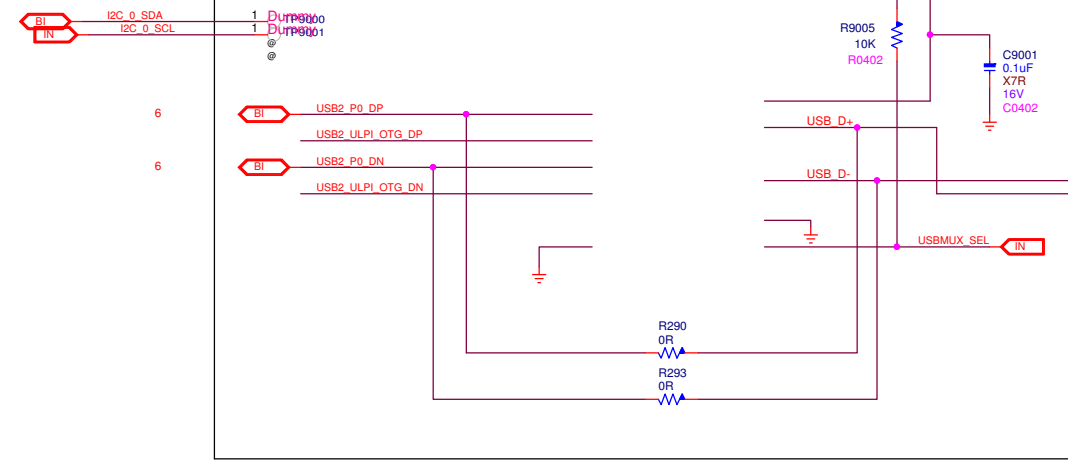
2015.7.2



2015.7.2

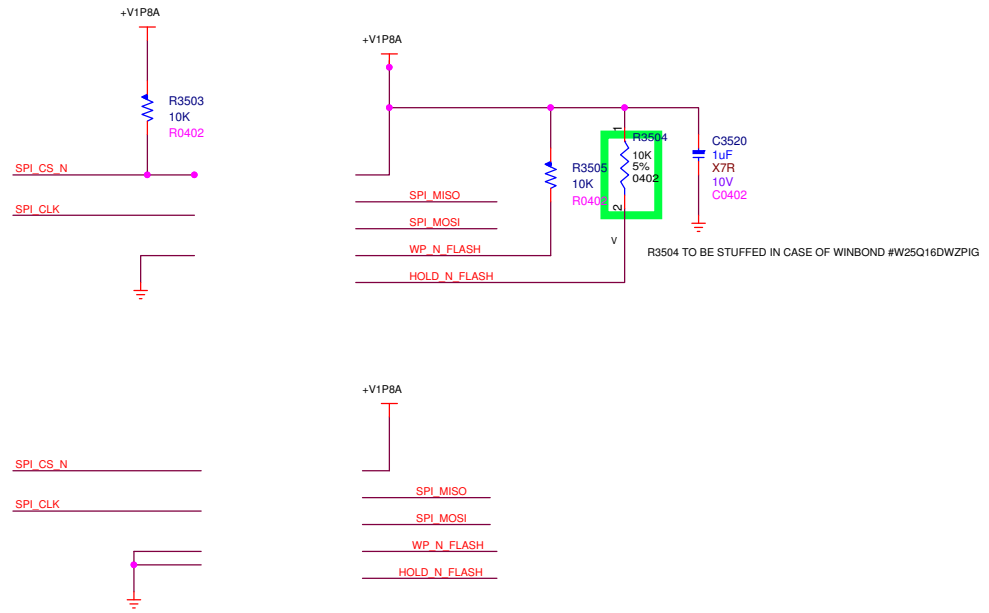


6  
6



Title		18_USB PHY & MUX
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 18 of 41

# SPI NOR FLASH



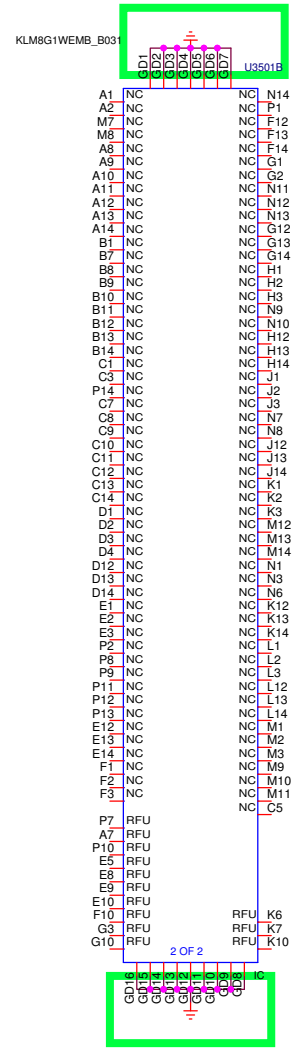
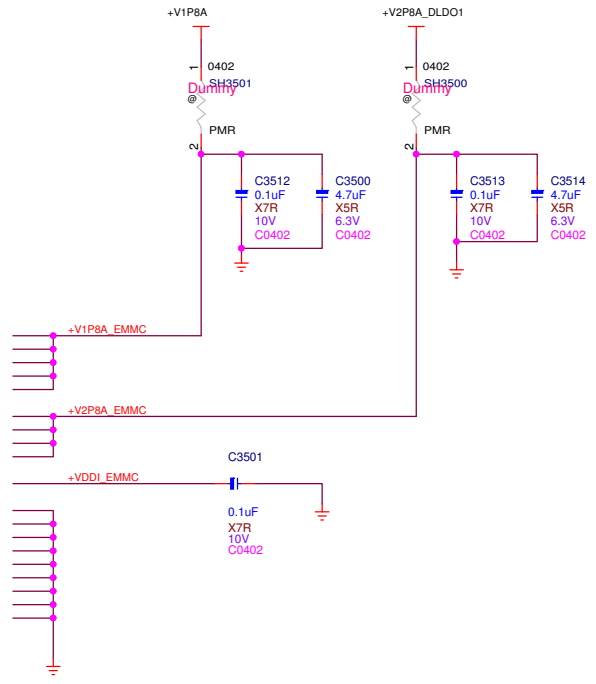
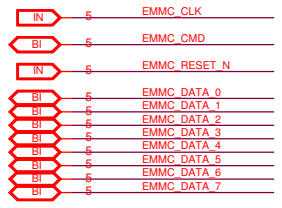
DEDIPROG MUST HAVE THE VOLTAGE SETTING FIXED AT 1.8V



Title		19_SPI NOR FLASH	
Size	Document Number	Rev V1.0	
Custom			
Date:	Friday, September 25, 2015	Sheet	19 of 41

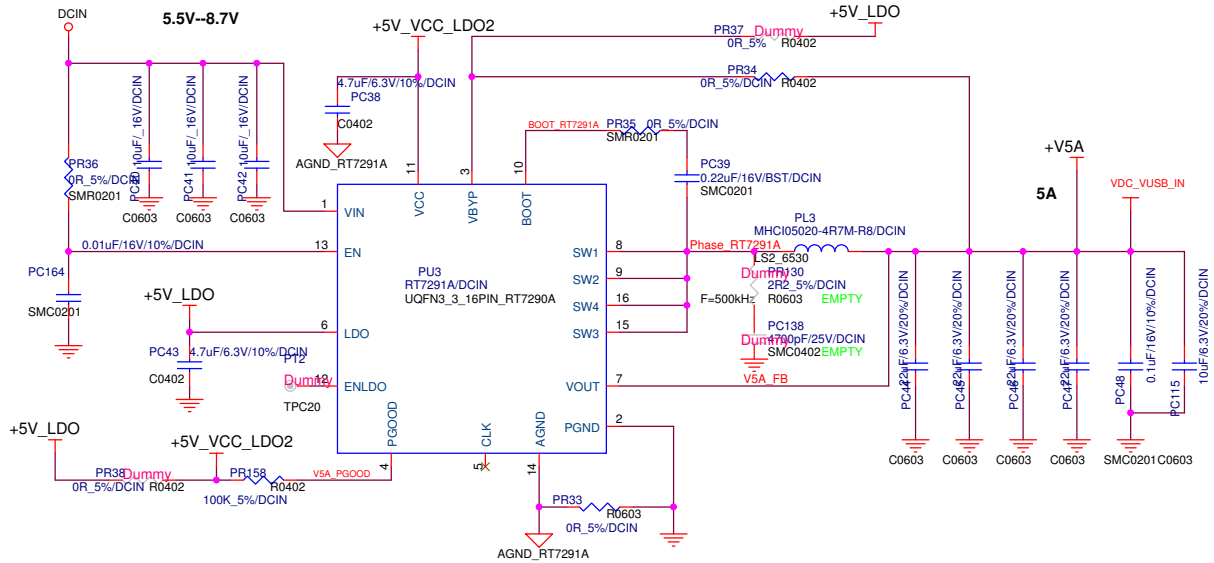
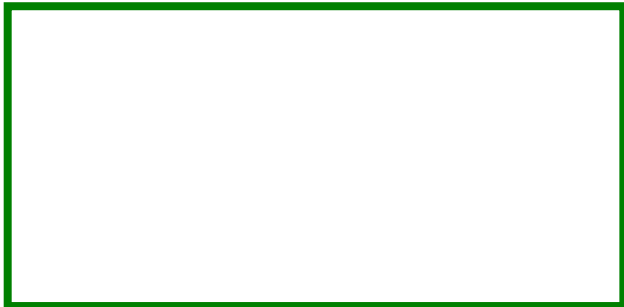
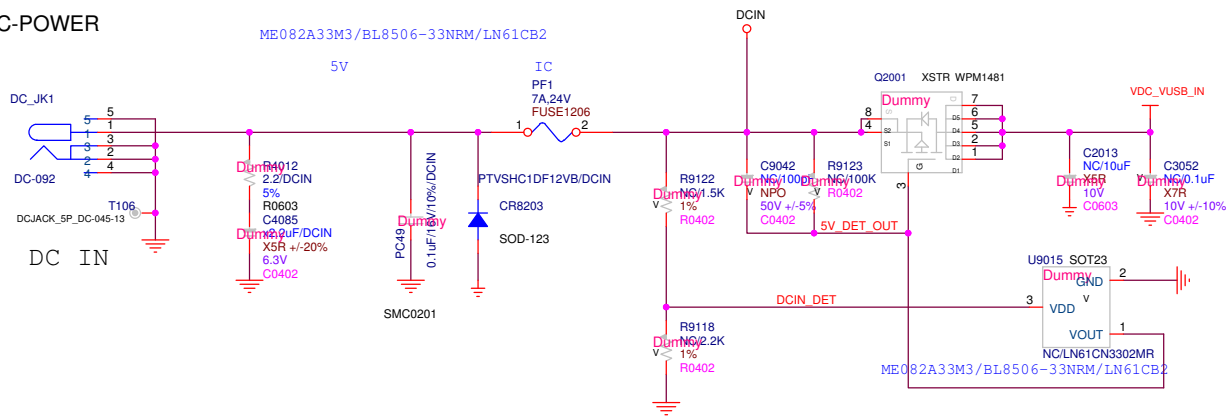


# EMMC

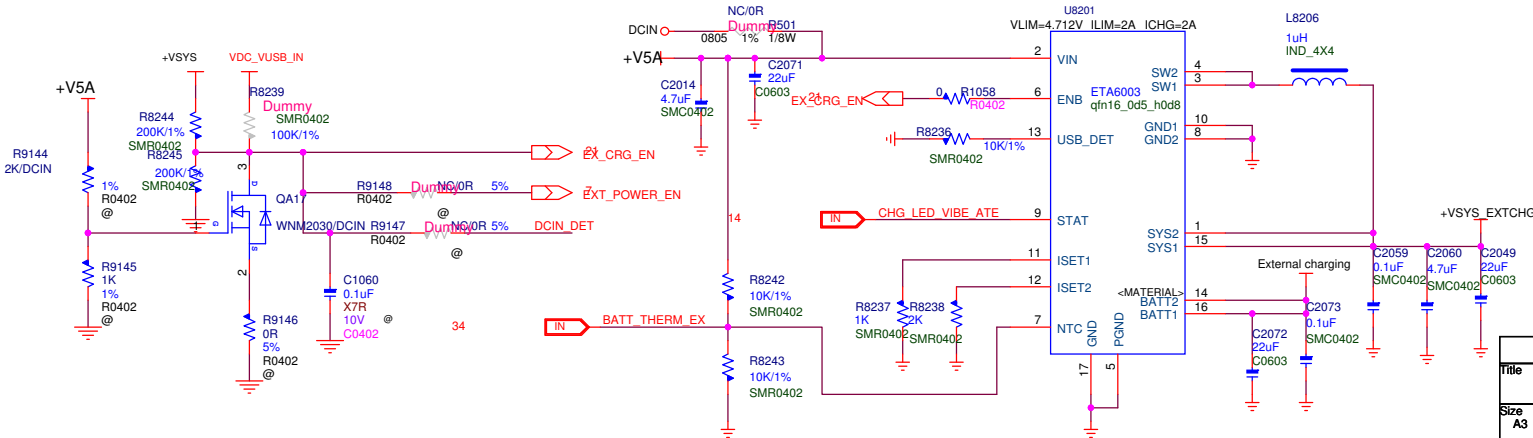


Title		
20_EMMC		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 20 of 41

DC-POWER

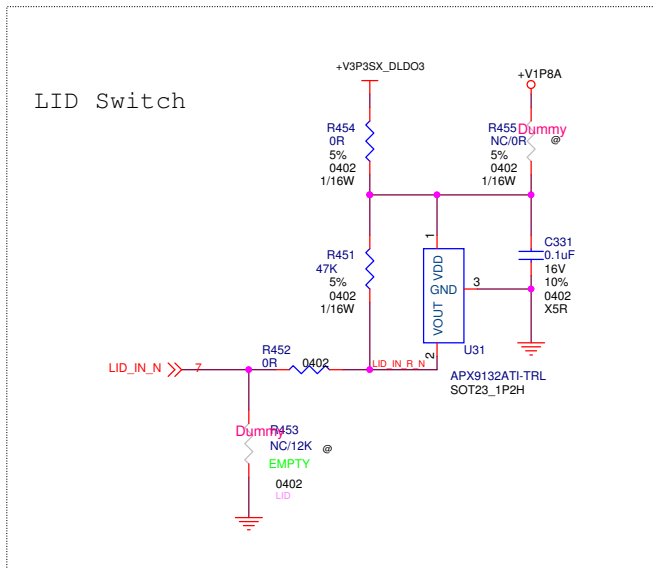


EXTERNAL CHARGER



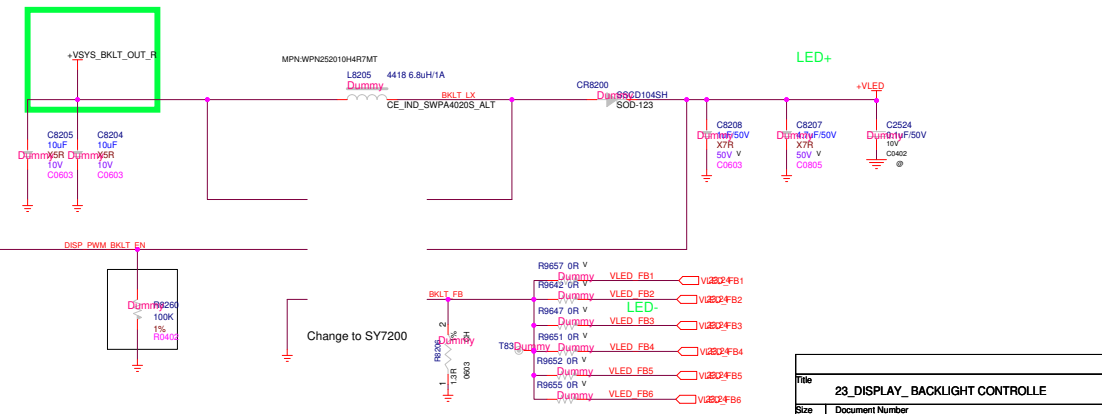
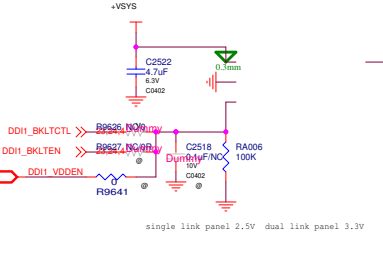
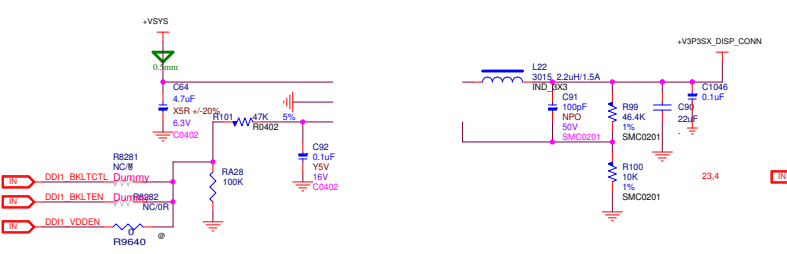
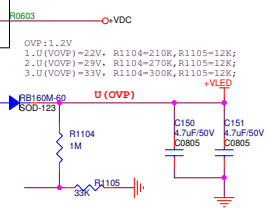
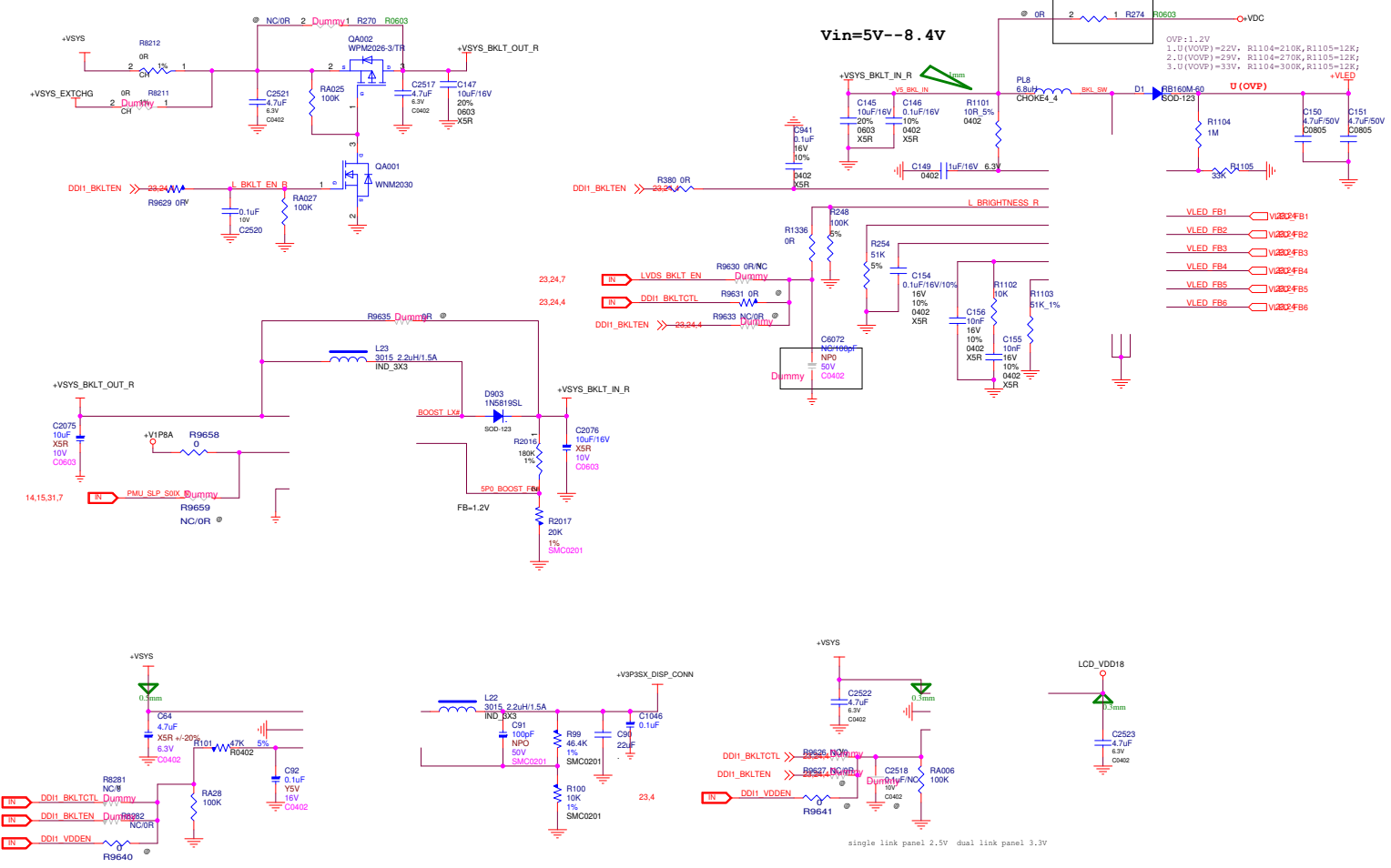
Title		
21_DISPLAY_LVDS BRIDGE		
Size	Document Number	Rev
A3		V1.0
Date:	Monday, August 24, 2015	Sheet 21 of 41

DISPLAY: BIAS VOLTAGE REGULATORS



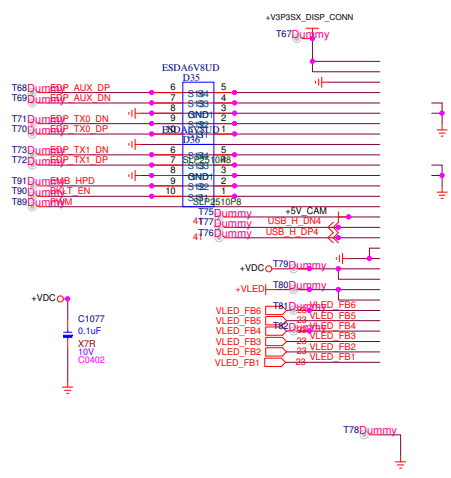
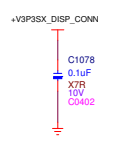
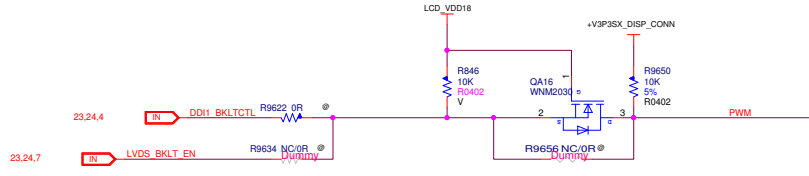
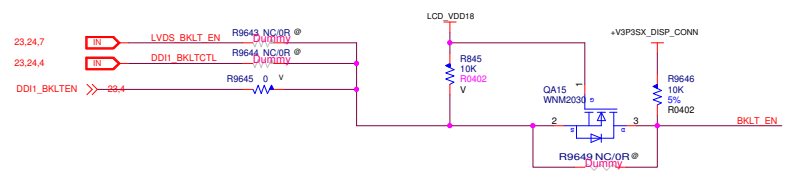
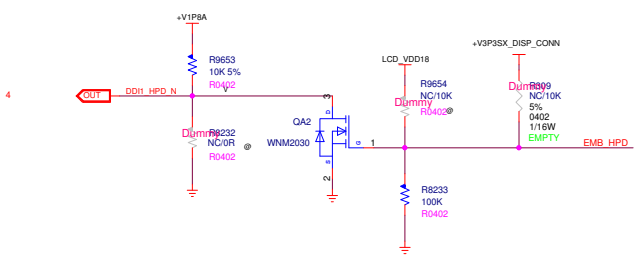
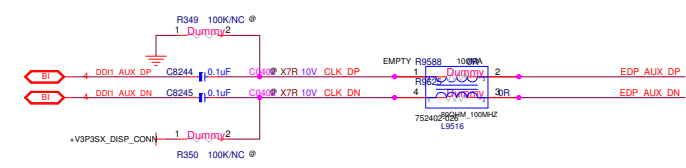
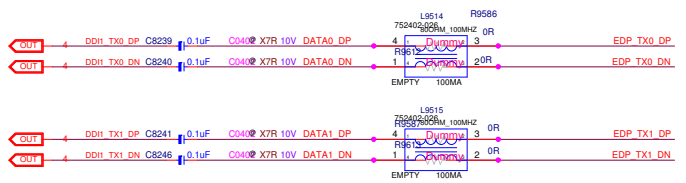
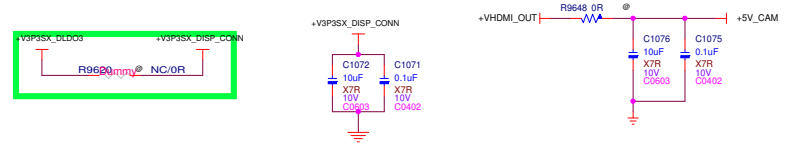
Title		
22_DISPLAY_BIAS VOLTAGE REGULA		
Size	Document Number	Rev
Custom		V1.0
Date:	Monday, August 24, 2015	Sheet 22 of 41

DISPLAY: BACKLIGHT CONTROLLER



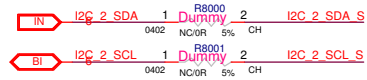
- VLED\_FB1 <input type="checkbox"/> V12B2FB1
- VLED\_FB2 <input type="checkbox"/> V12B2FB2
- VLED\_FB3 <input type="checkbox"/> V12B2FB3
- VLED\_FB4 <input type="checkbox"/> V12B2FB4
- VLED\_FB5 <input type="checkbox"/> V12B2FB5
- VLED\_FB6 <input type="checkbox"/> V12B2FB6

File: 23_DISPLAY_BACKLIGHT CONTROLLER		
Size: Custom	Document Number:	Rev: V1.0
Date: Friday, September 25, 2015	Sheet: 23	of 41

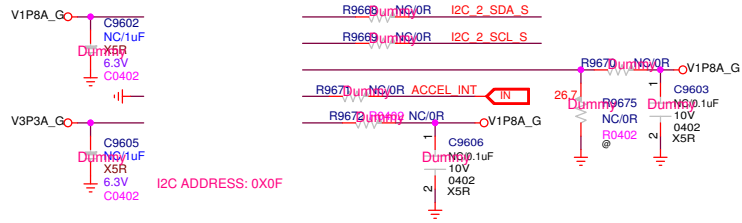


Title		
24_DISPLAY & TOUCH CONNECTORS		
Doc C	Document Number	Rev V1.0
Date:	Friday, September 25, 2015	Sheet 24 of 41

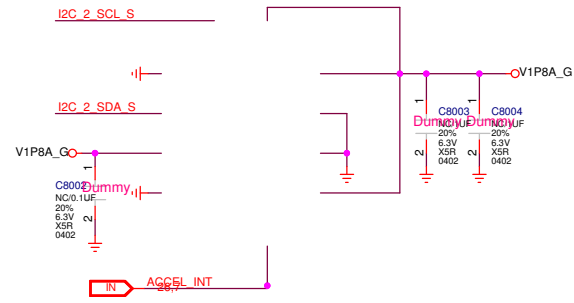
# SENSORS



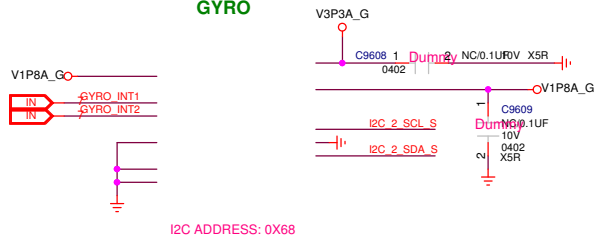
## ACCELEROMETER



## Accel Compass



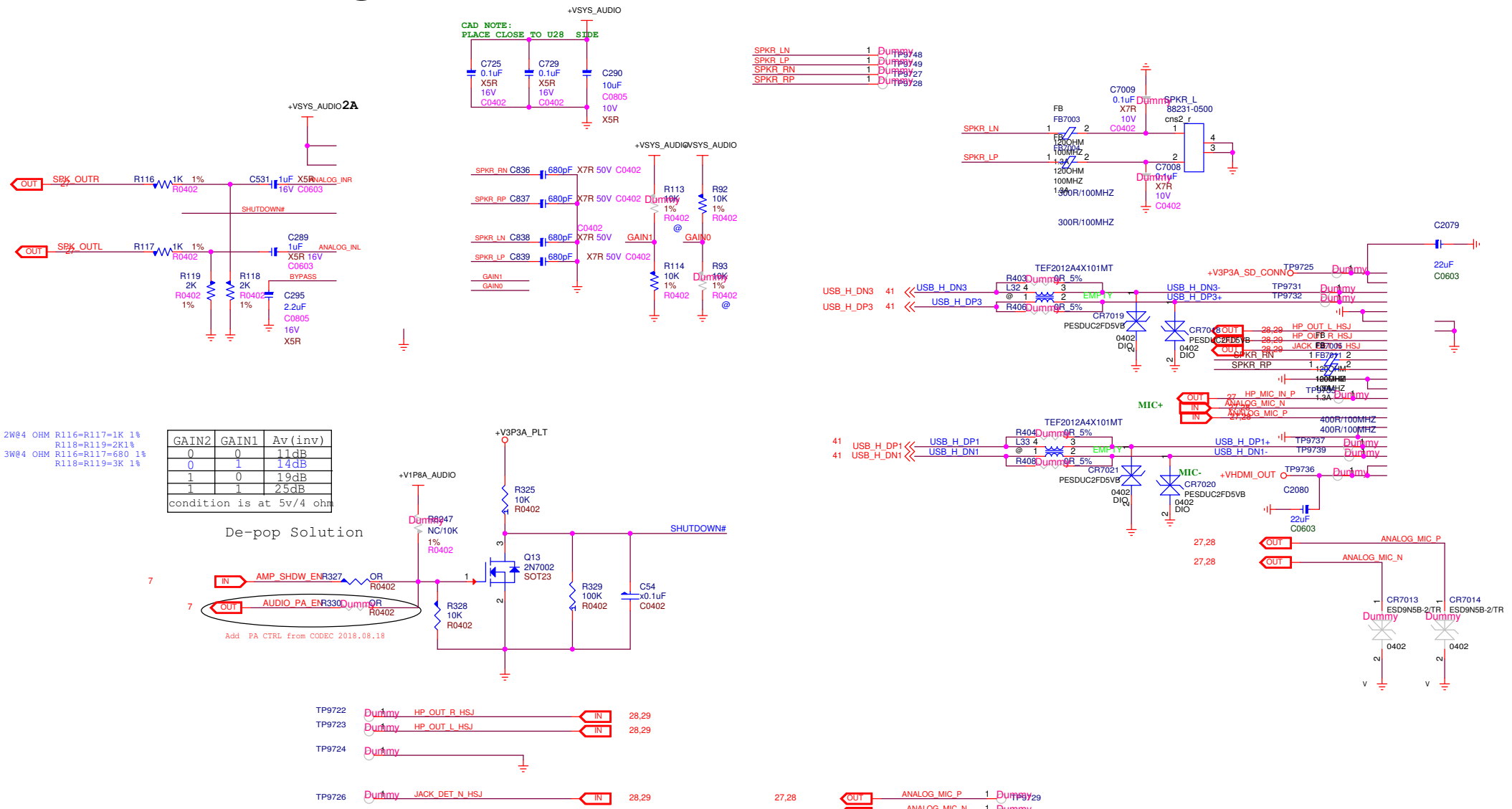
## GYRO



Title		
26_SENSORS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 26 of 40



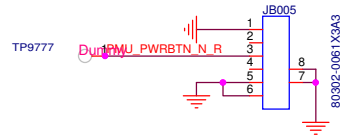
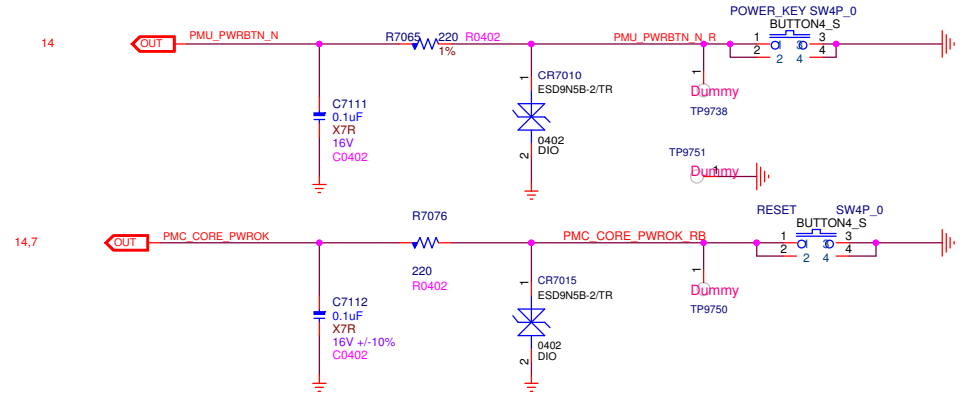
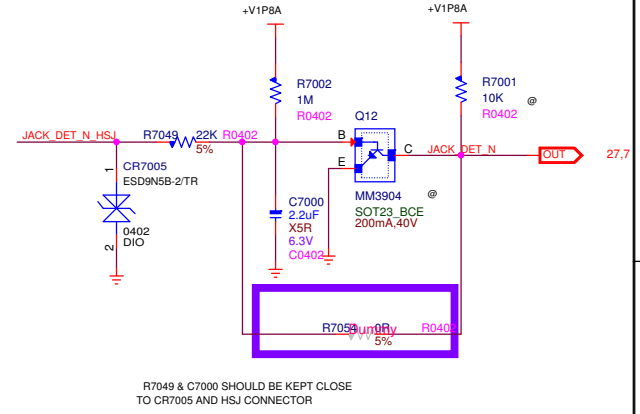
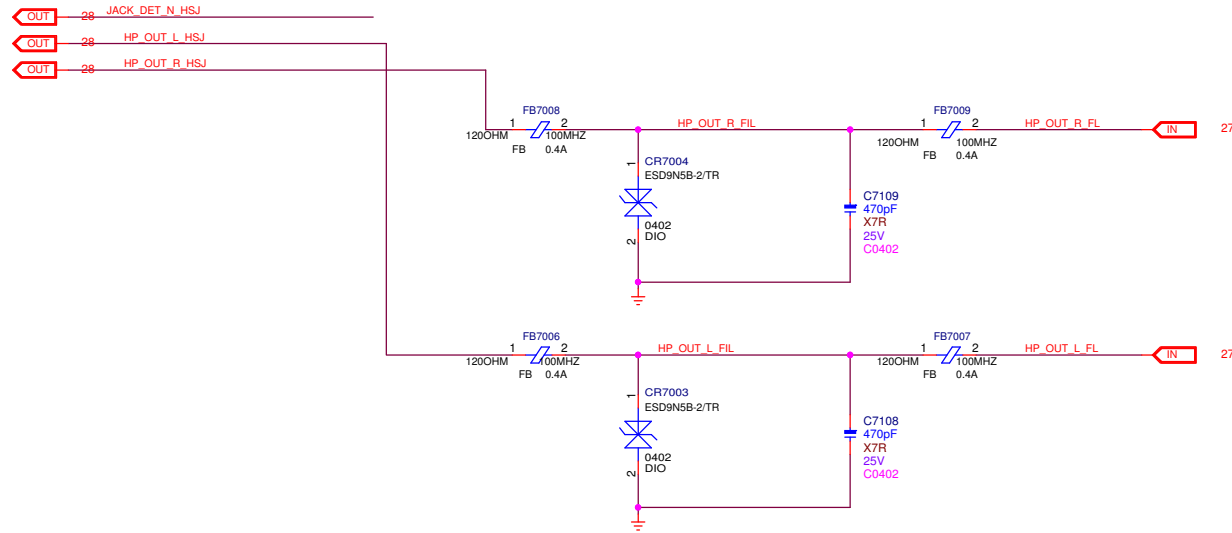
# SPEAKER



Title		
28_SPEAKERS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 28 of 41

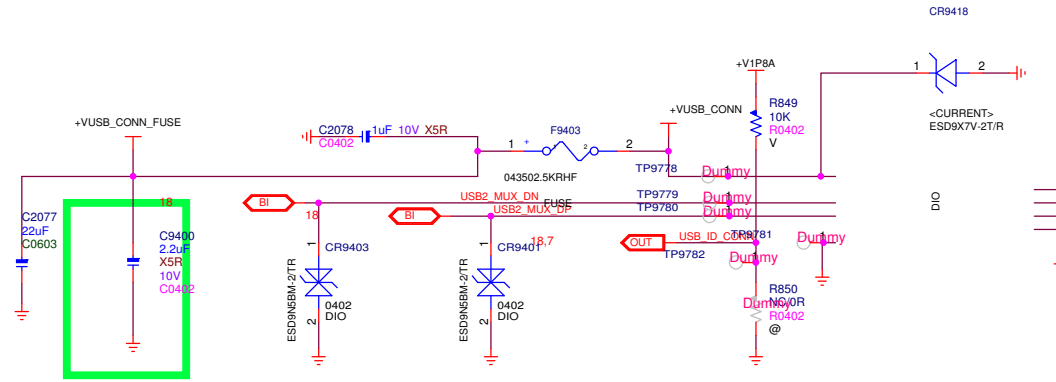


# HEADSET JACK AND BUTTONS



Title		
29_HEADSET JACK AND BUTTONS		
Size	Document Number	Rev
Custom		V1.0
Date:	Monday, August 24, 2015	Sheet 29 of 41

# USB CONNECTOR

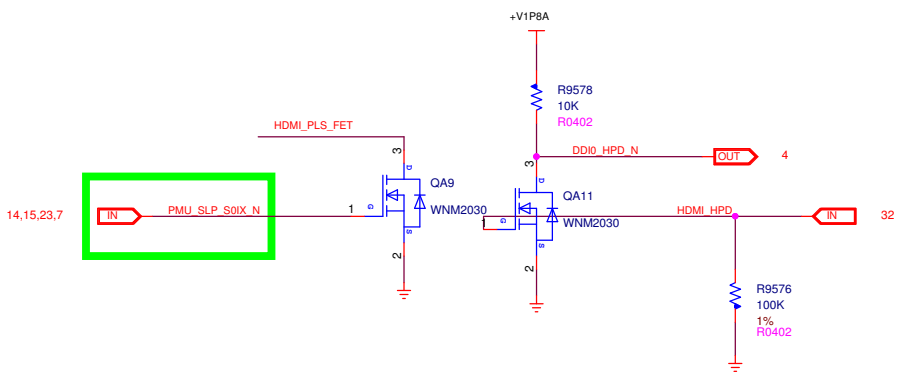
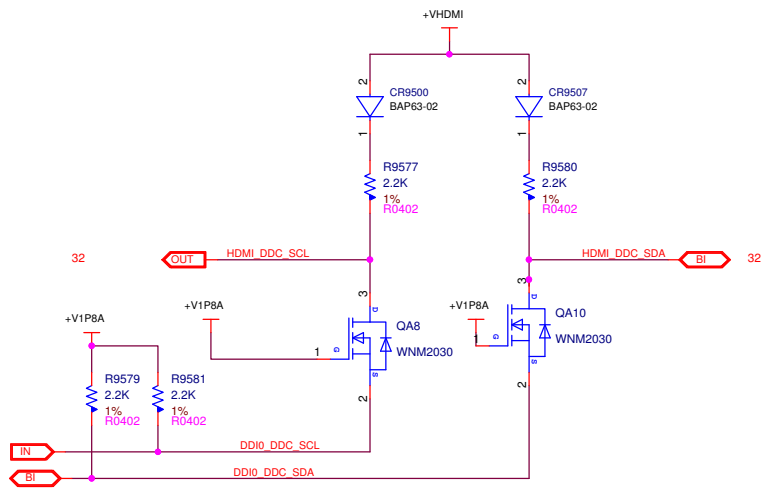
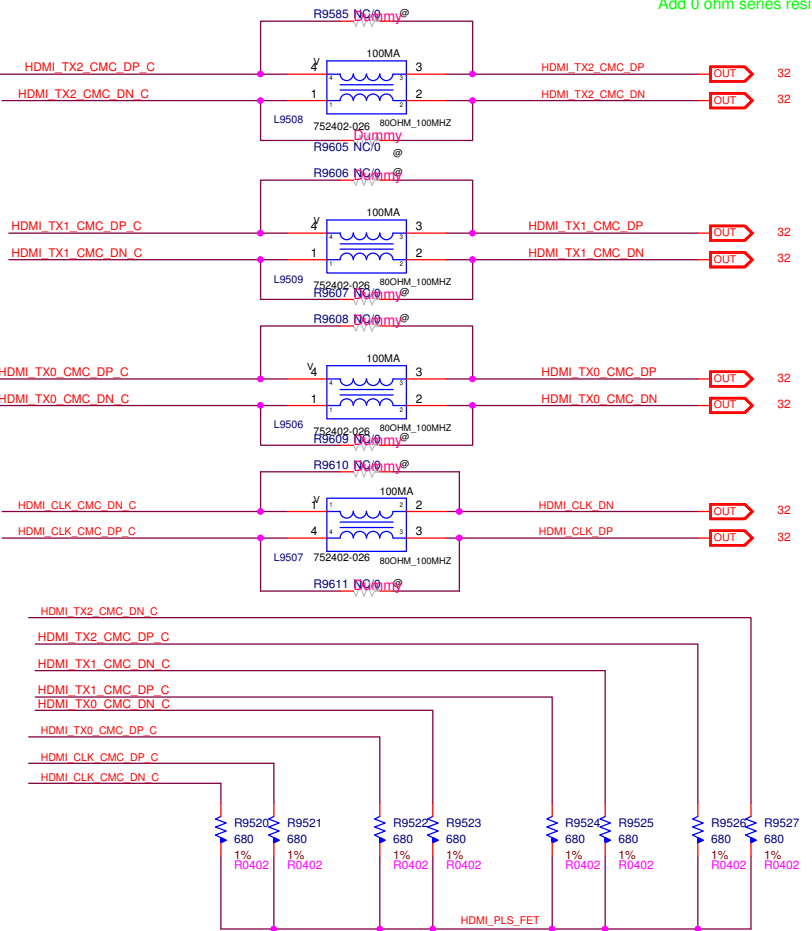


Title		
30_USB CONNECTOR		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 30 of 41

# HDMI LEVEL SHIFTERS & CONNECTOR

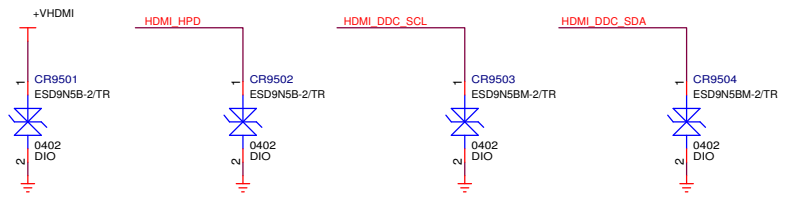
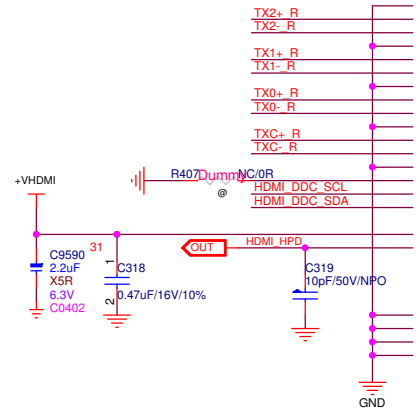
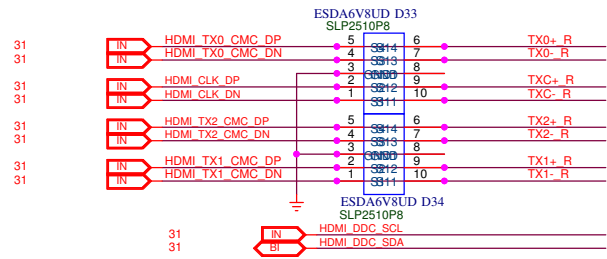
4	IN	DDIO_TX0_DP	C9580	0.1uF C0402	X7R 10V	HDMI_TX2_CMC_DP_C
4	IN	DDIO_TX0_DN	C9581	0.1uF C0402	X7R 10V	HDMI_TX2_CMC_DN_C
4	IN	DDIO_TX1_DP	C9582	0.1uF C0402	X7R 10V	HDMI_TX1_CMC_DP_C
4	IN	DDIO_TX1_DN	C9583	0.1uF C0402	X7R 10V	HDMI_TX1_CMC_DN_C
4	IN	DDIO_TX2_DP	C9584	0.1uF C0402	X7R 10V	HDMI_TX0_CMC_DP_C
4	IN	DDIO_TX2_DN	C9585	0.1uF C0402	X7R 10V	HDMI_TX0_CMC_DN_C
4	IN	DDIO_TX3_DP	C9586	0.1uF C0402	X7R 10V	HDMI_CLK_CMC_DP_C
4	IN	DDIO_TX3_DN	C9587	0.1uF C0402	X7R 10V	HDMI_CLK_CMC_DN_C

Add 0 ohm series resistors by Momu in 2014.4.24



Title		31_HDMI LEVEL SHIFTERS	
Size	Document Number	Rev	
Custom		V1.0	
Date:	Monday, August 24, 2015	Sheet	31 of 41

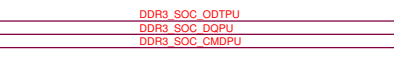
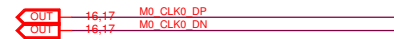
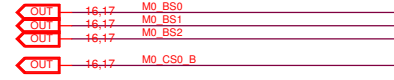
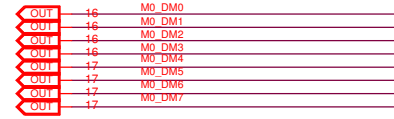
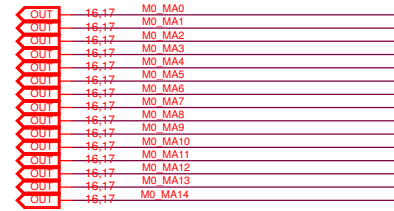
# HDMI CONNECTOR



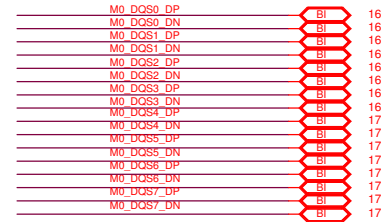
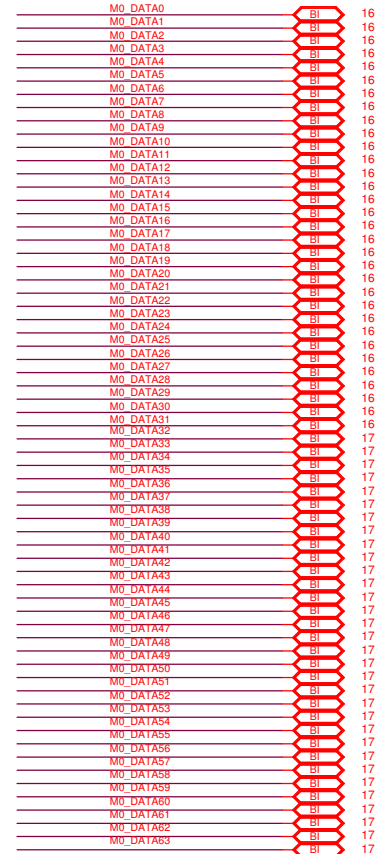
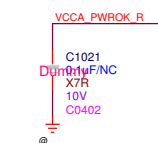
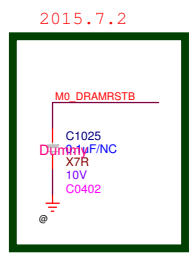
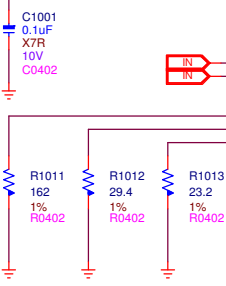
Title		
32_HDMI CONNECTOR		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 32 of 41

SOC: MEMORY

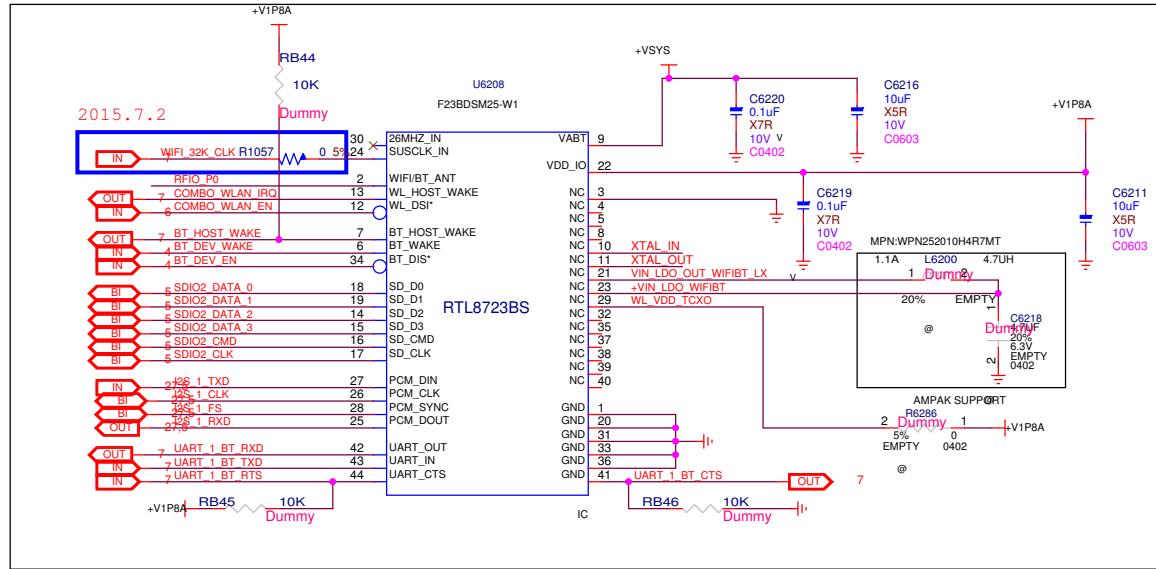
IPN:H28446-001



+VREF\_DDR

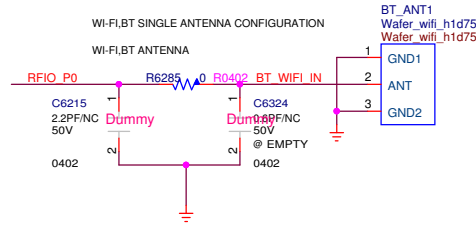
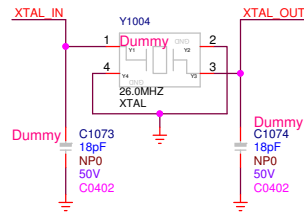


Title		
03_SOC_MEMORY		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 3 of 41



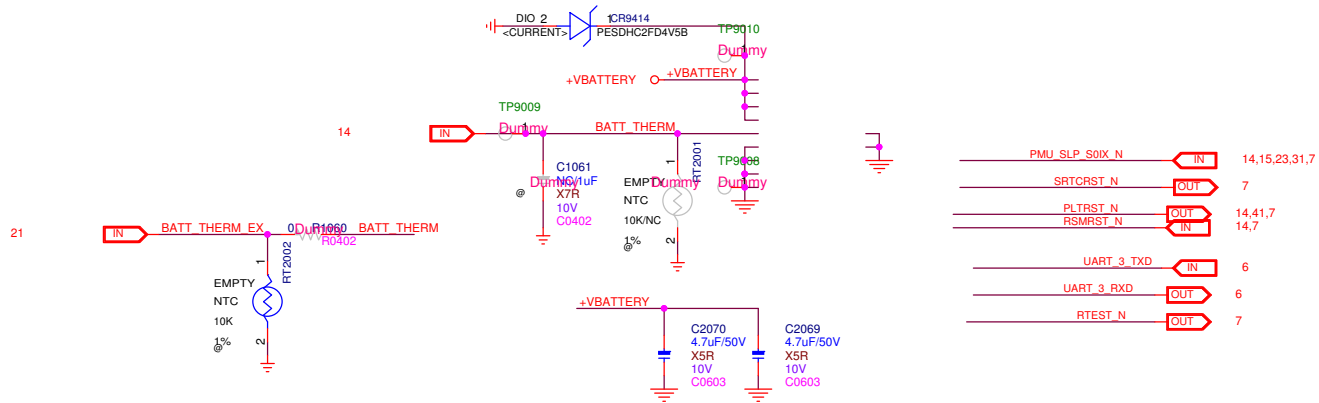
- COMBO WLAN IRO 1 DummyTP6200
- BT\_DEV\_WAKE 1 DummyTP6201
- BT\_HOST\_WAKE 1 DummyTP6202
- WIFI\_32K\_CLK 1 DummyTP6203
- BT\_DEV\_EN 1 DummyTP6204
- SDIO2\_DATA\_0 1 DummyTP6205
- SDIO2\_CMD 1 DummyTP6206
- SDIO2\_CLK 1 DummyTP6207
- UART\_1\_BT\_RXD 1 DummyTP6208
- UART\_1\_BT\_TXD 1 DummyTP6209

FOR AMPAK, R6289 TO BE CHANGED TO 10PF



Title		
33_WIFI-BT		
Size	Document Number	Rev
Custom		V1.0
Date:	Monday, August 24, 2015	Sheet 33 of 41

# BATTERY & DEBUG CONNECTOR

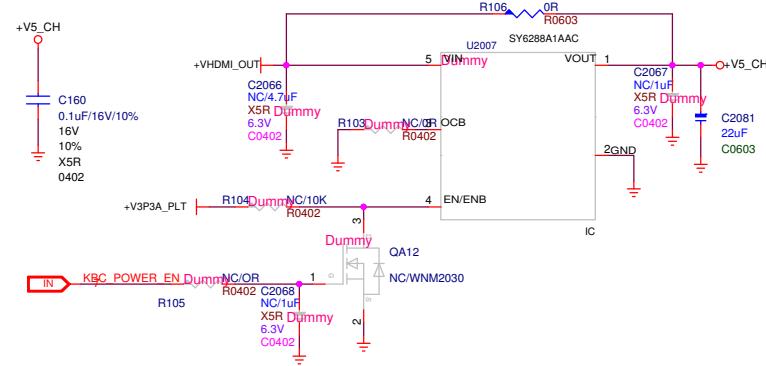
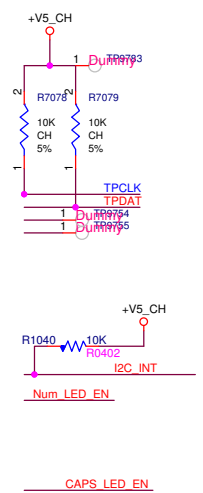
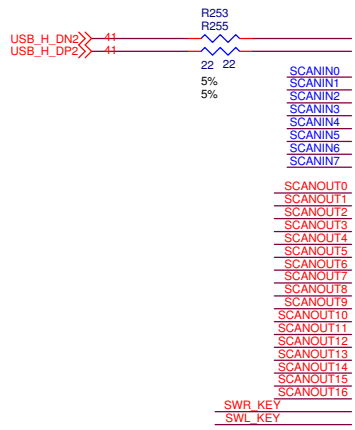
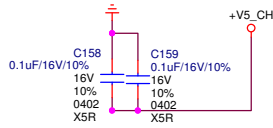


PMU_SLP_S0IX_N	IN	14,15,23,31,7
SRTCORST_N	OUT	7
PLTRST_N	OUT	14,41,7
RSMRST_N	IN	14,7
UART_3_TXD	IN	6
UART_3_RXD	OUT	6
RTEST_N	OUT	7

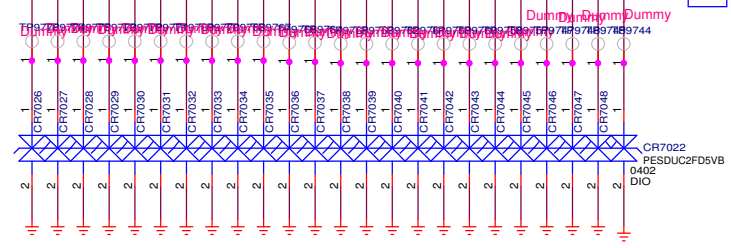
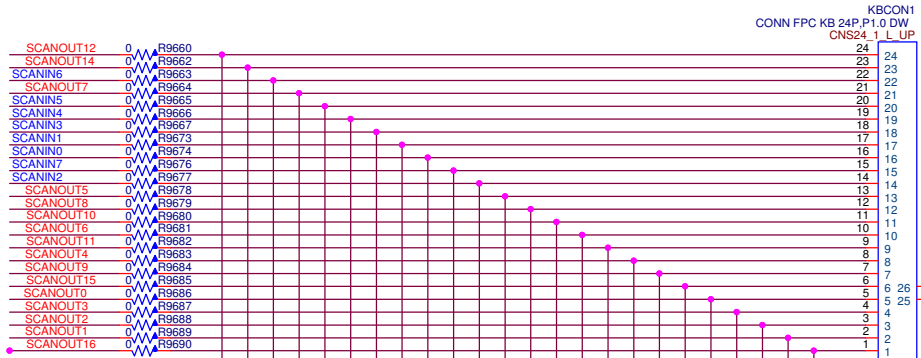
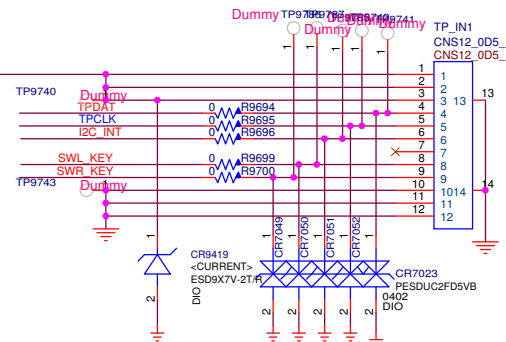
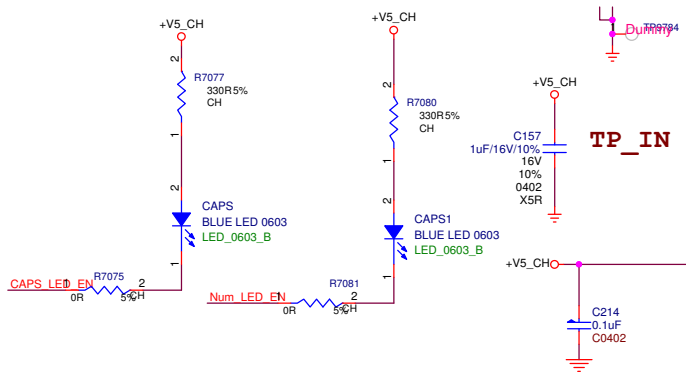
Title		
36_BATTERY & DEBUG CONNECTOR		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 34 of 41

# KBC/TOUCH

2015.7.2



11.6"KB



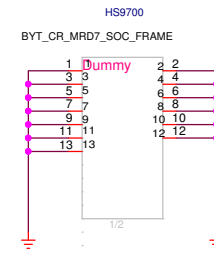
Title		35_KBC/TOUCH	
Size	Custom	Document Number	
Date:	Friday, September 25, 2015	Sheet	35 of 41
		Rev	V1.0

LED+

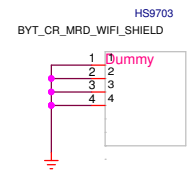


# MFG TEST POINTS, SHIELDS & MOUNTING HOLES

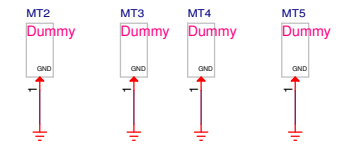
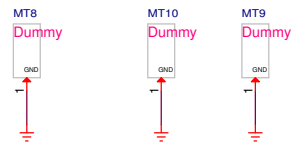
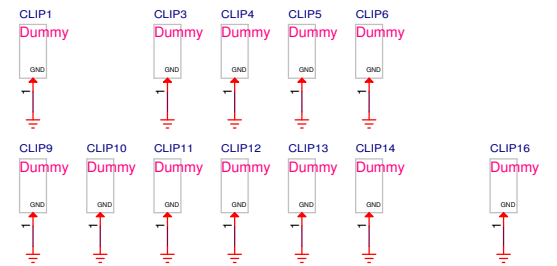
TPS SHOULD BE PLACED CLOSE TO DEBUG CONNECTOR



SHIELD

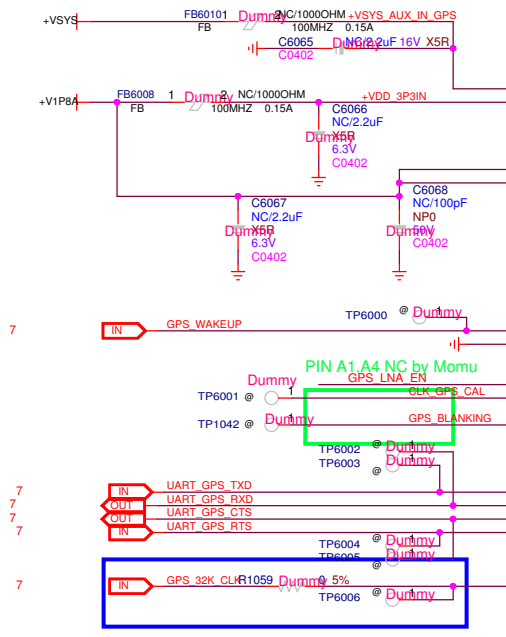


SHIELD

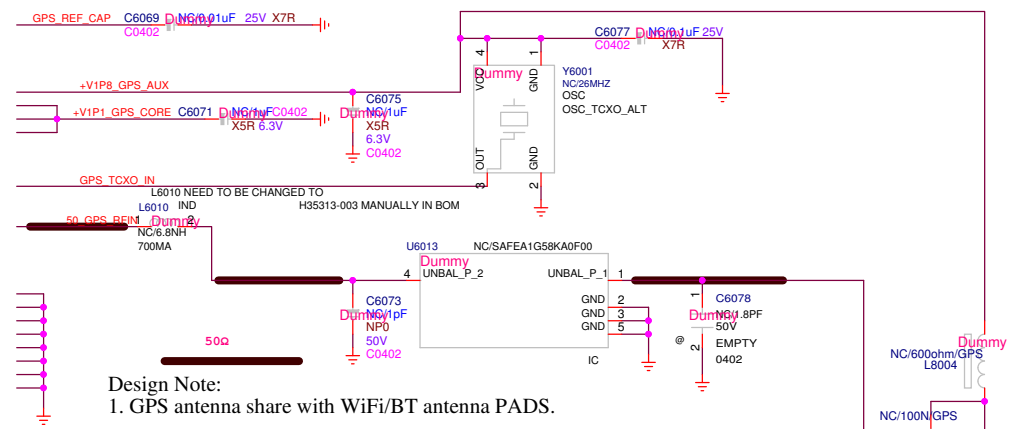
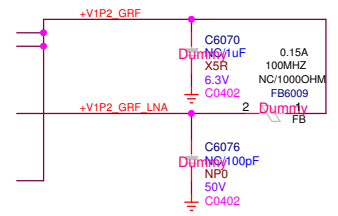


Title		
36_MFG TEST POINTS, SHIELDS & M		
Size	Document Number	Rev
Custom		V1.0
Date:	Monday, August 24, 2015	Sheet 36 of 41

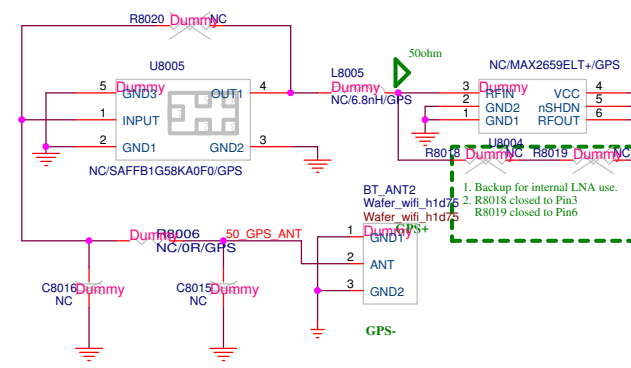
GPS



2015.7.2

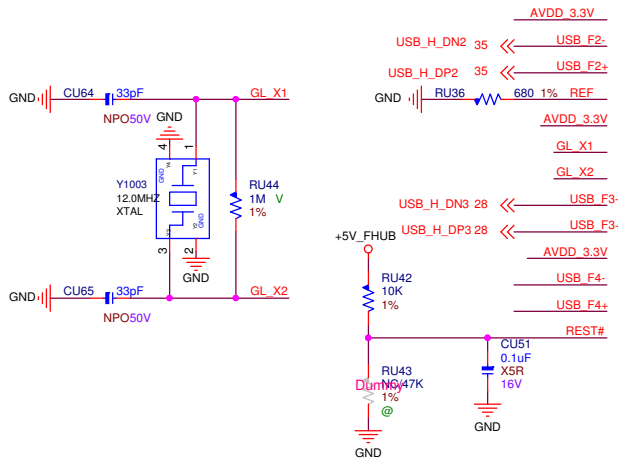


Design Note:  
1. GPS antenna share with WiFi/BT antenna PADS.

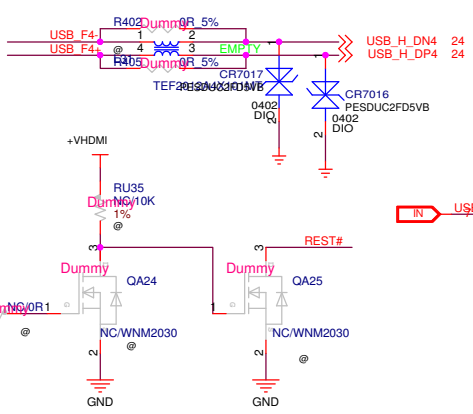


Title		
38_GPS		
Size	Document Number	Rev
Custom		V1.0
Date:	Friday, September 25, 2015	Sheet 38 of 41

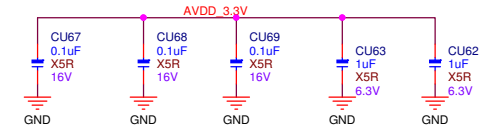
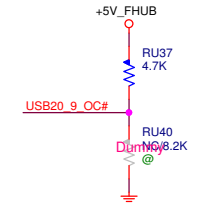
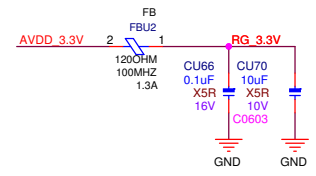
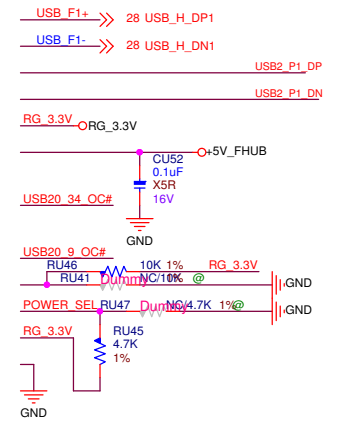
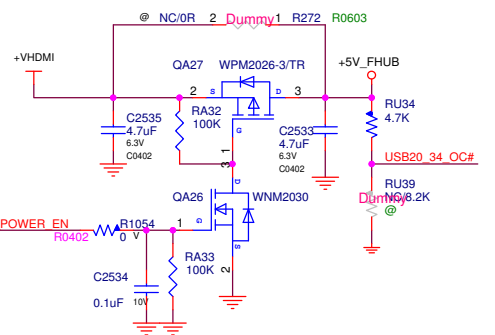
# HUB



EMI



POWER SELECT  
0: GL850G is bus-powered.  
1: GL850G is self-powered.



CAD NOTE:  
U100 PIN1,PIN5,PIN10,PIN24 each puts one 0.1UF capacitor

Title		41_HUB	
Size	Document Number	Rev	
Custom		V1.0	
Date:	Friday, September 25, 2015	Sheet	41 of 41

LED+