

Occupied Bandwidth Plots

The following are occupied bandwidth plots (26dB and 99%) for report number WIR118717-FCC407 RSS247.

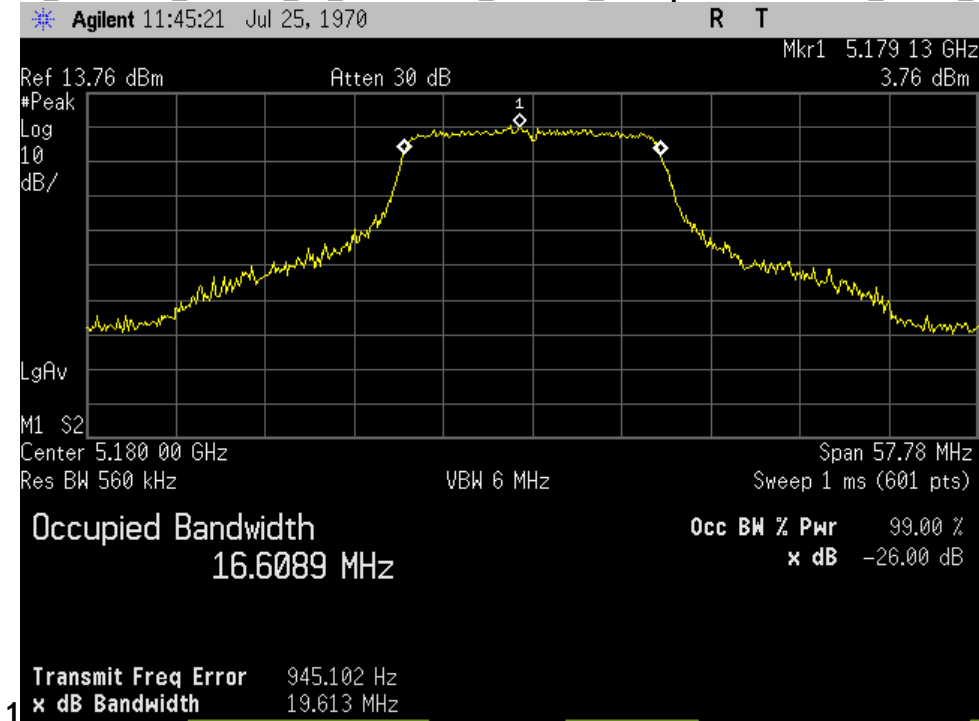
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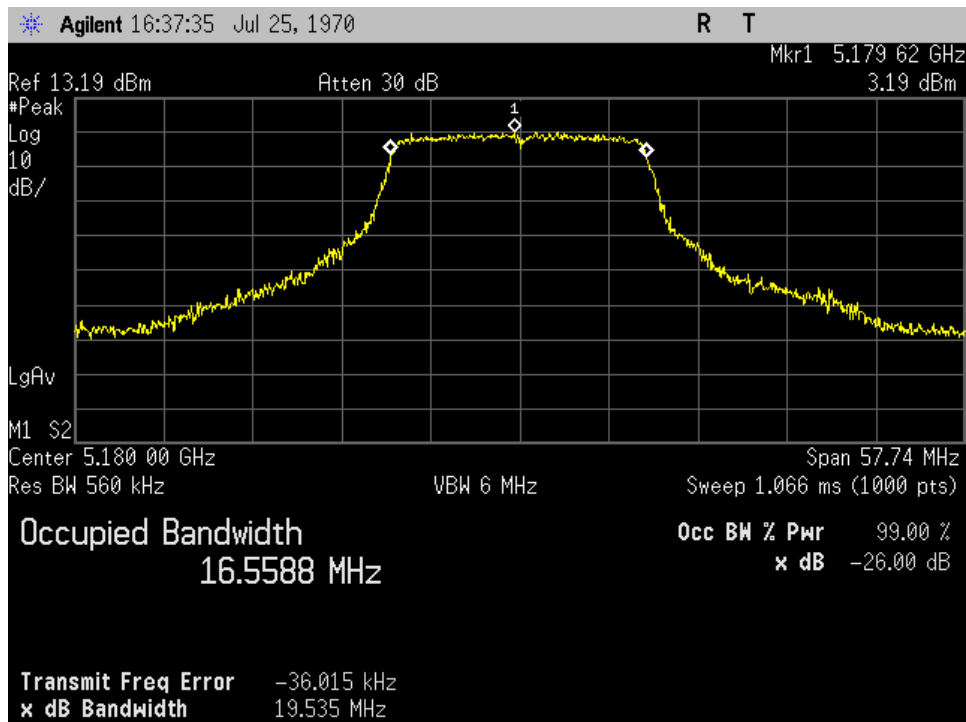
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U-NII-1

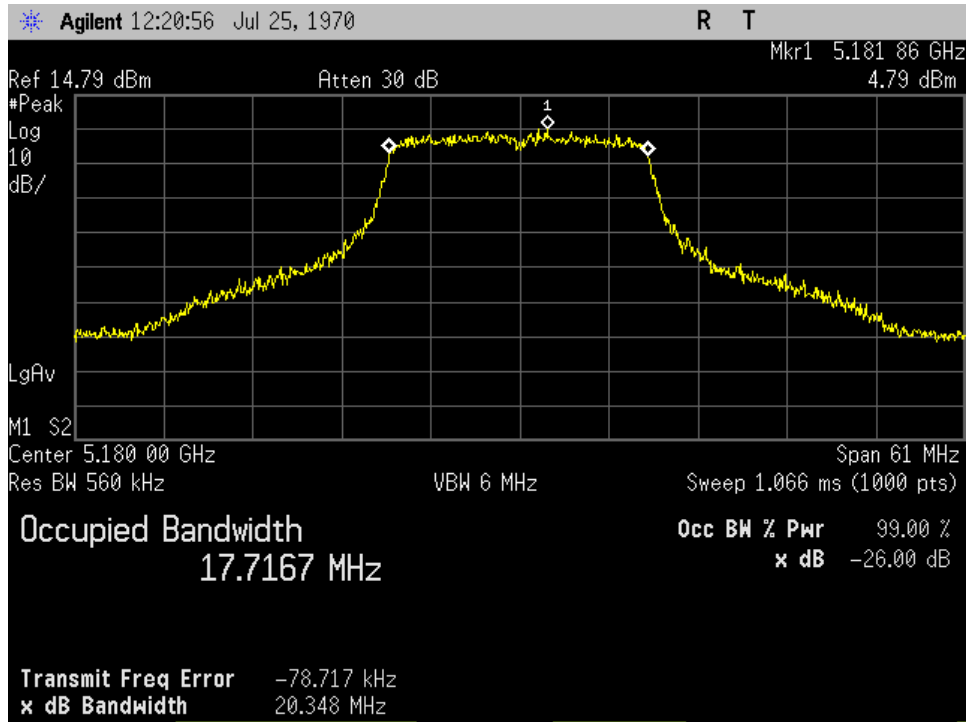
U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port



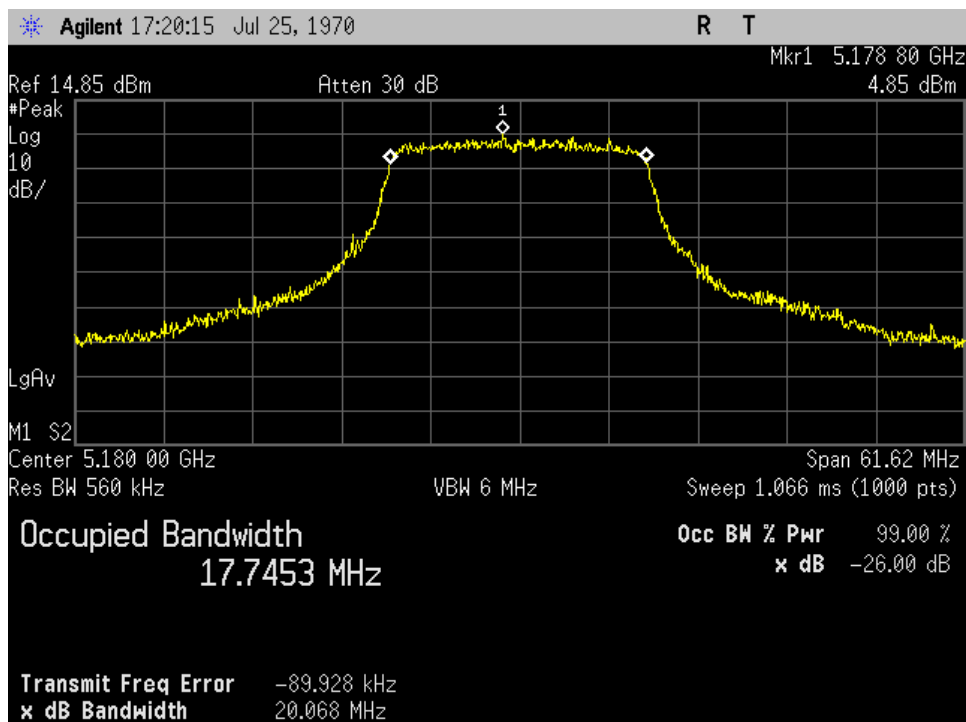
U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2



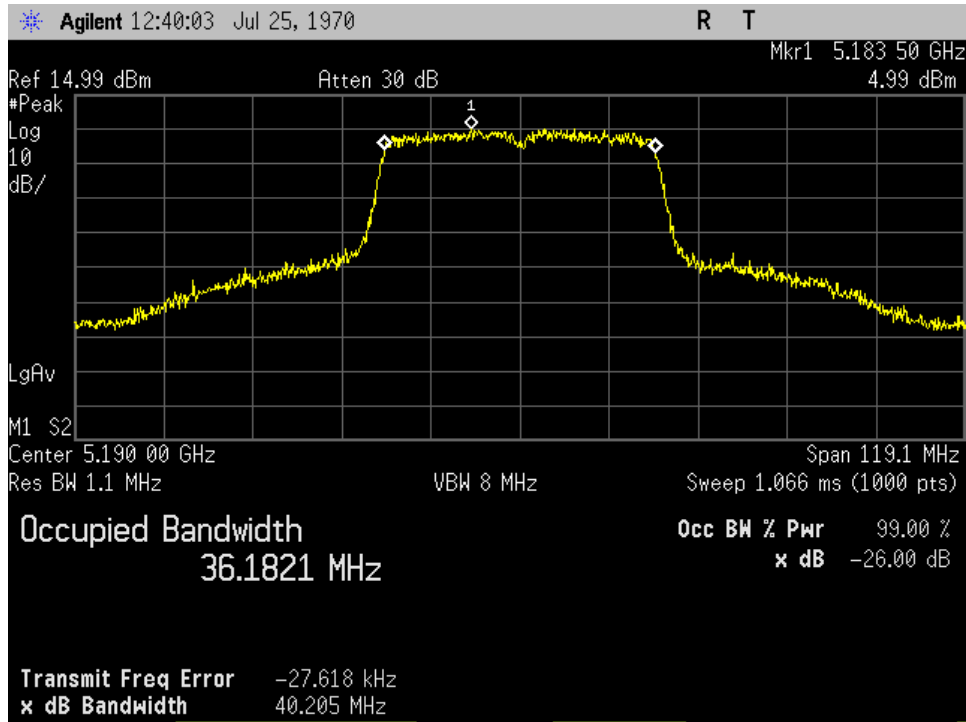
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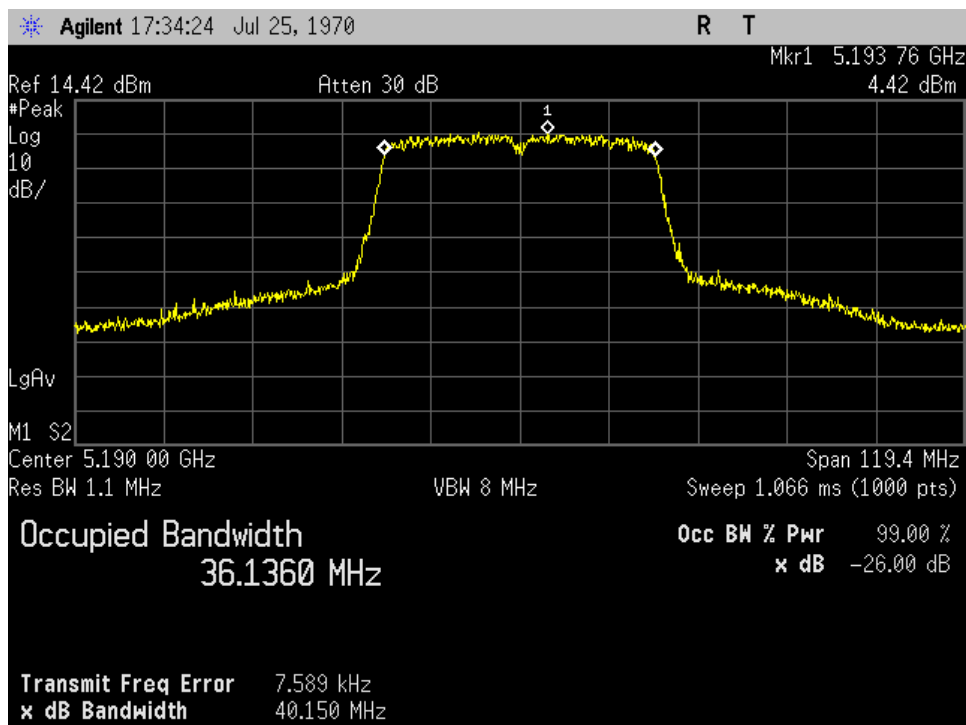
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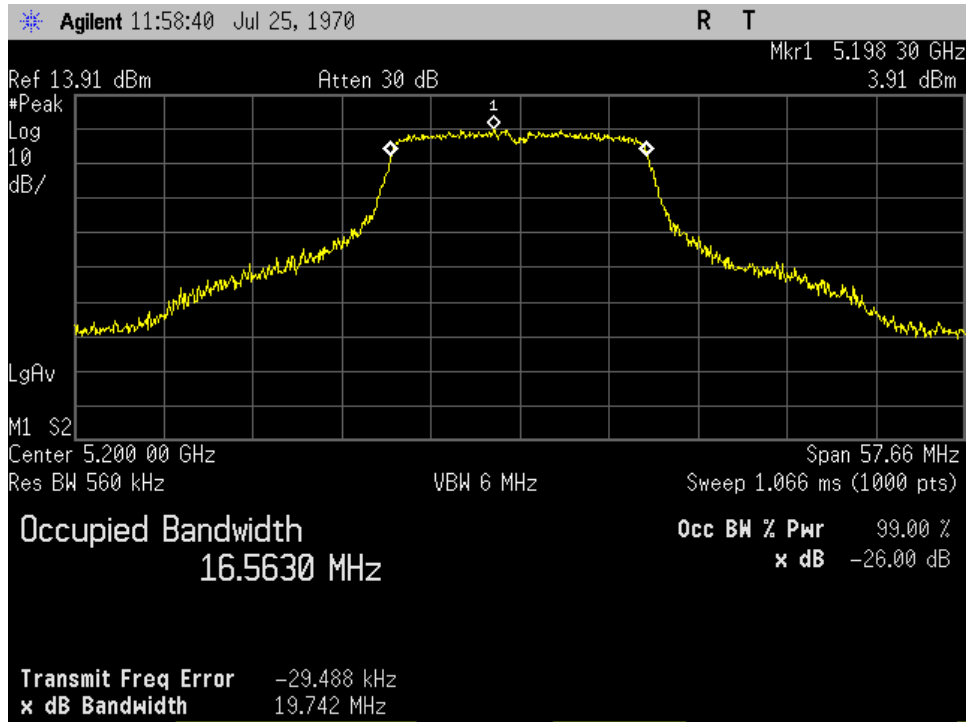
U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



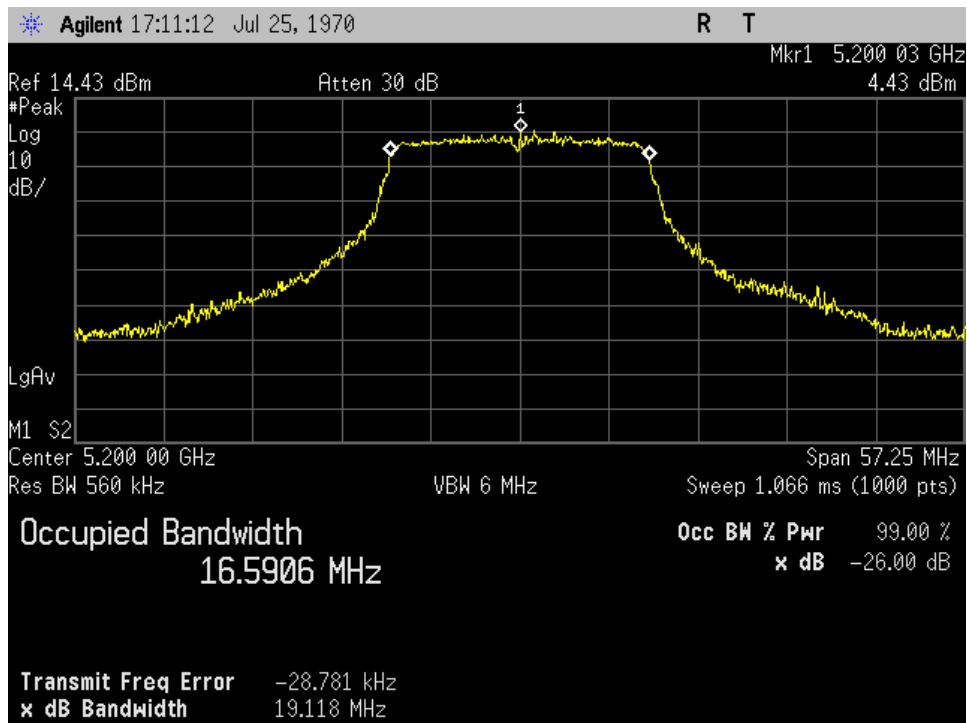
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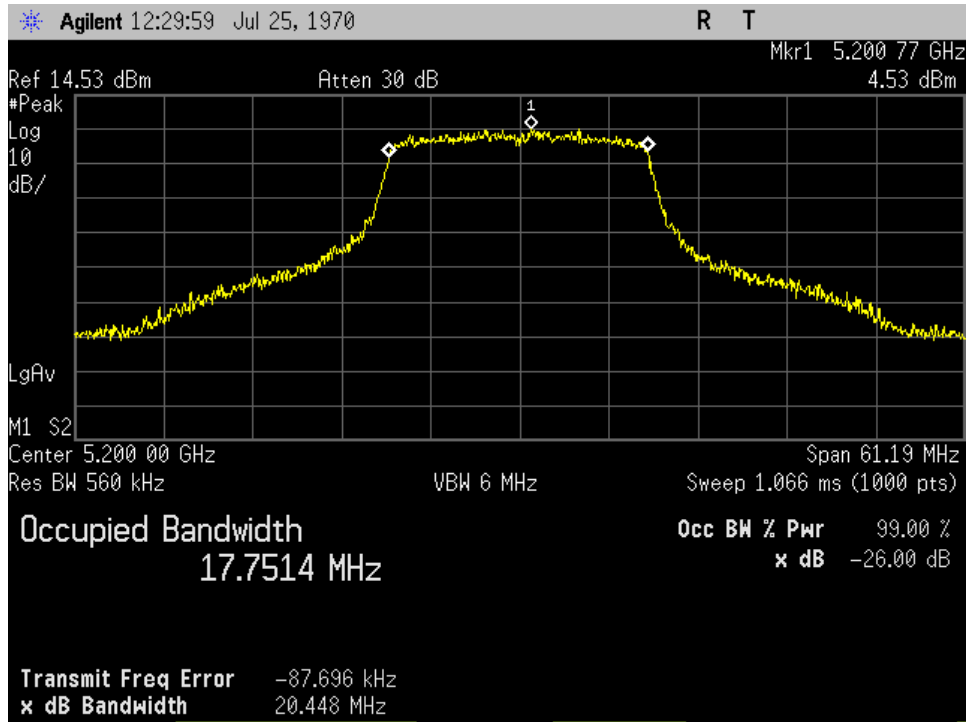
U-NII-1_5200MHz_low_mid Ch_40_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1



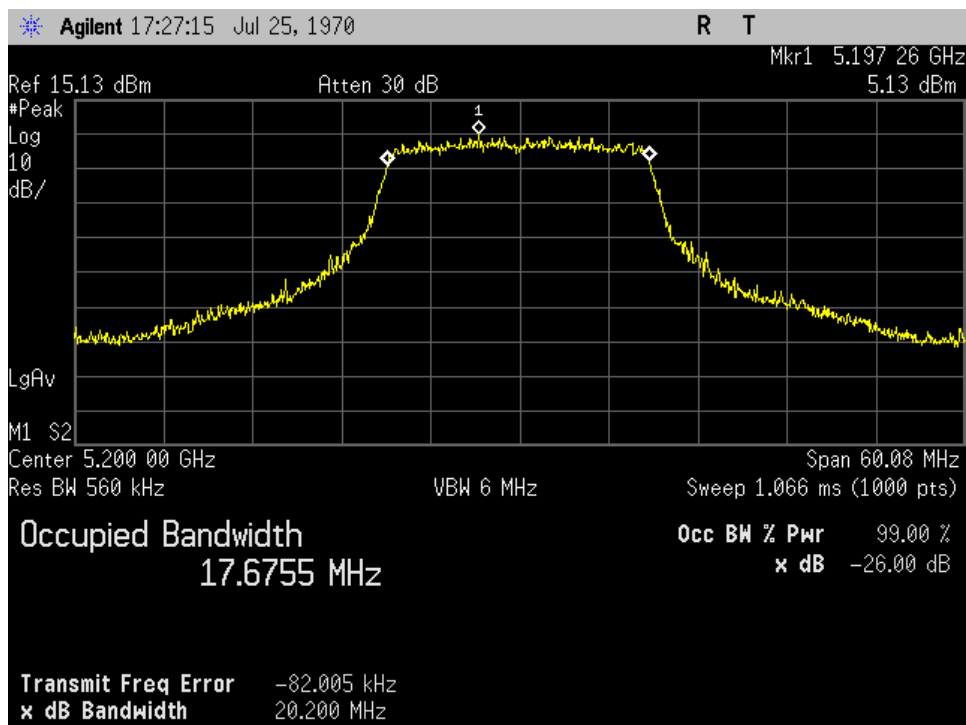
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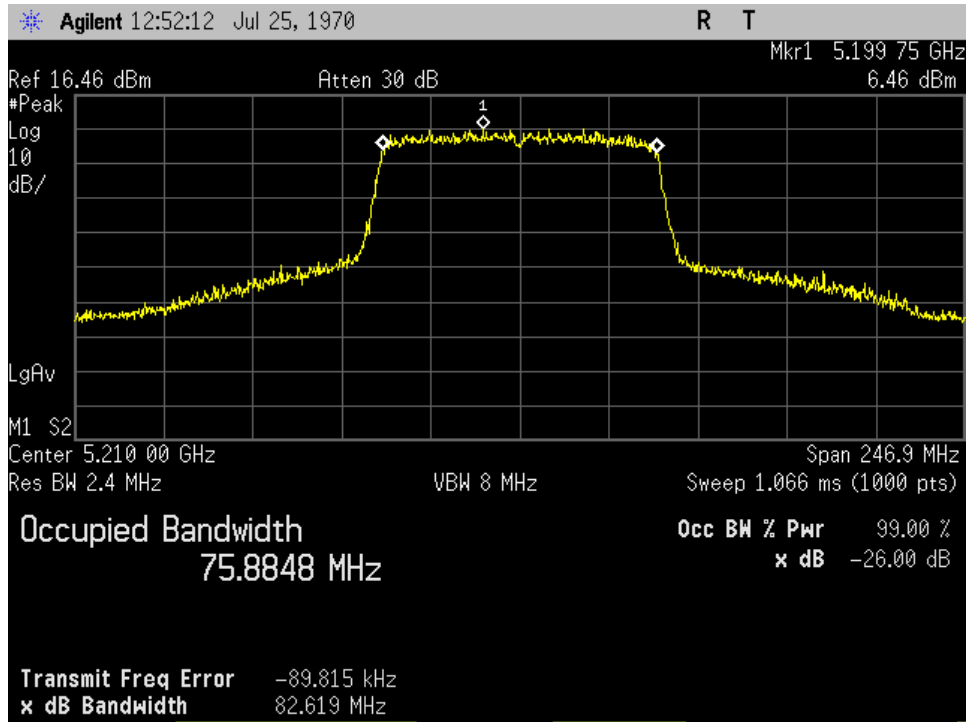
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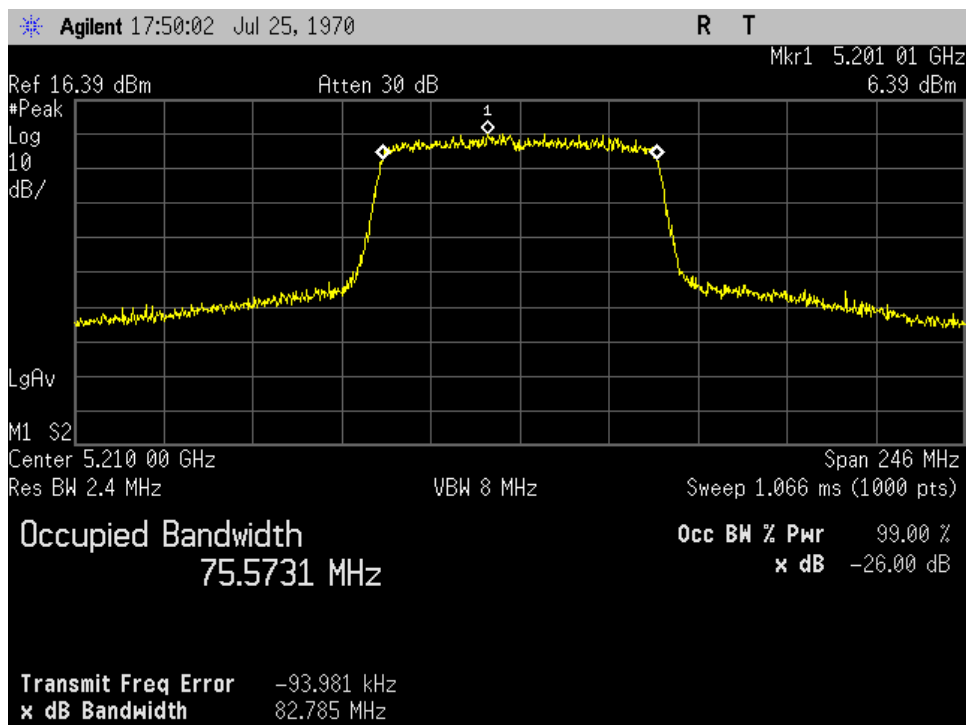
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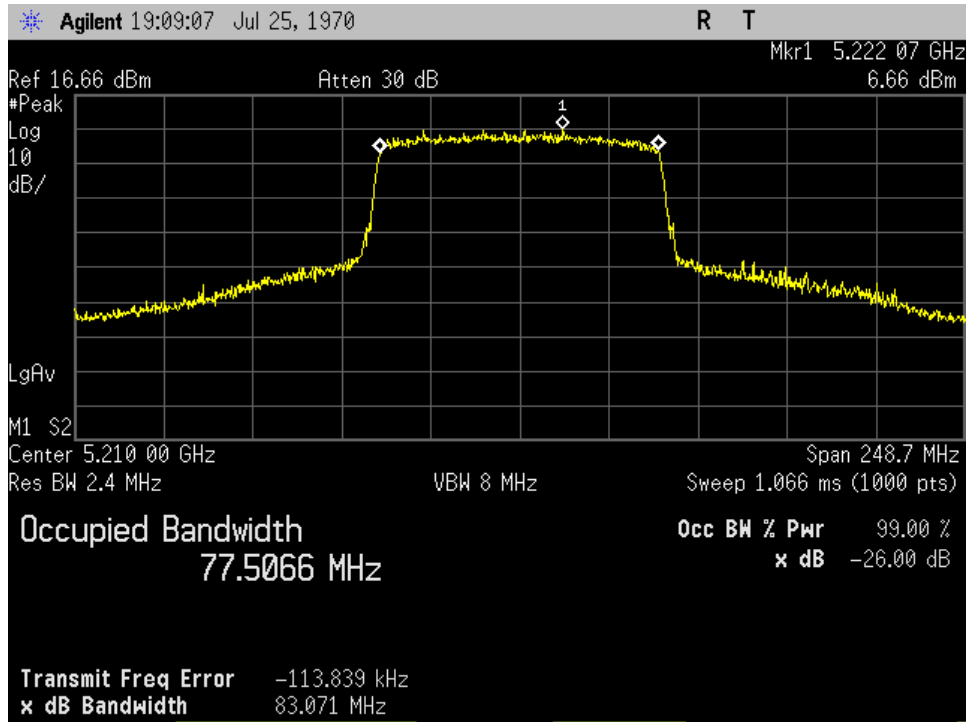
U-NII-1_5210MHz_Low mid Ch_42_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1



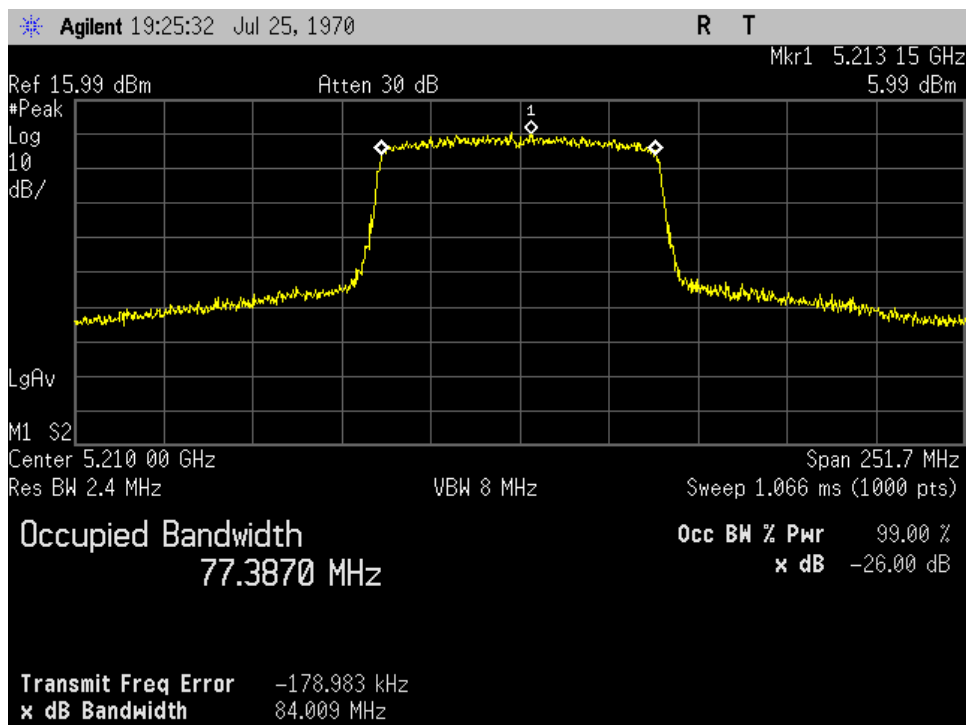
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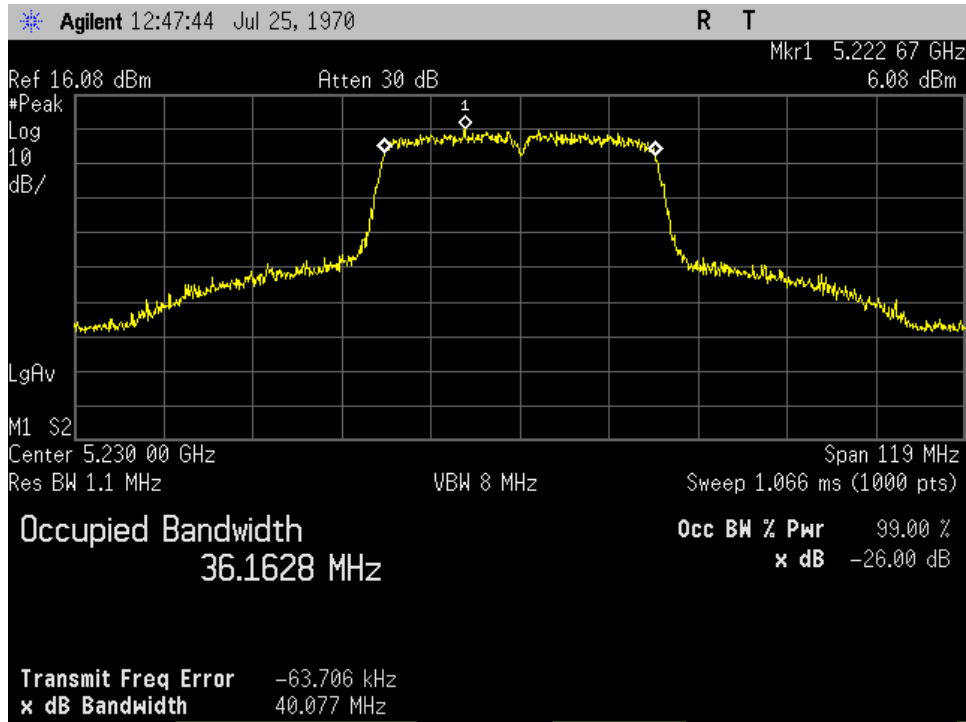
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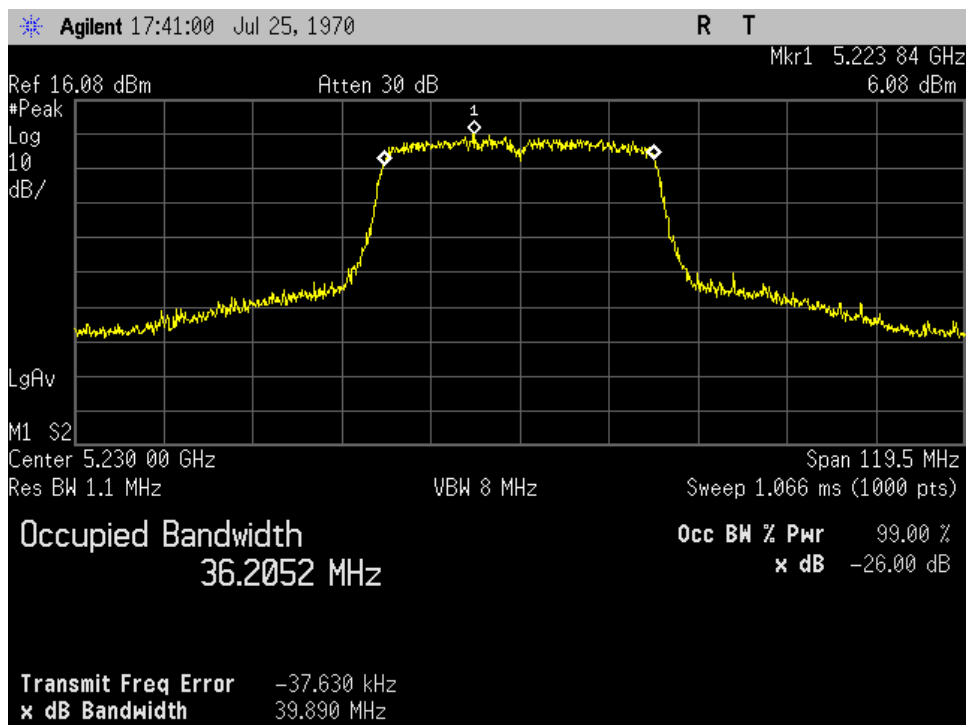
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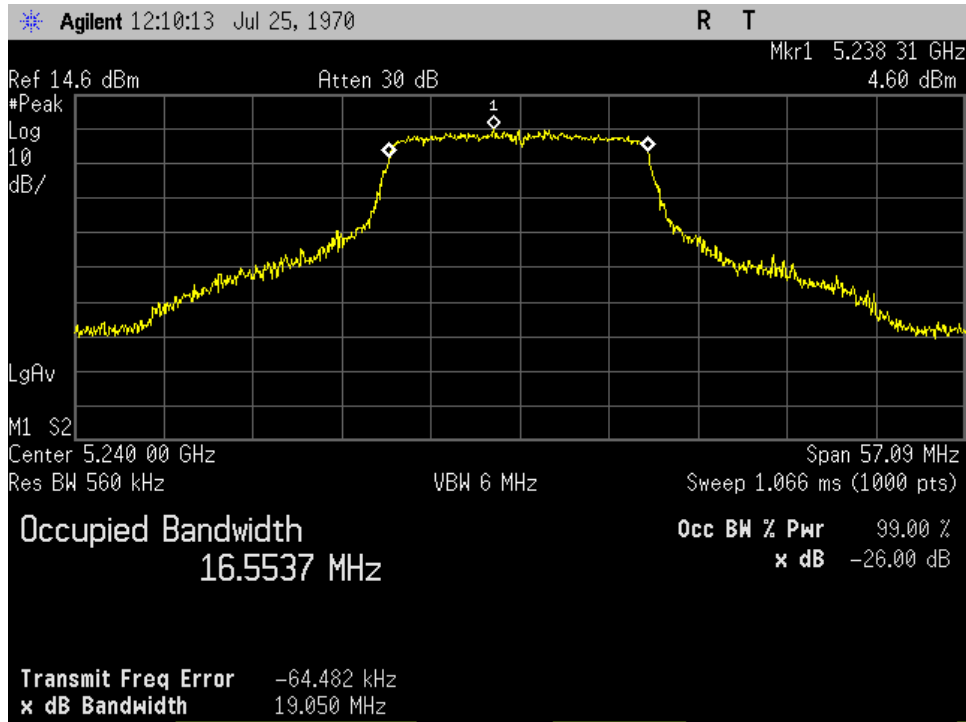
U-NII-1_5230MHz_high_mid Ch_46_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



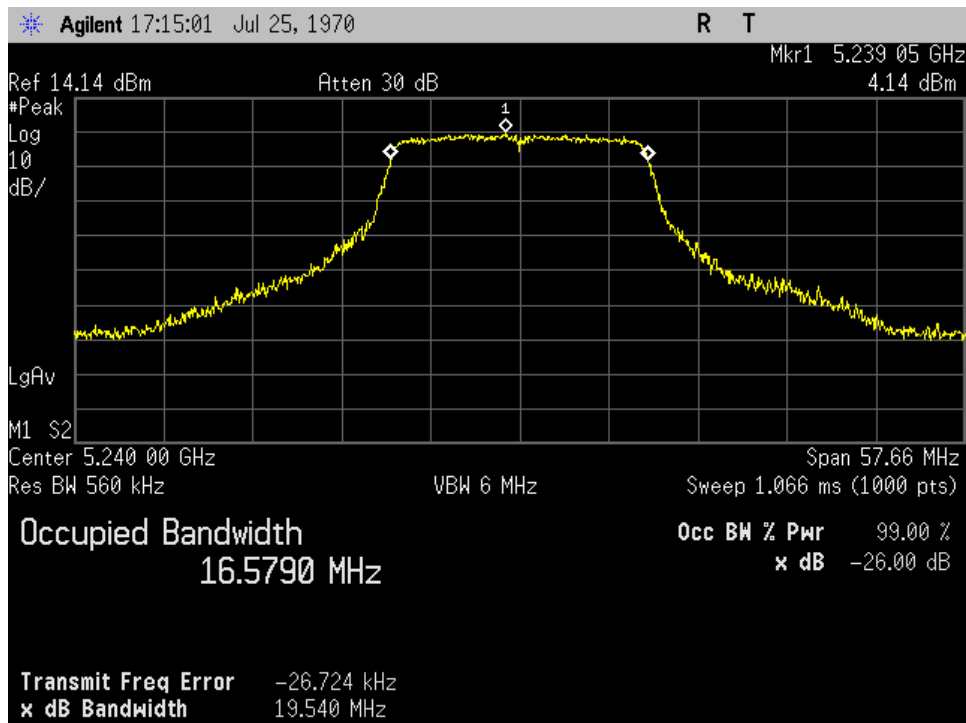
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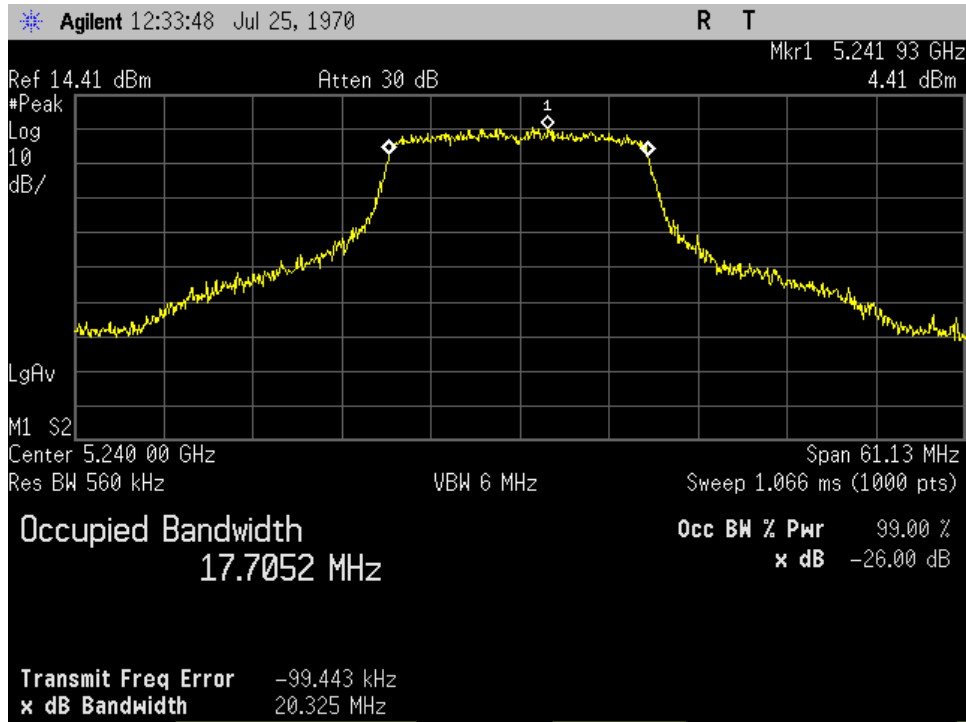
U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1



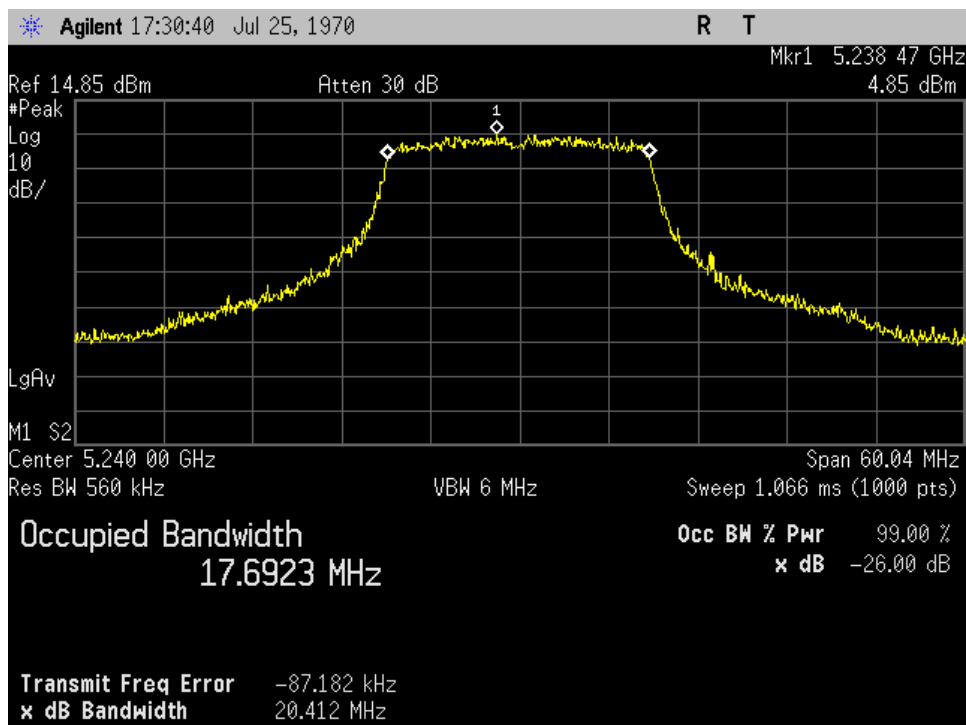
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U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1

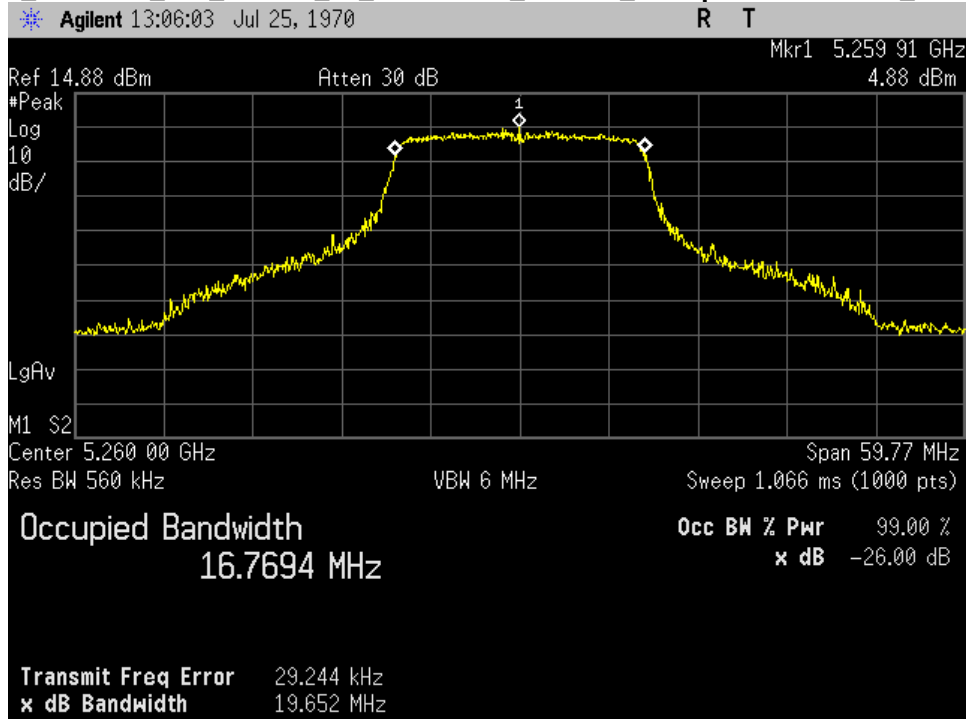


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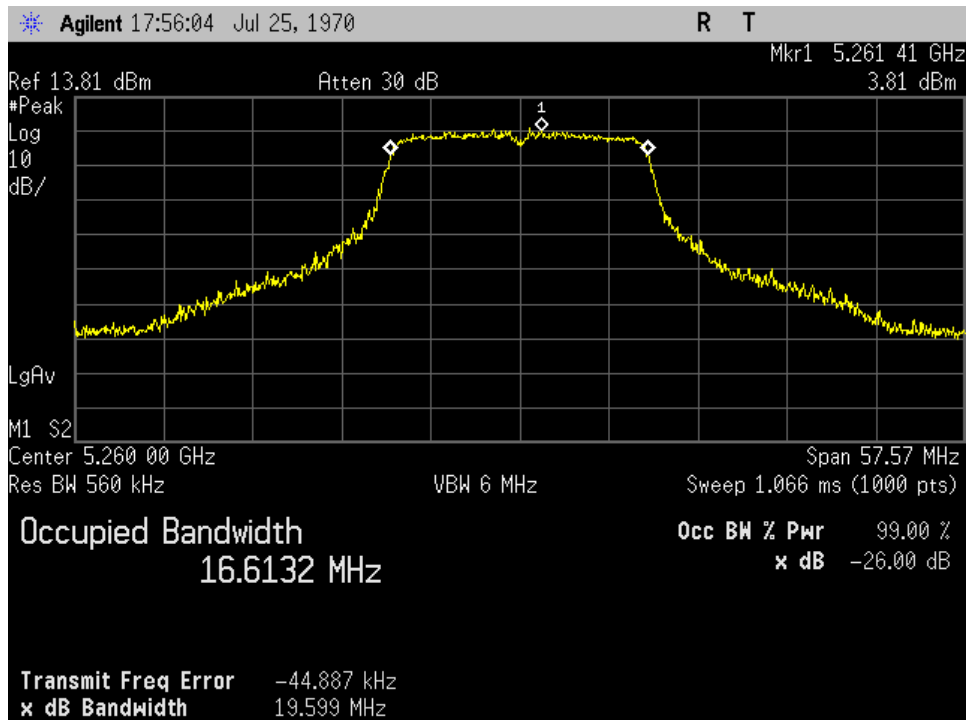


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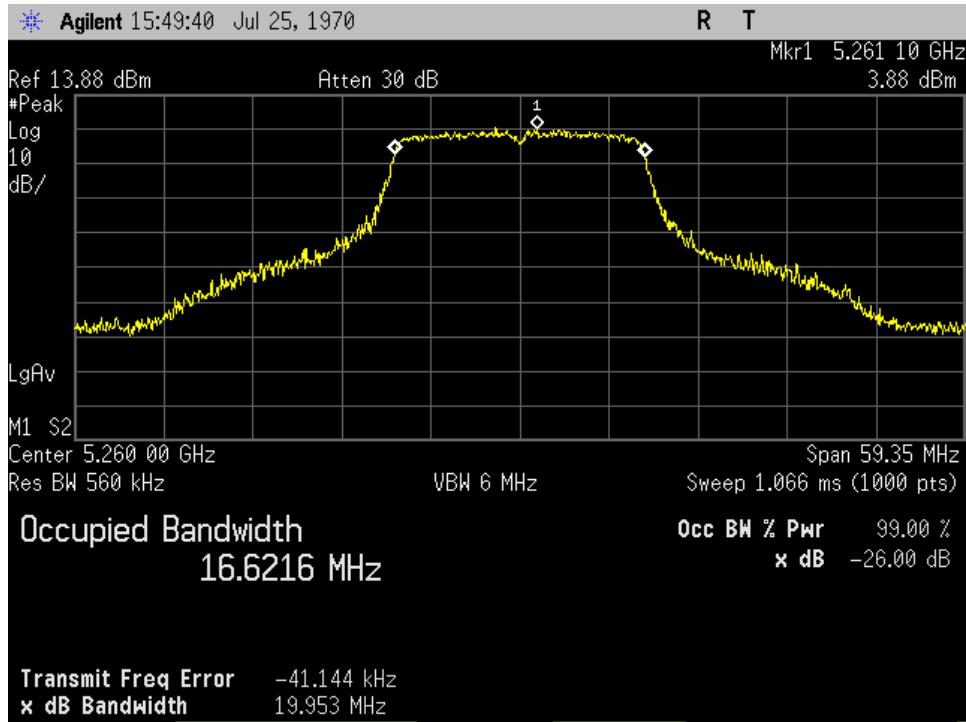
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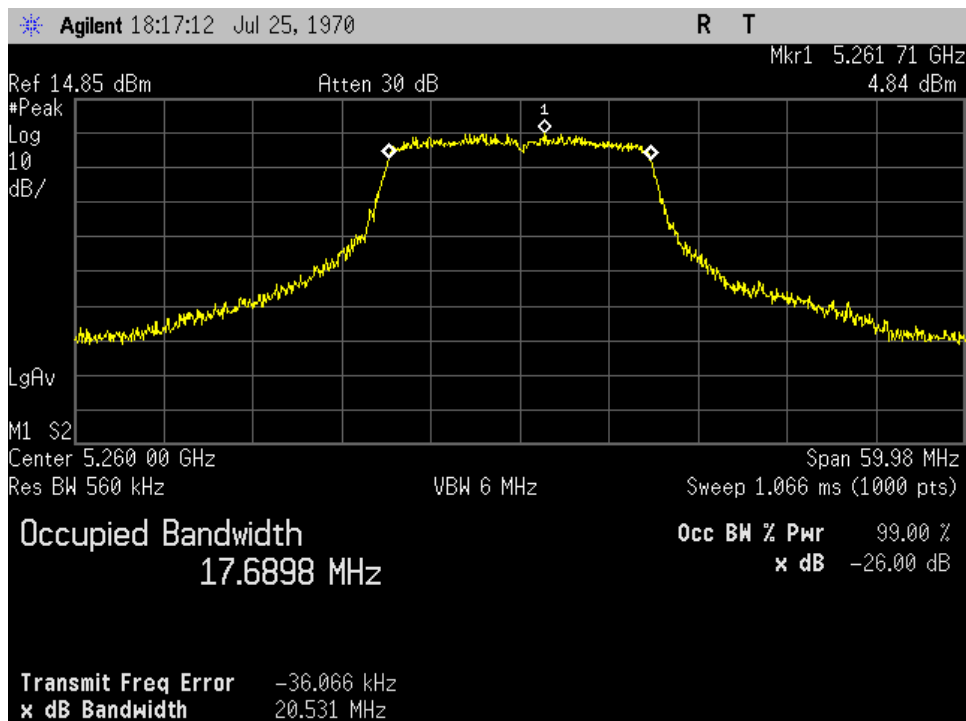
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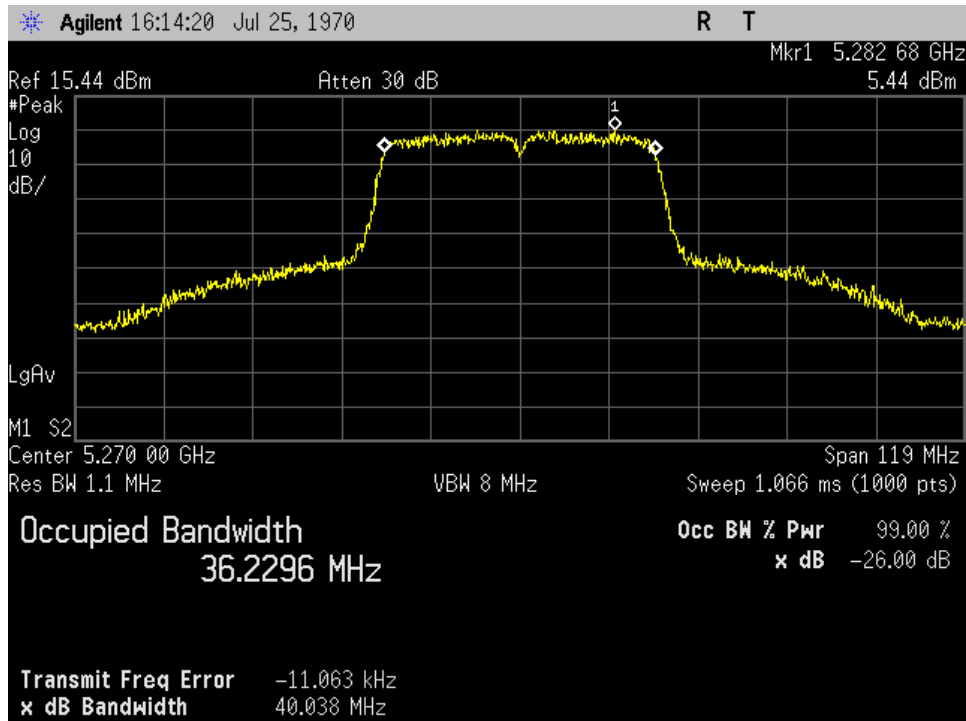
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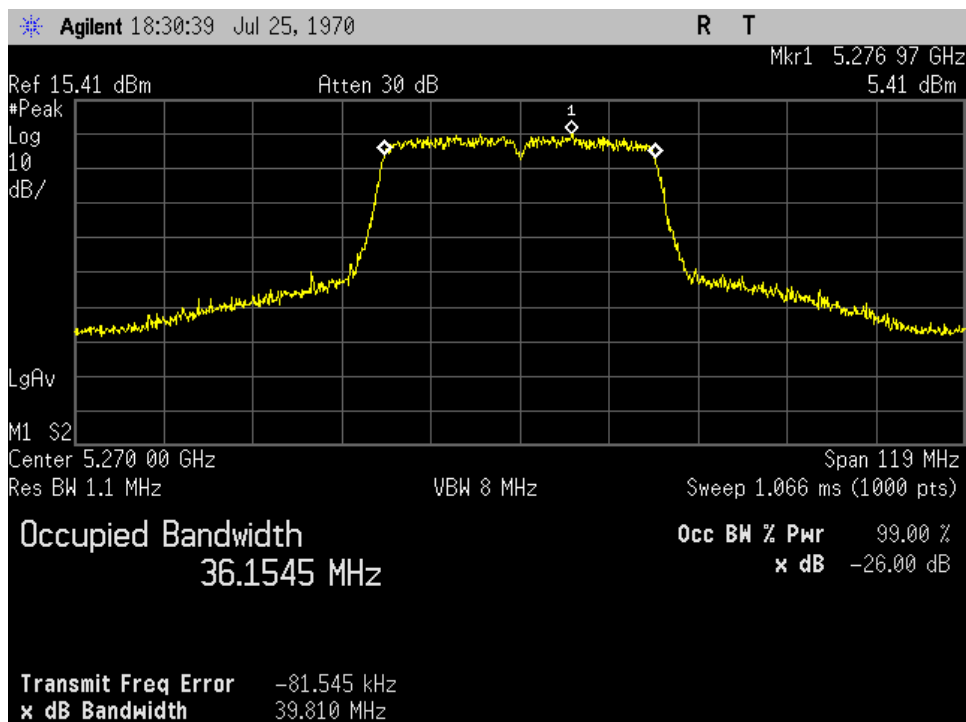
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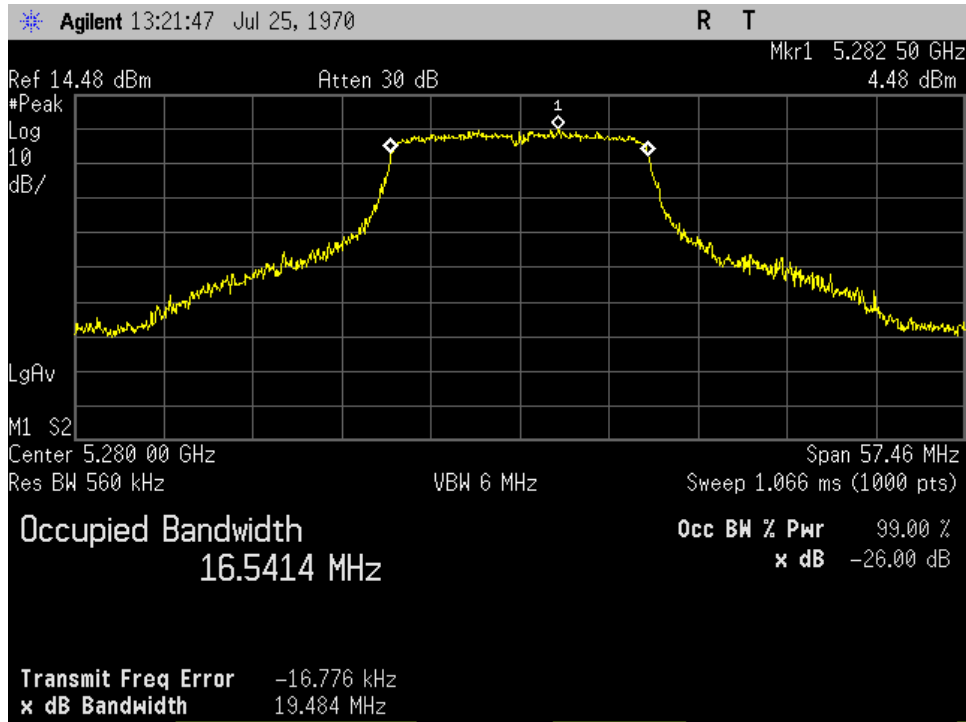
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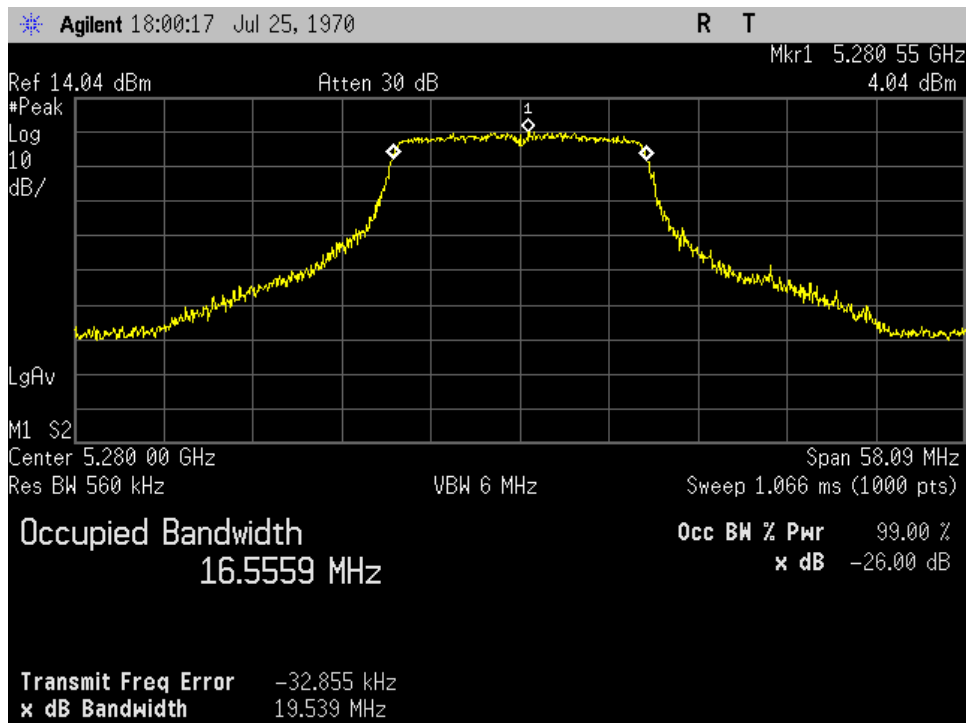
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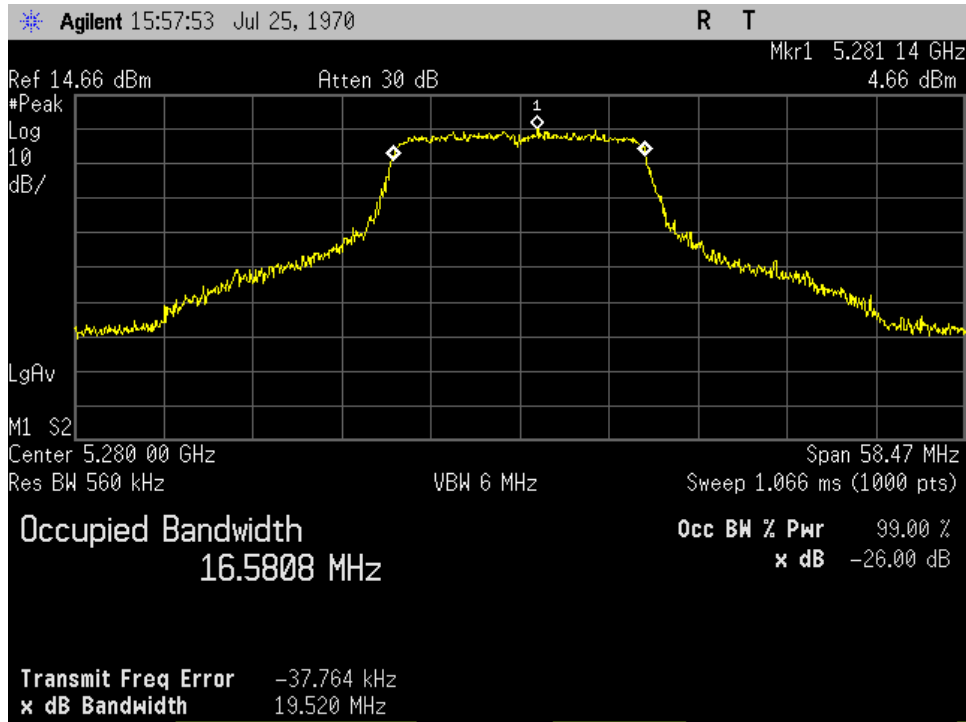
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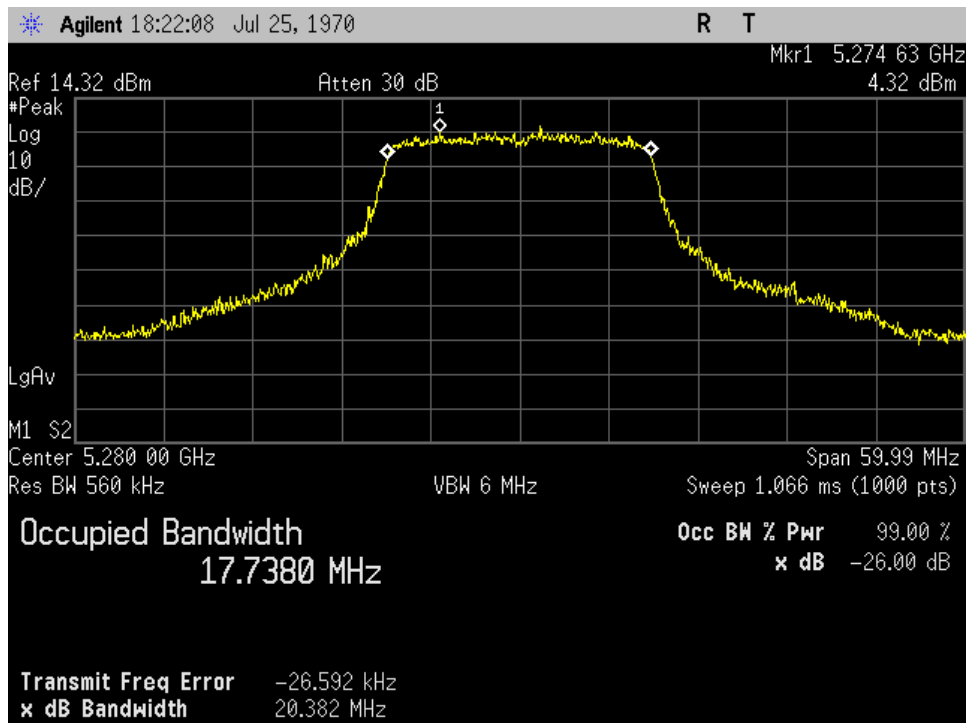
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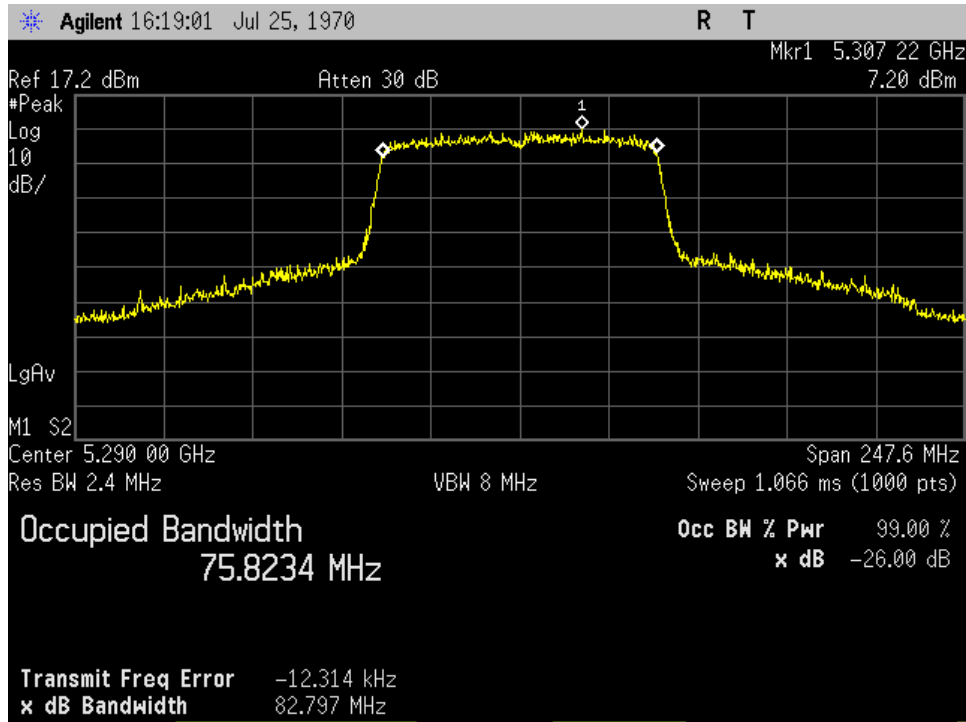
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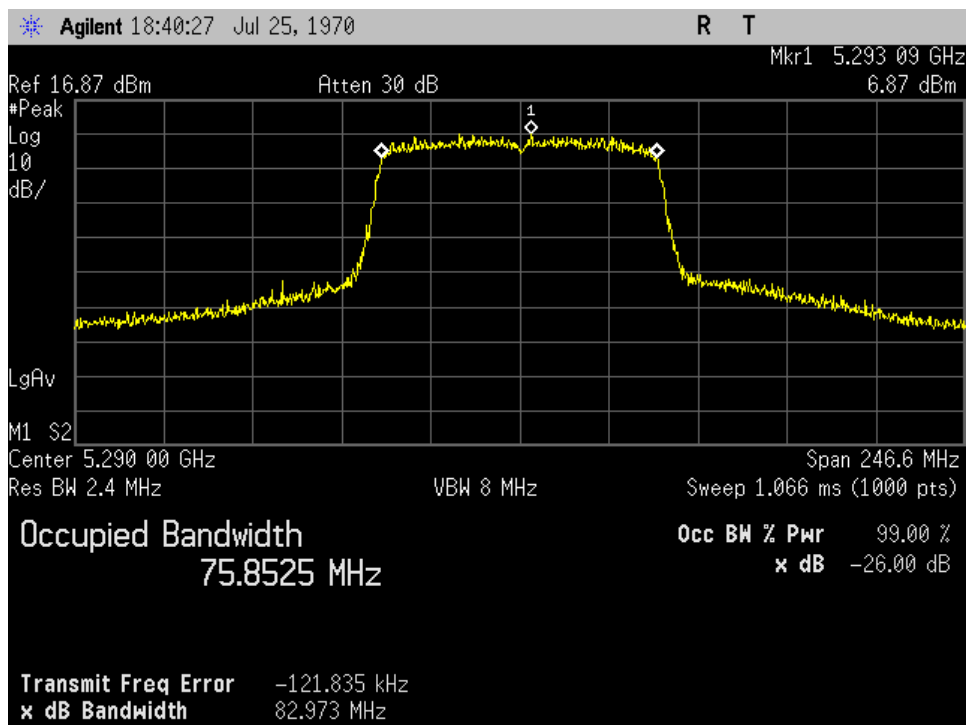
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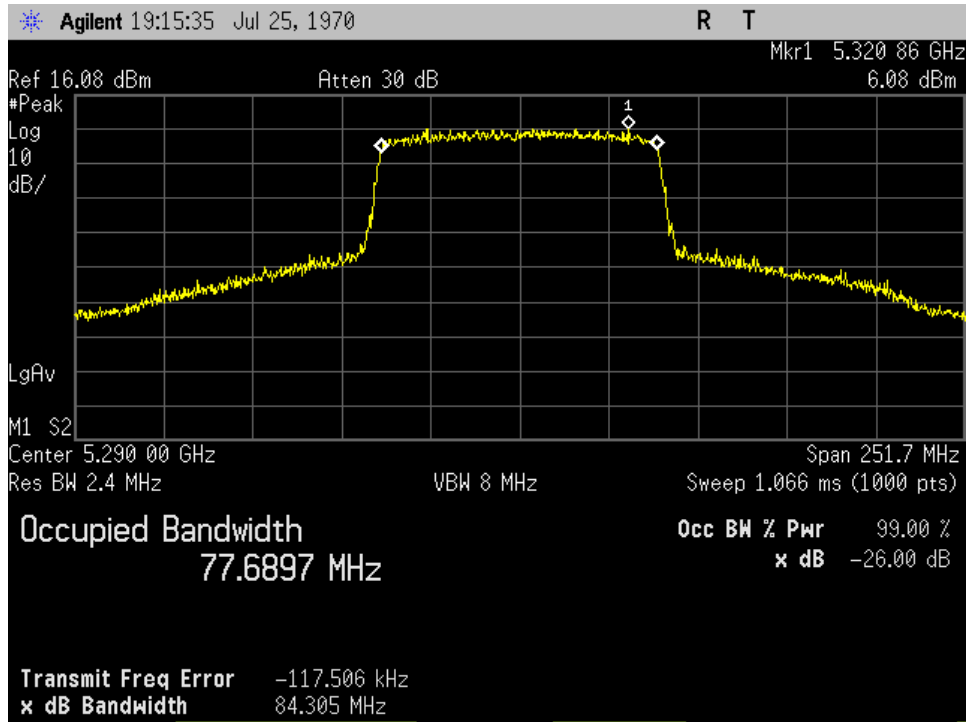
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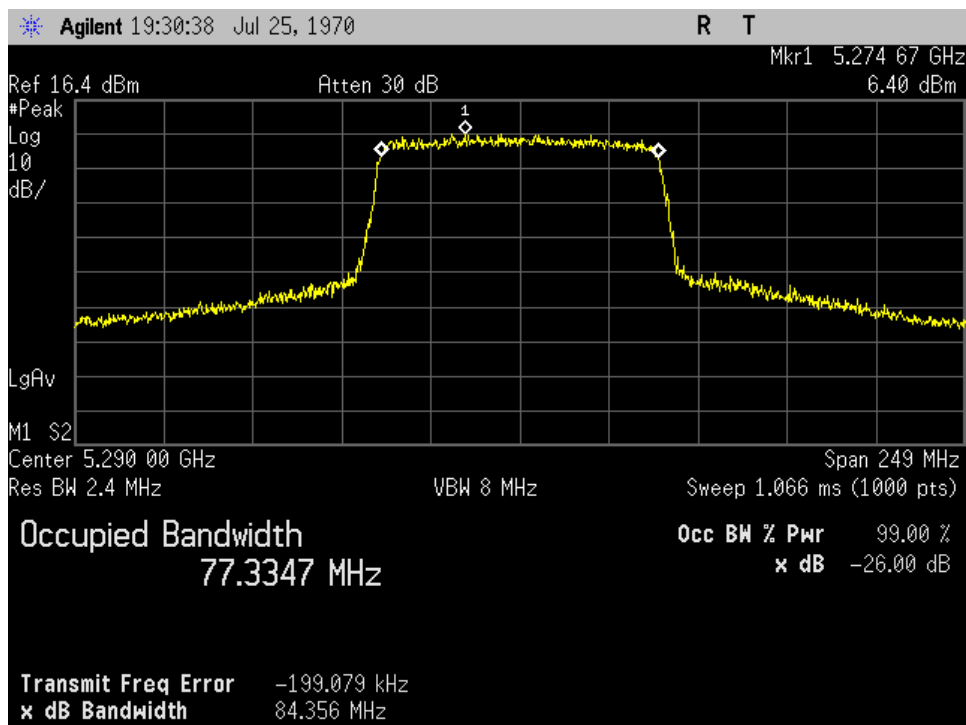
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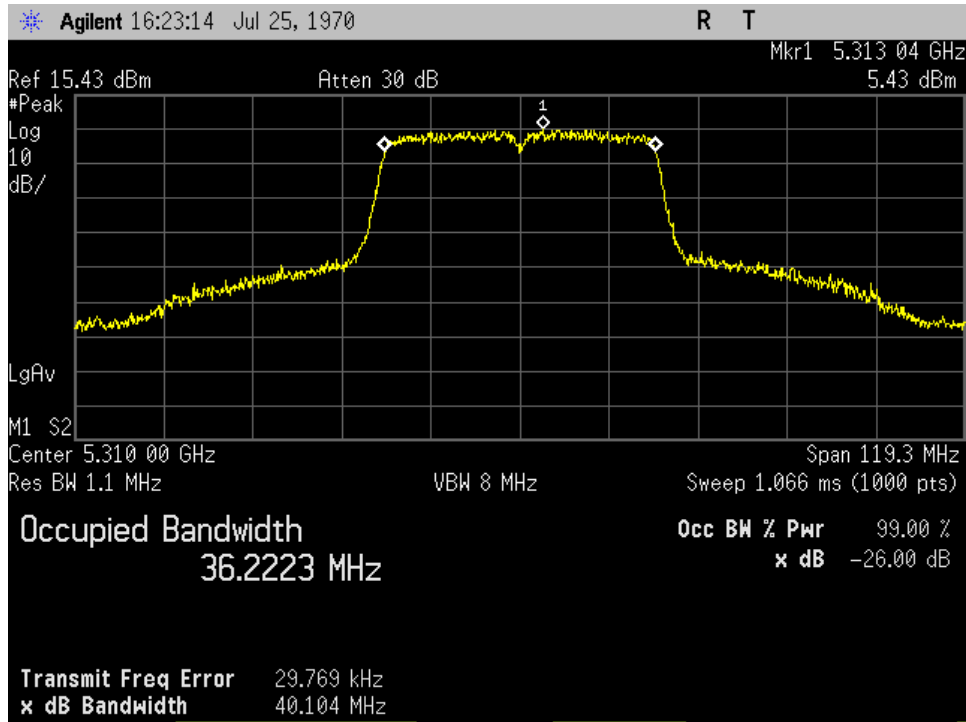
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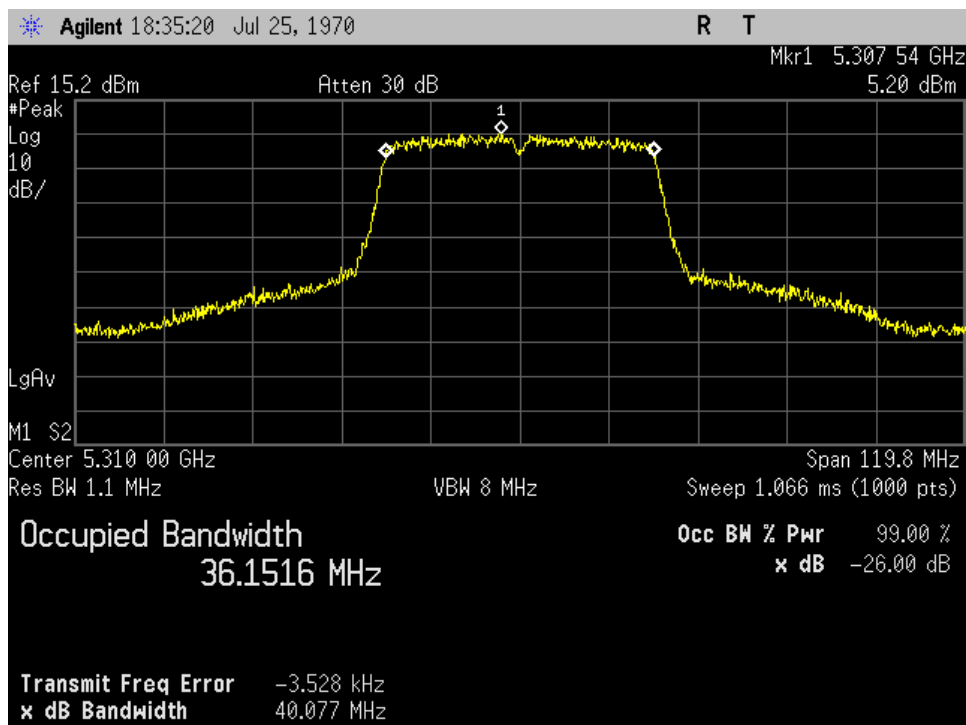
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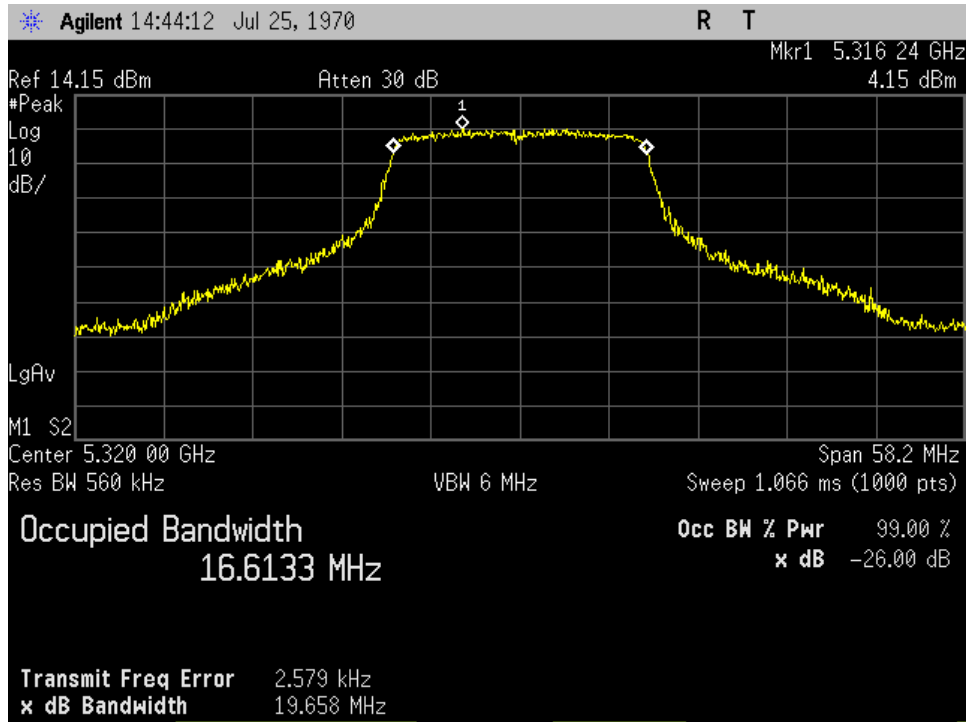
U-NII-2A_5310MHz_High Ch_62_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



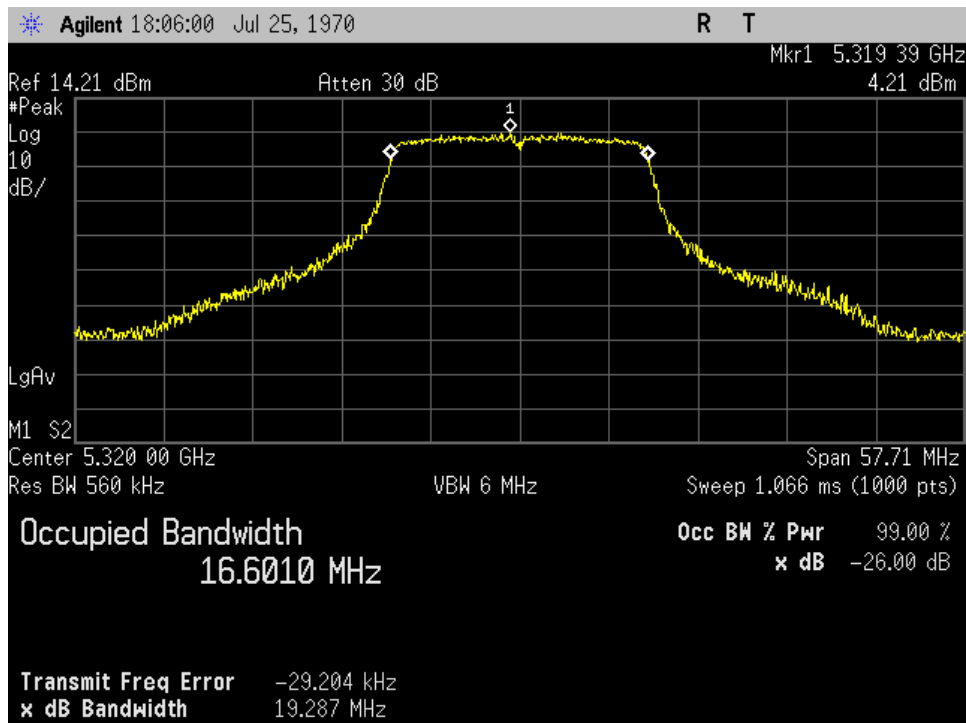
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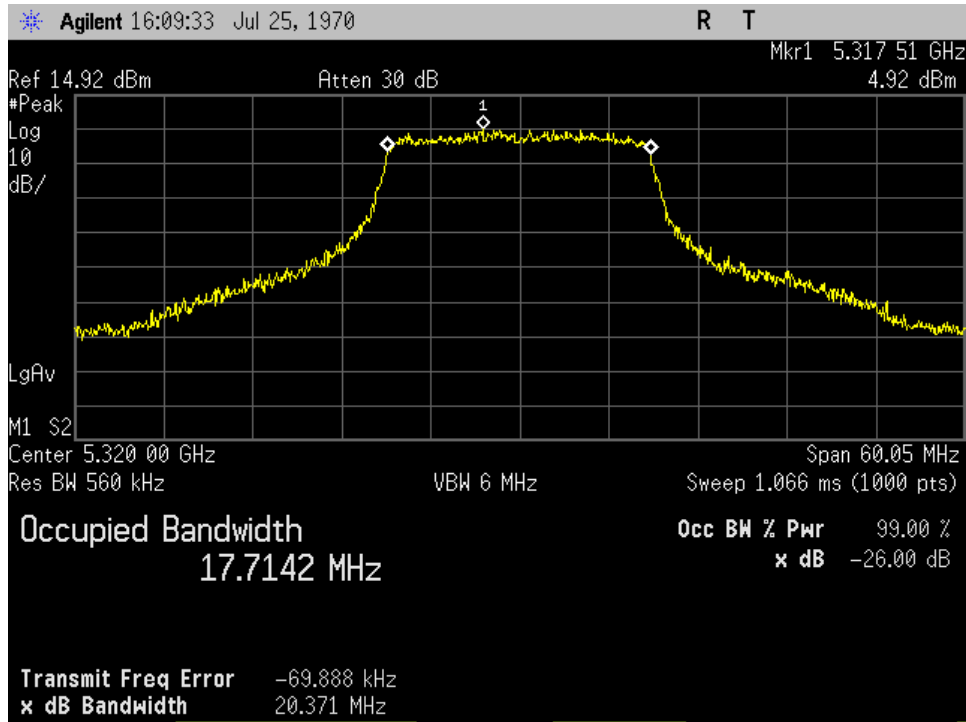
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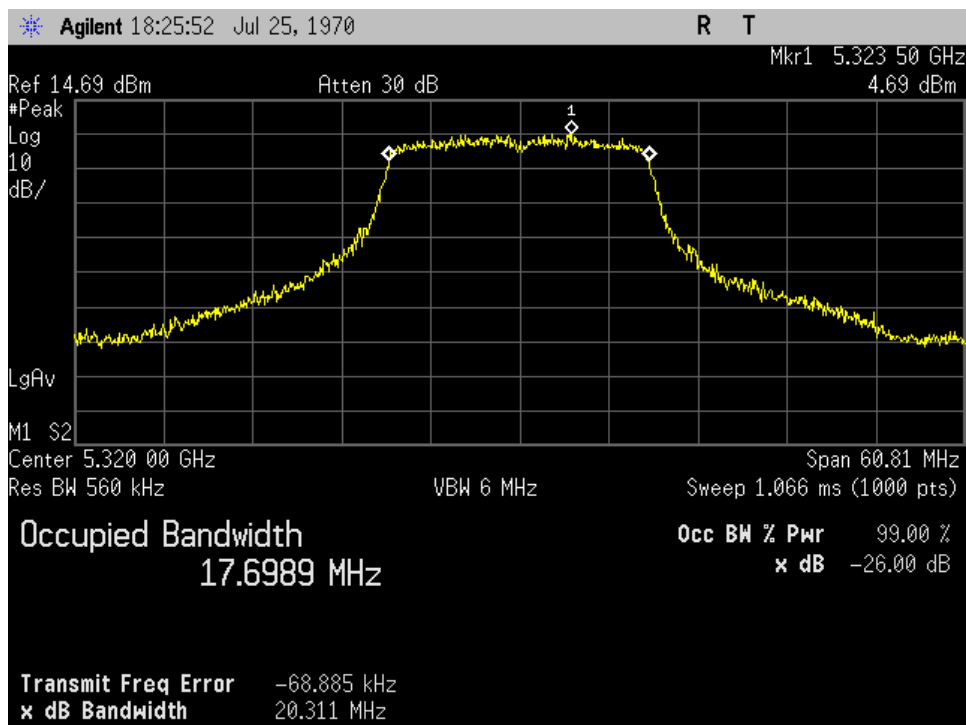
U-NII-2A_5320MHz_High Ch_64_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2



U-NII-2A_5320MHz_High Ch_64_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1

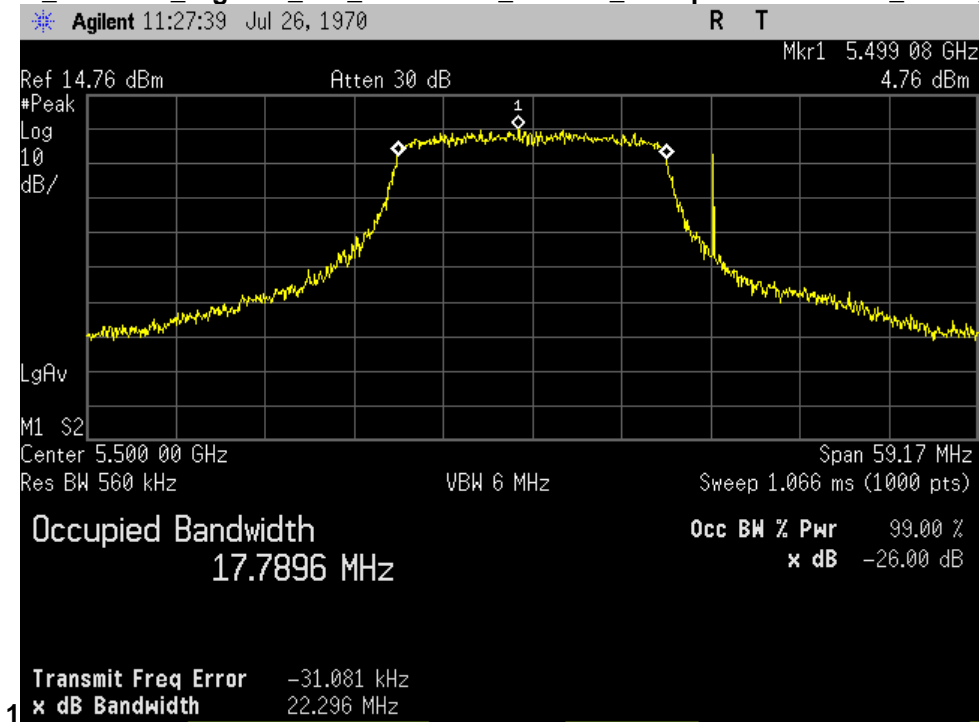


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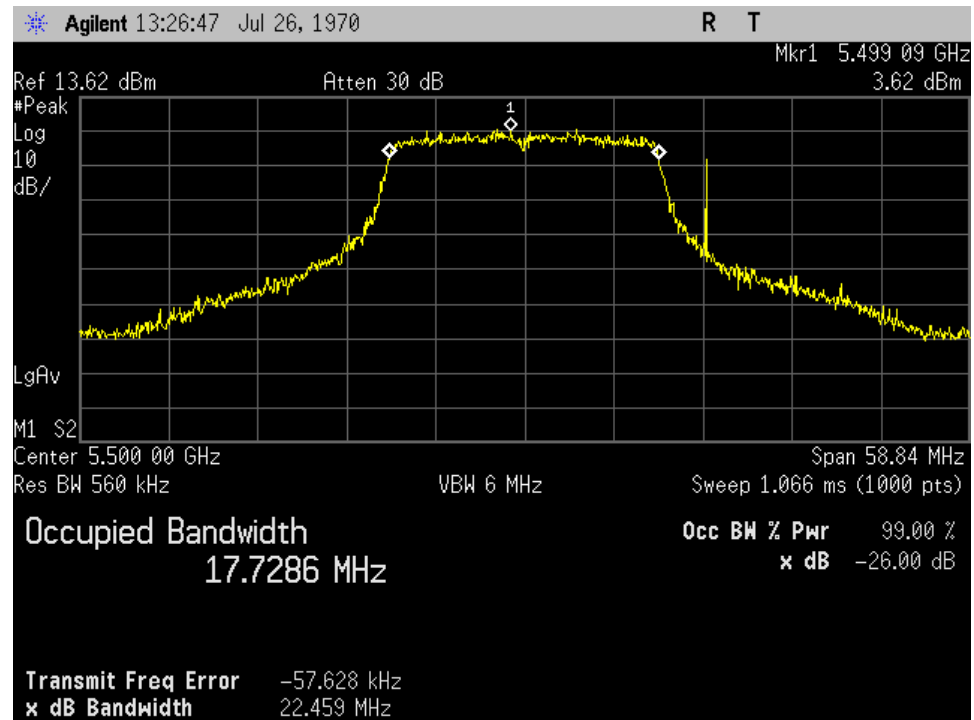


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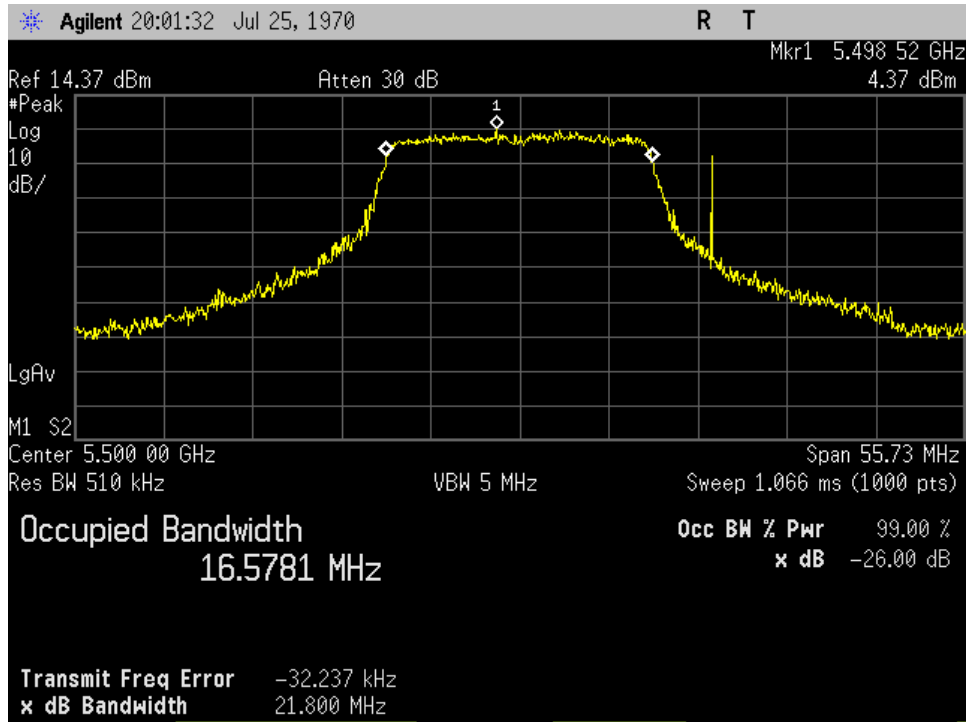
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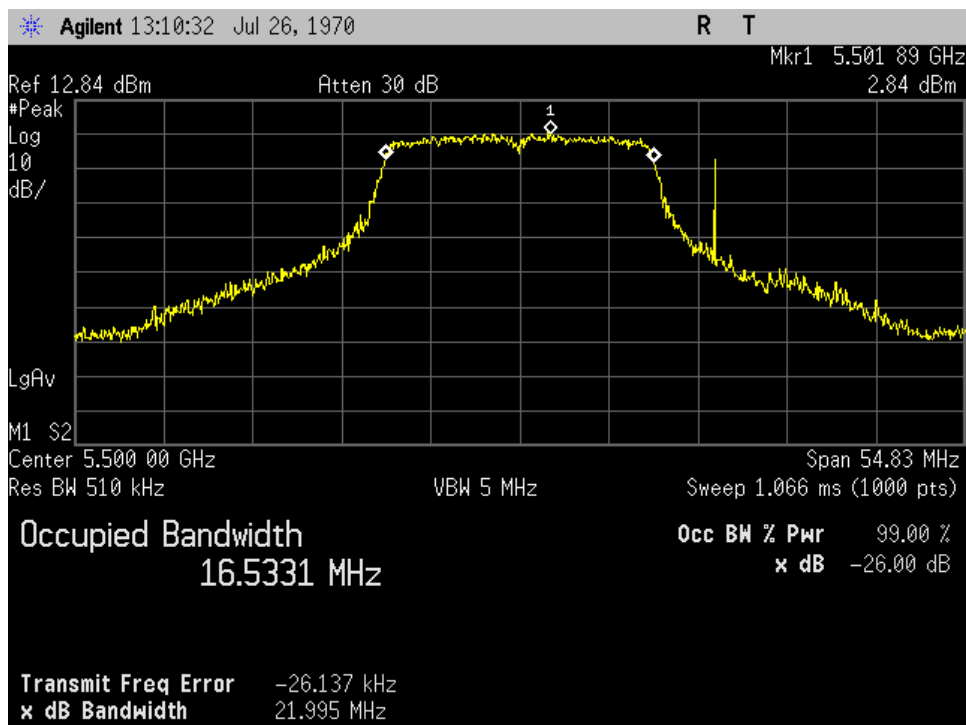
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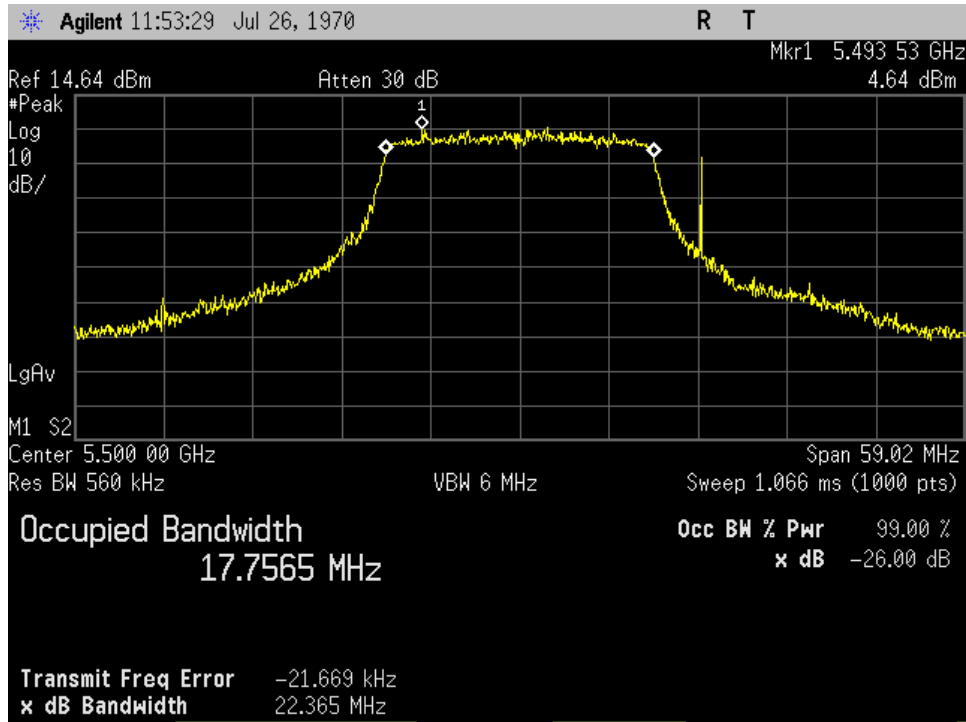
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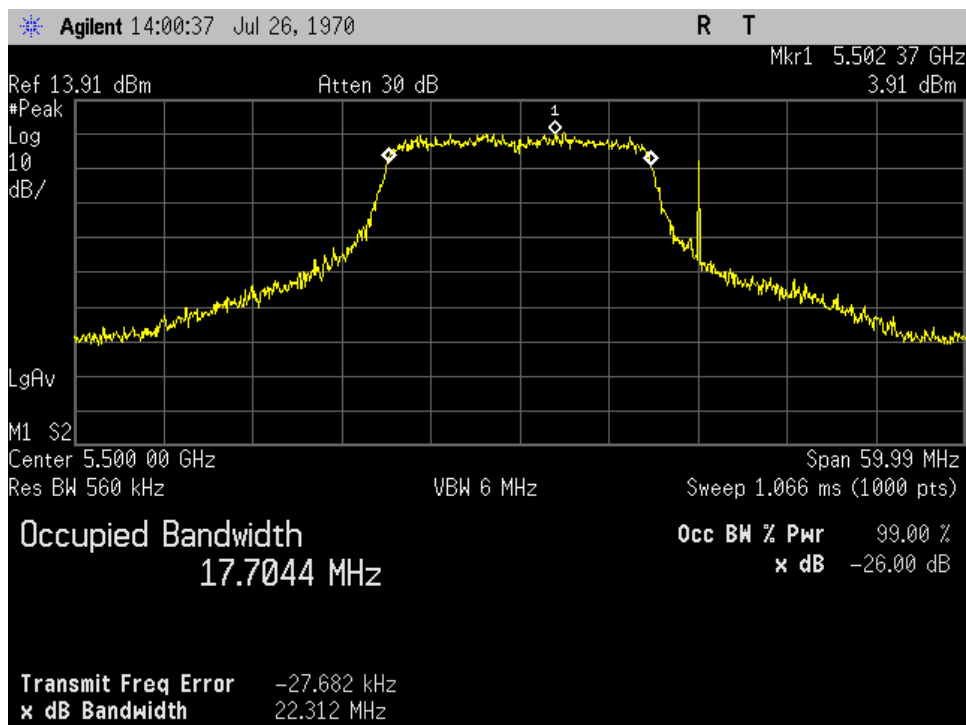
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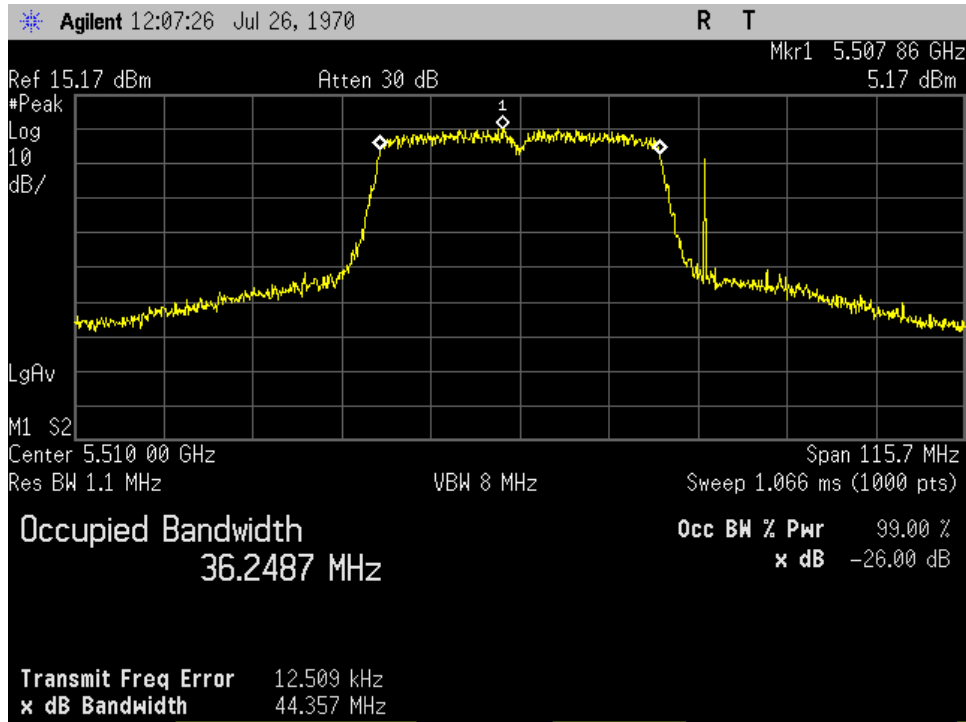
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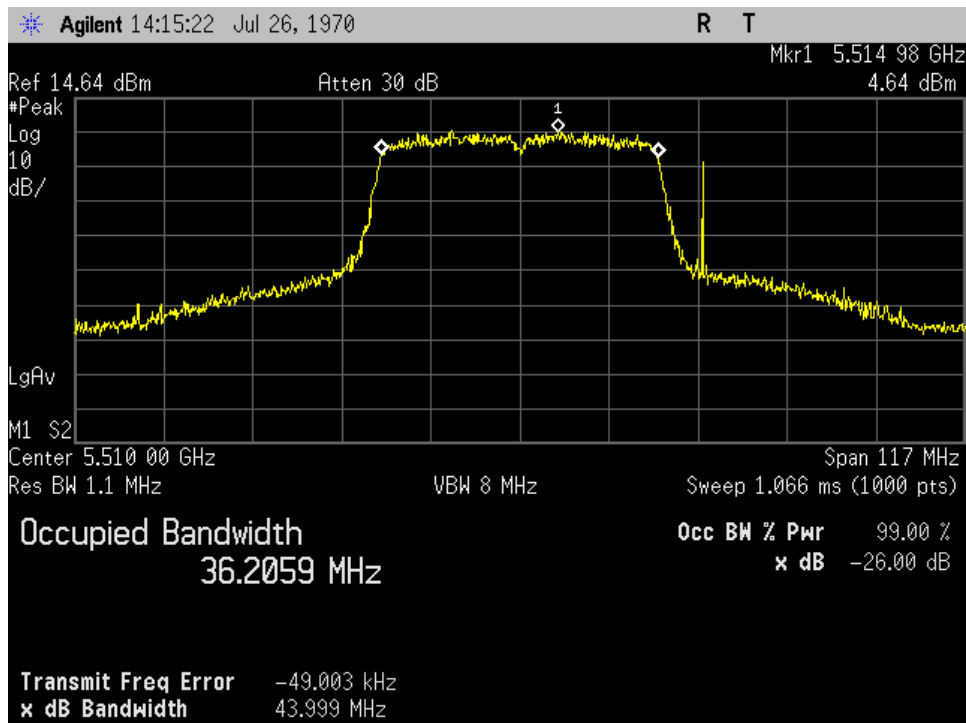
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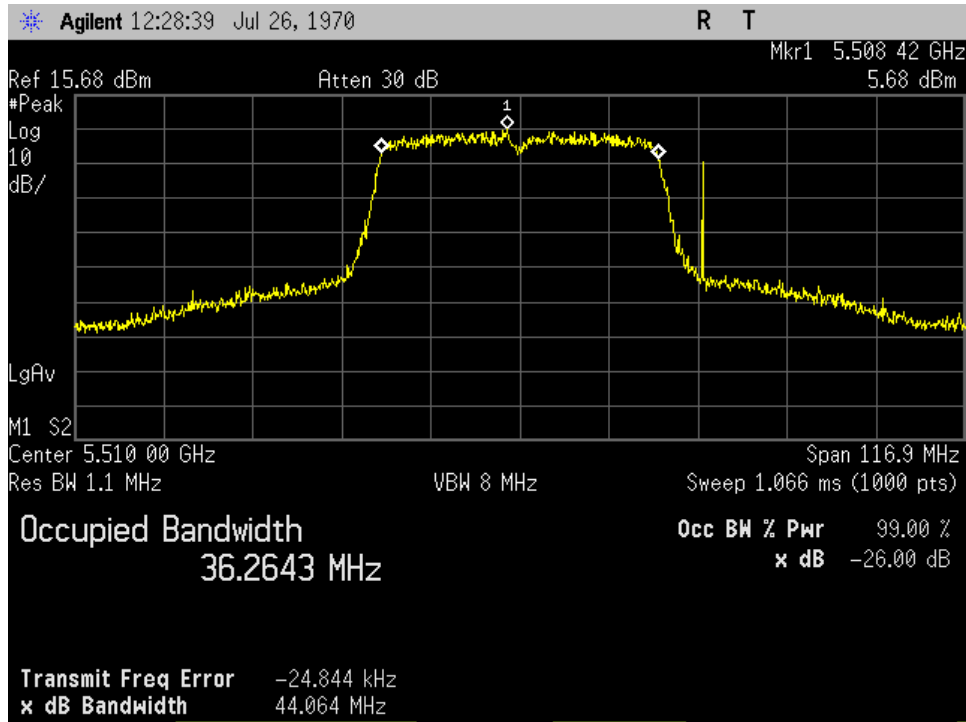
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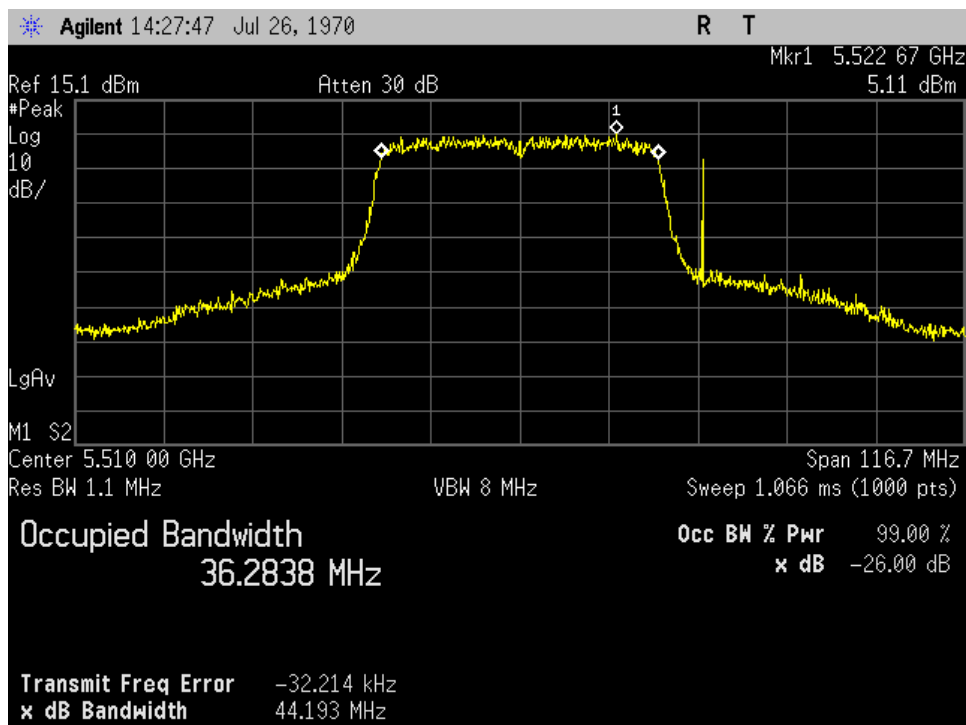
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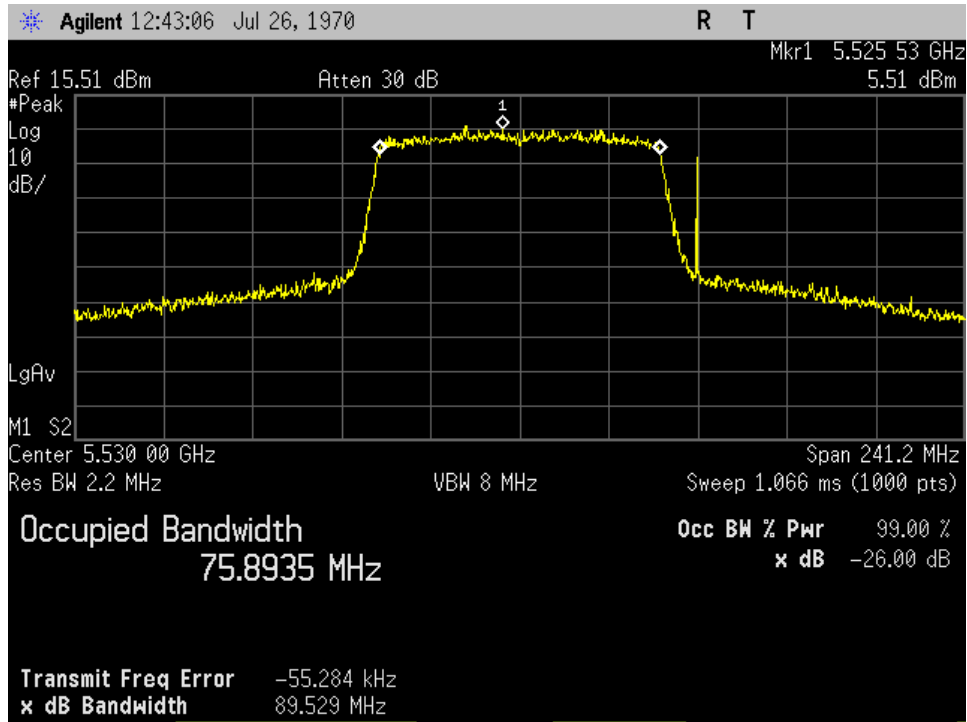
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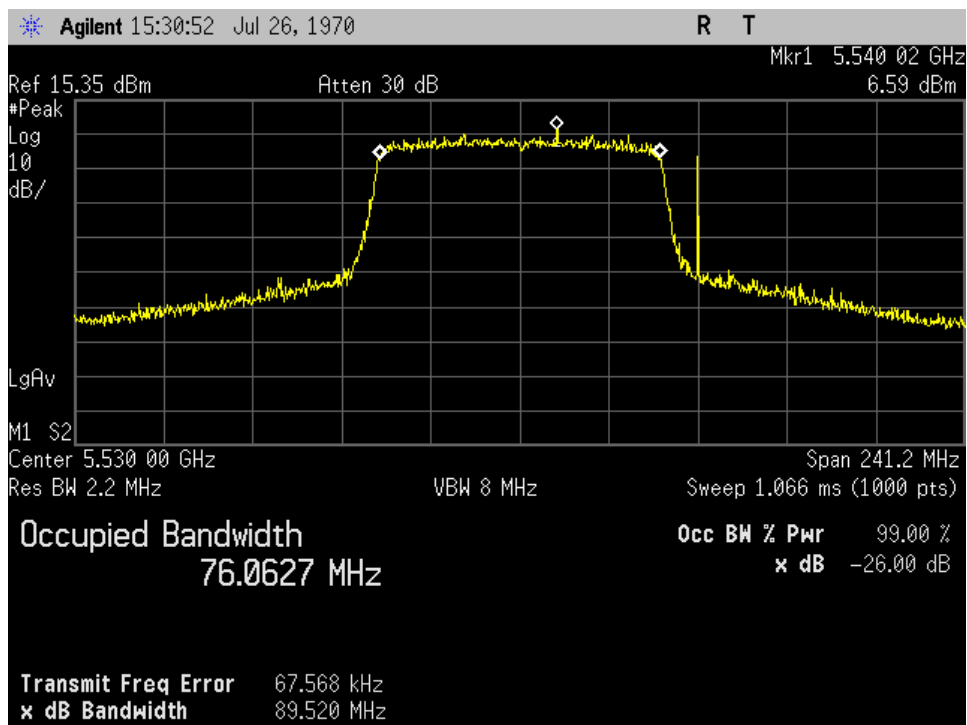
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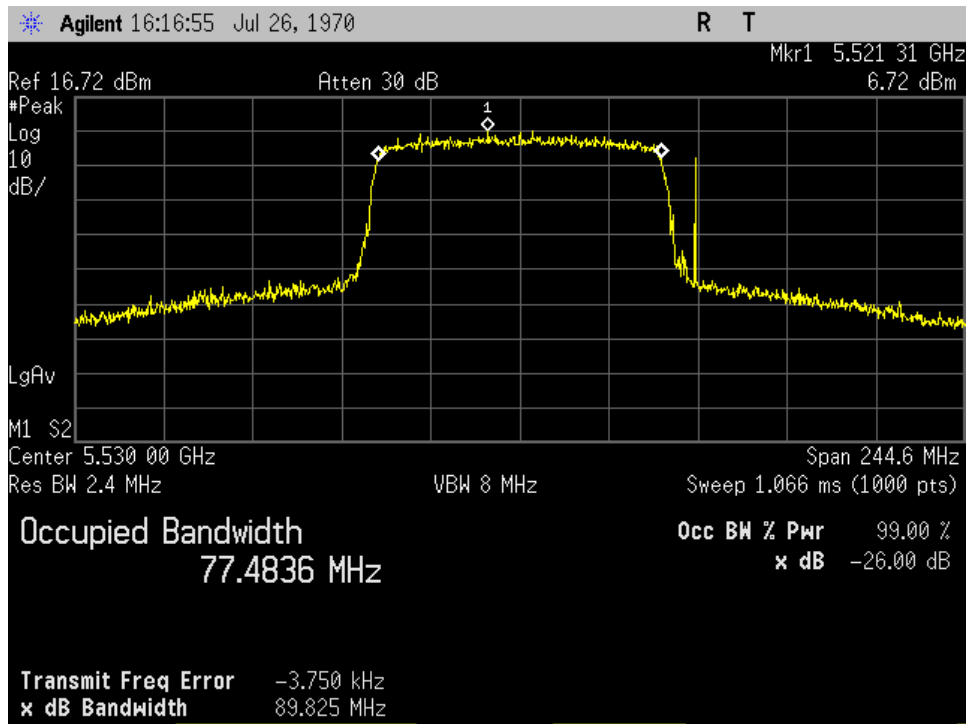
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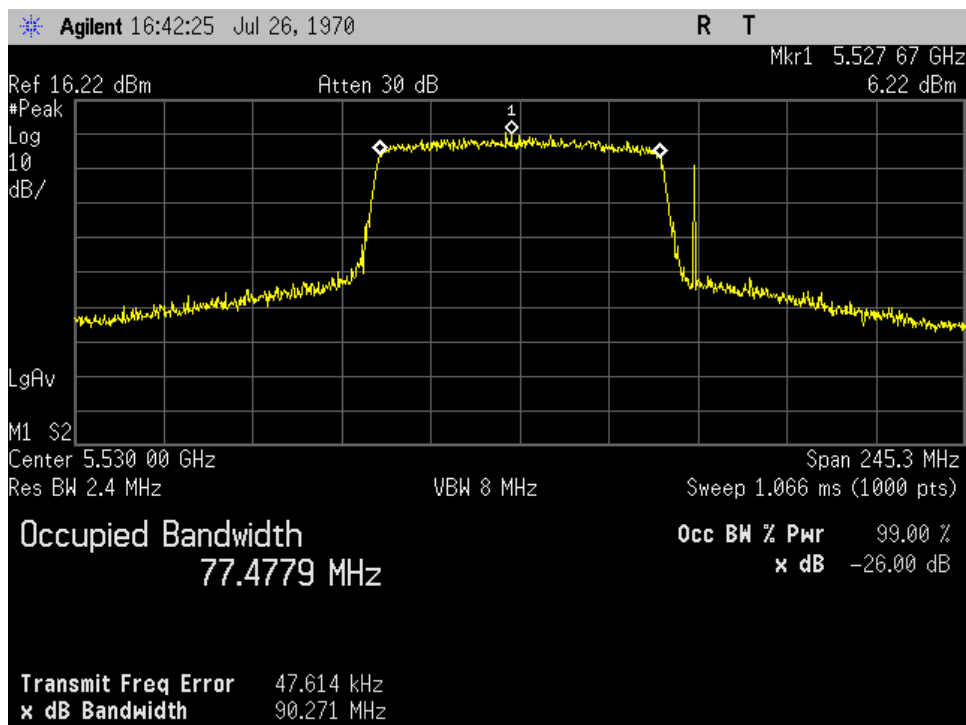
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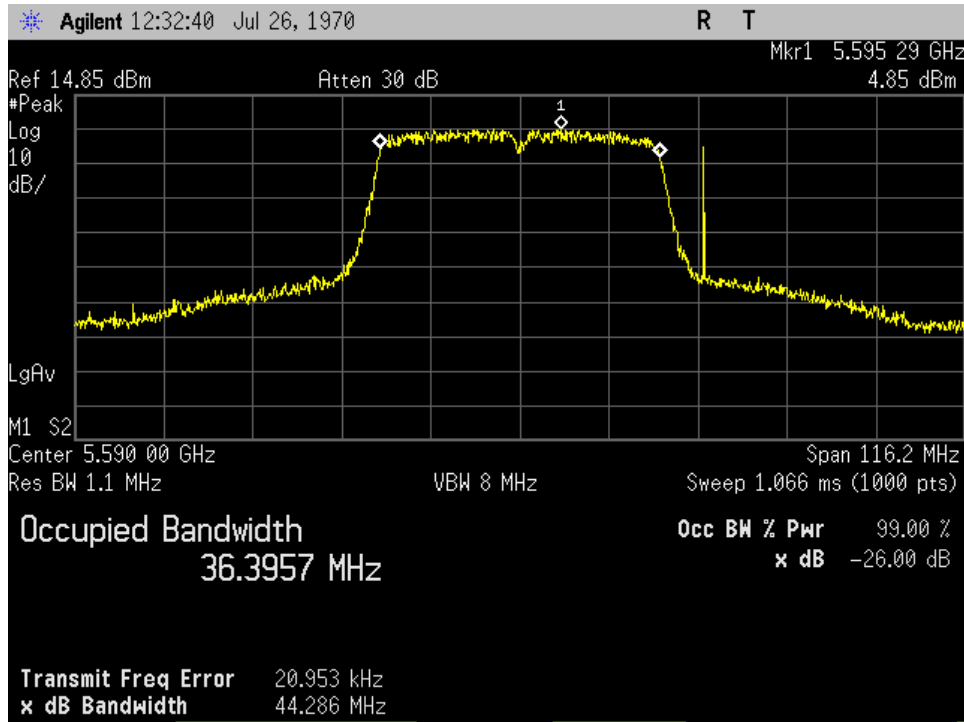
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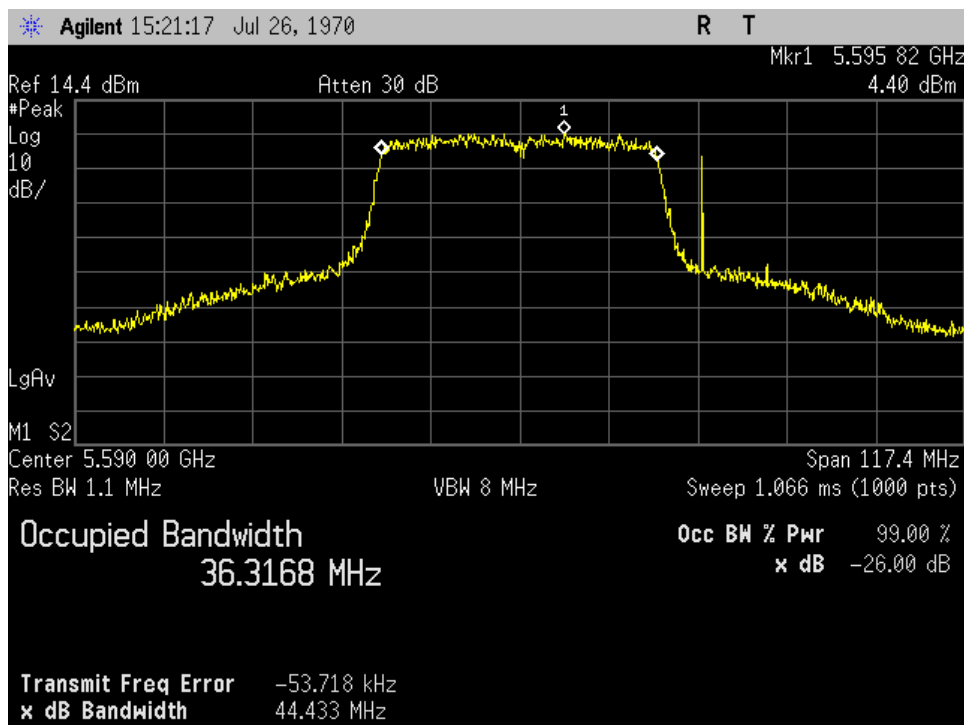
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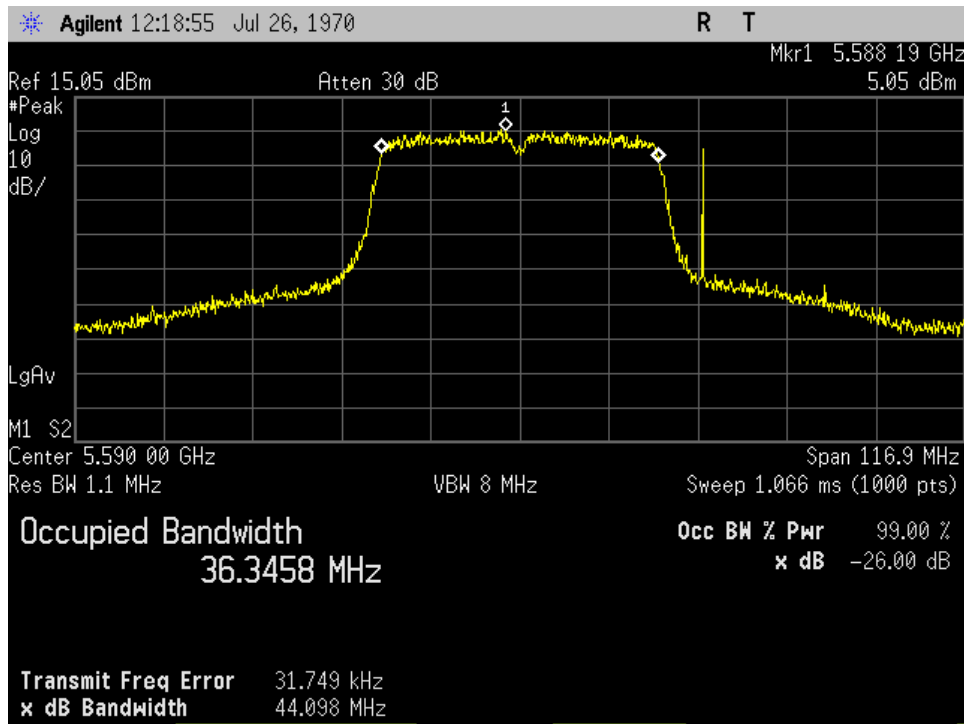
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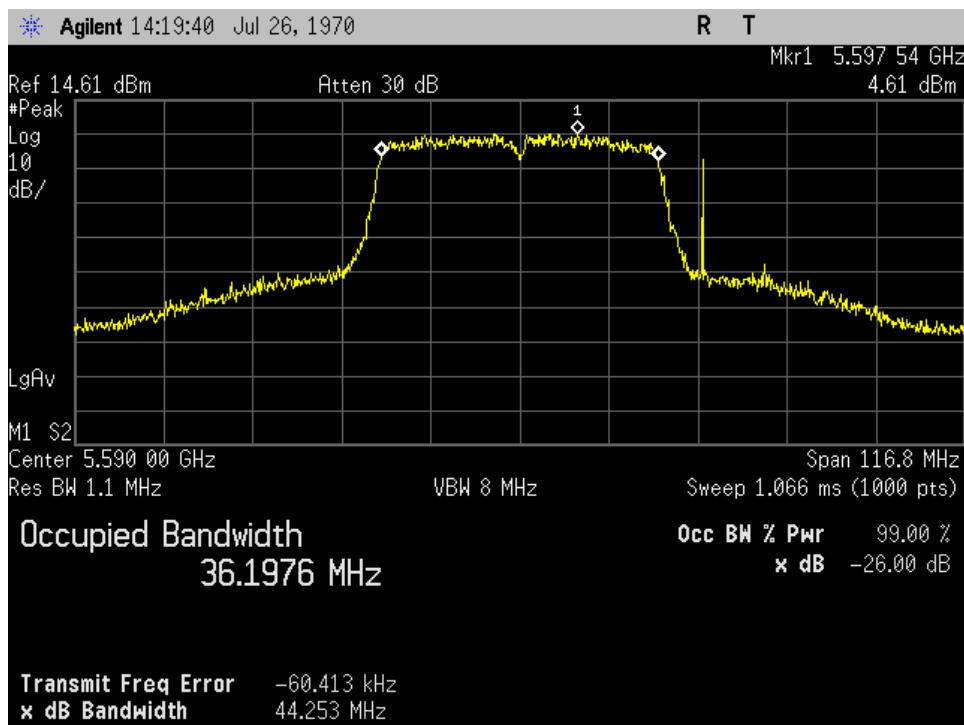
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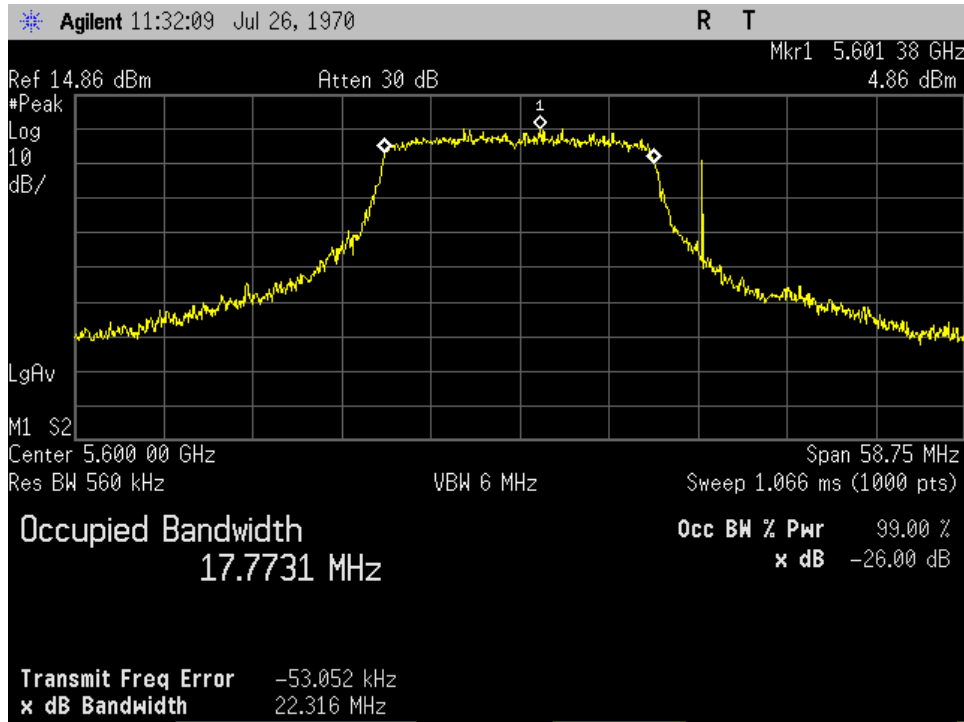
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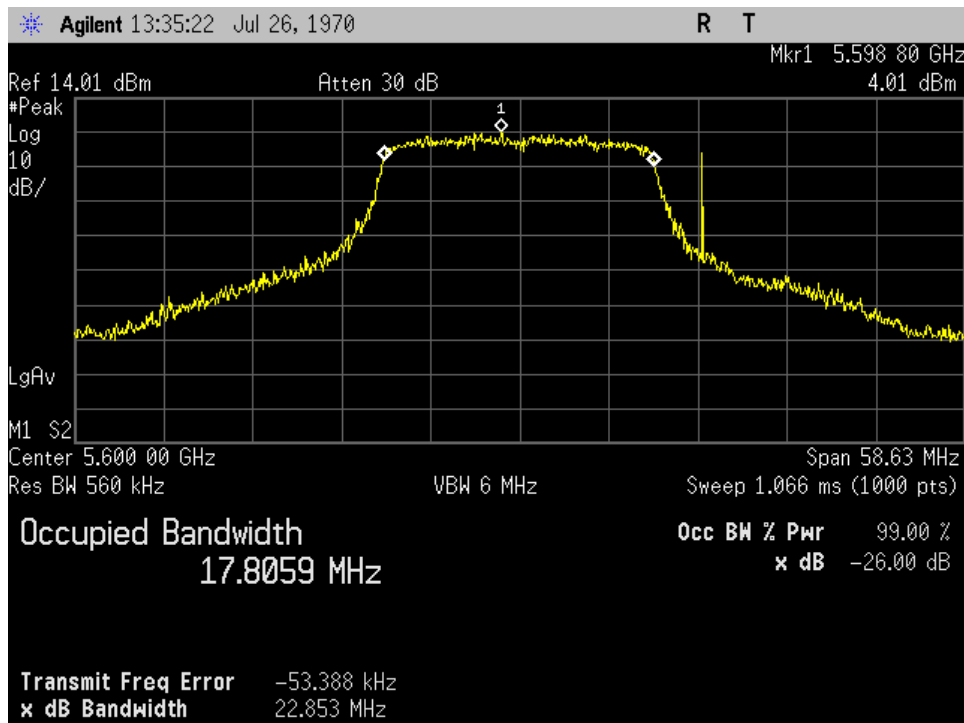
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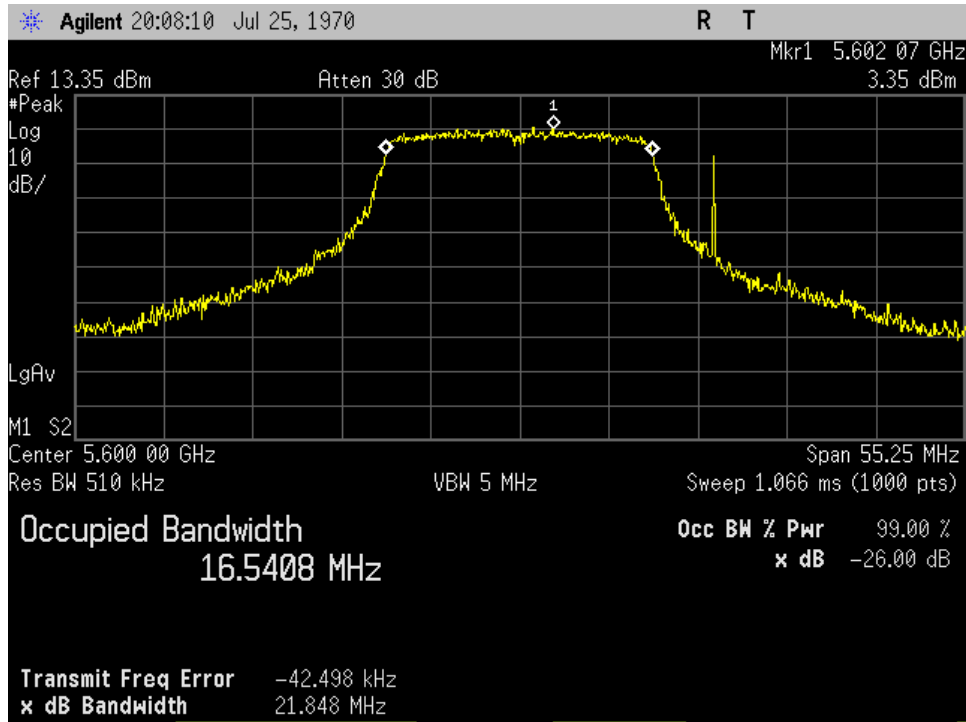
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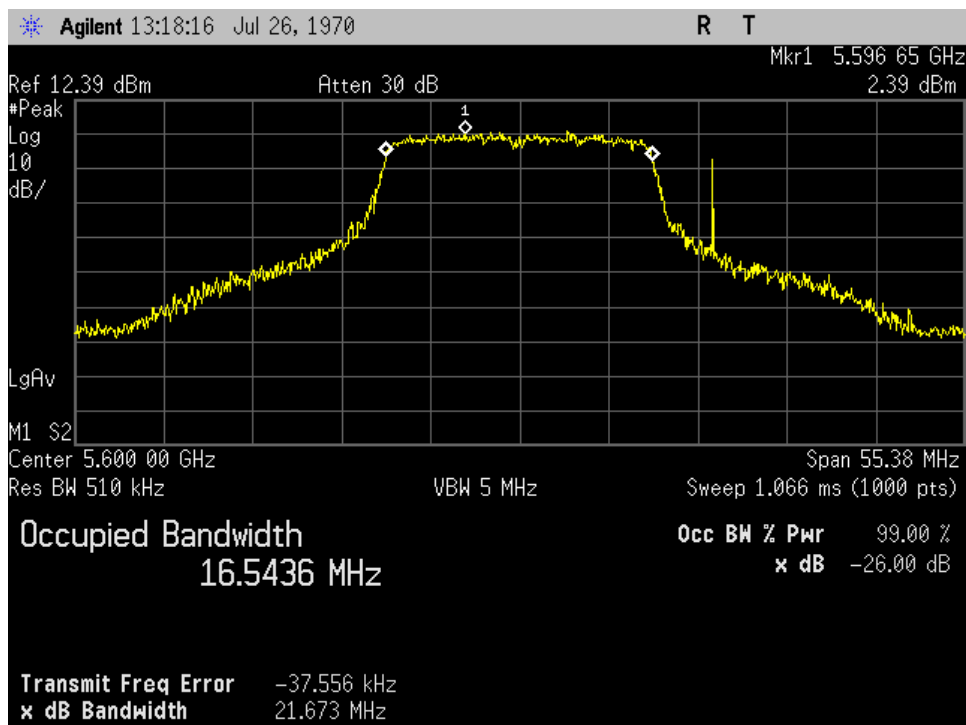
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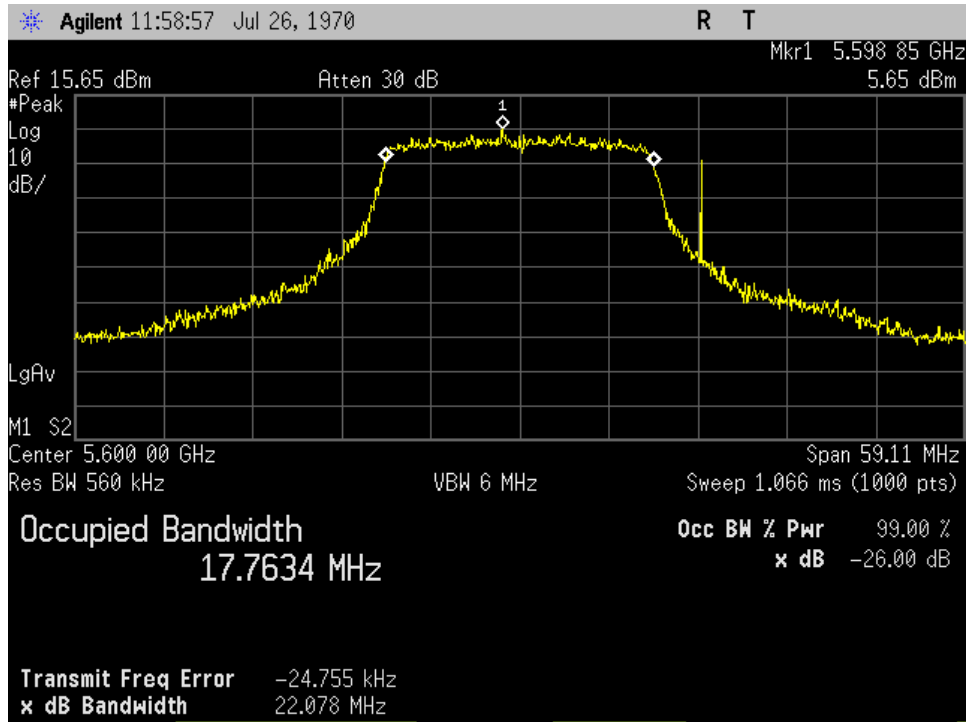
U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1



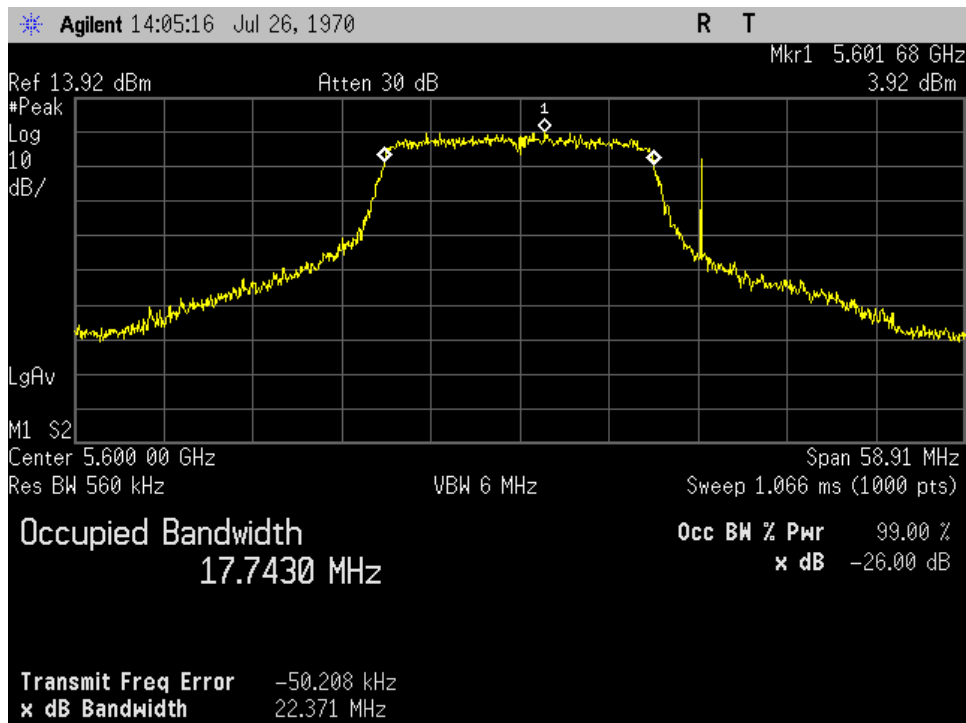
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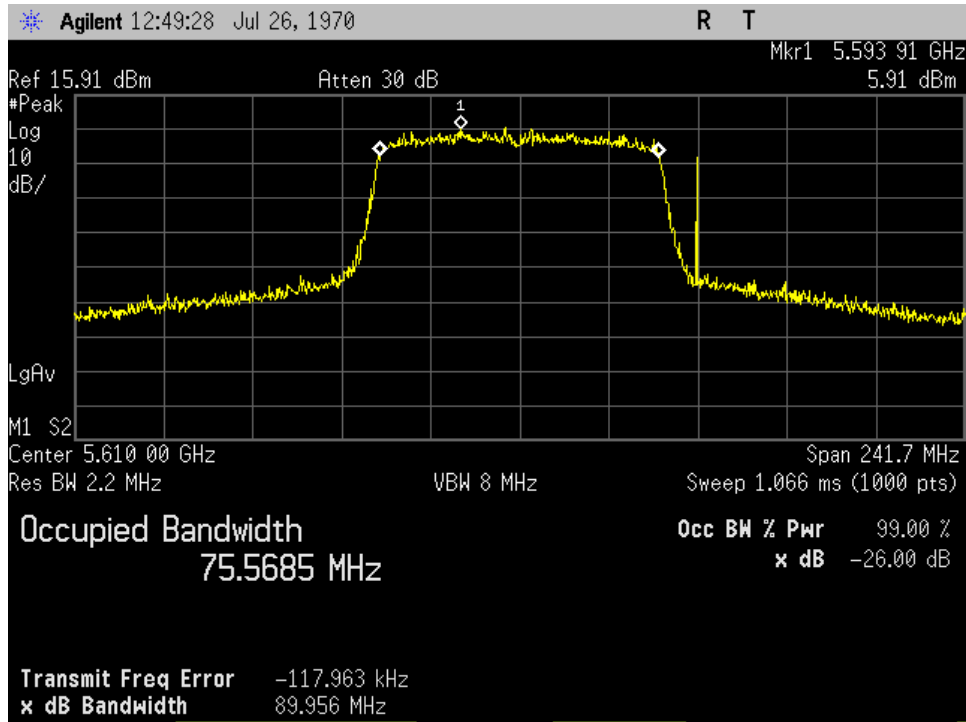
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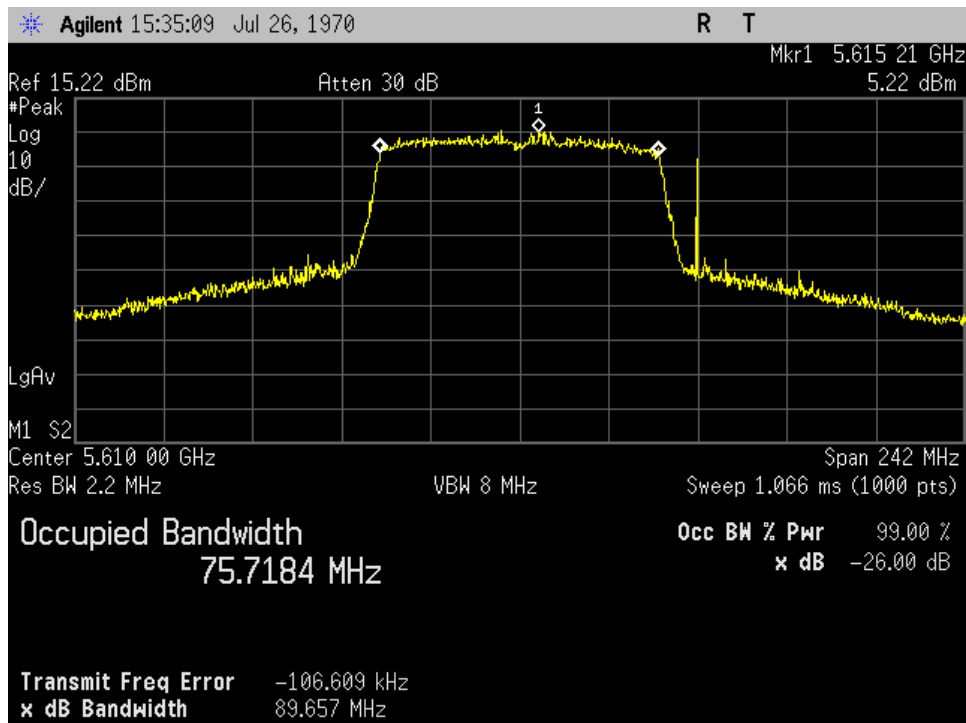
U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2



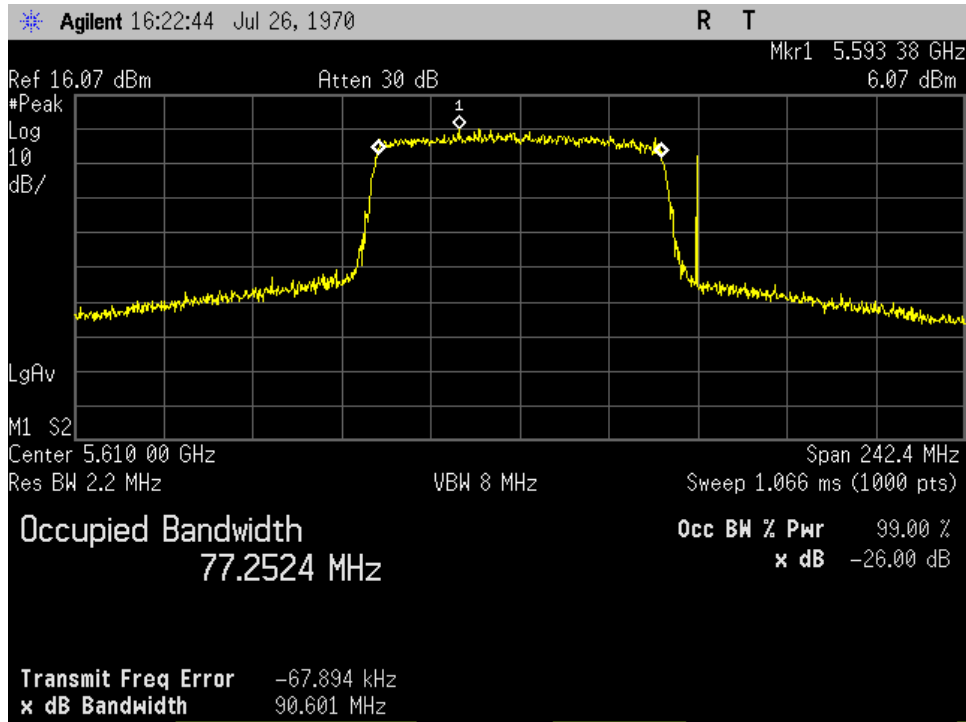
U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1



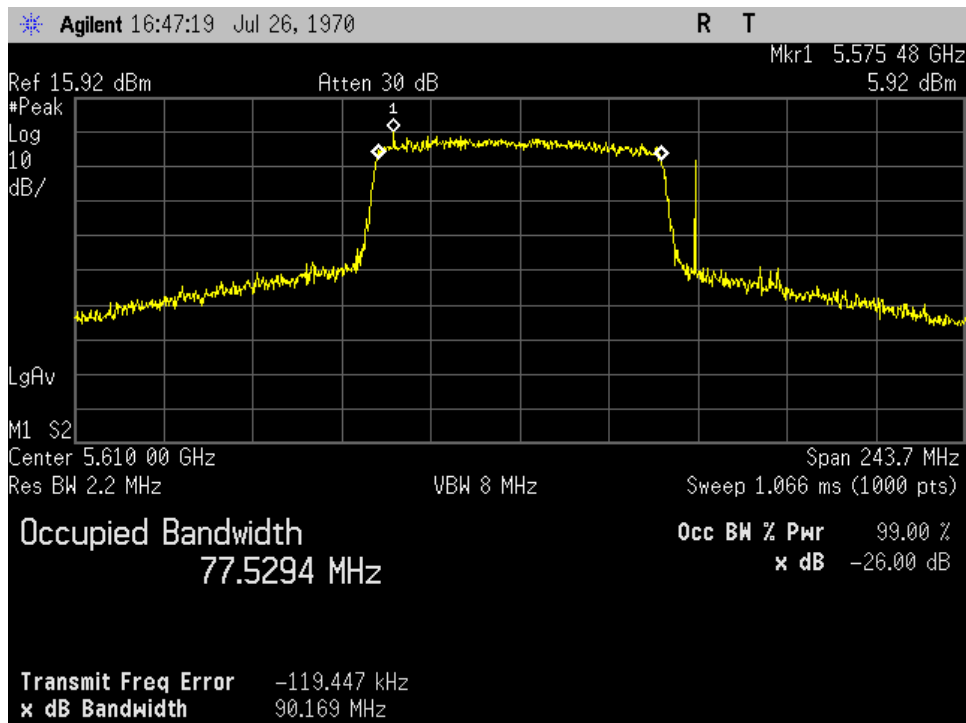
U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2



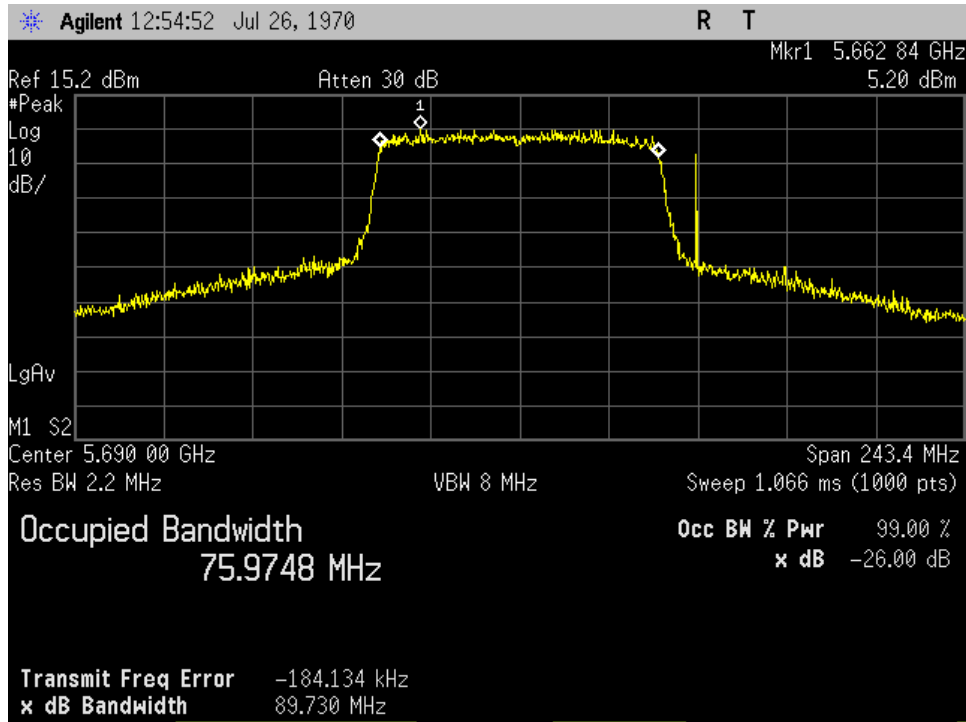
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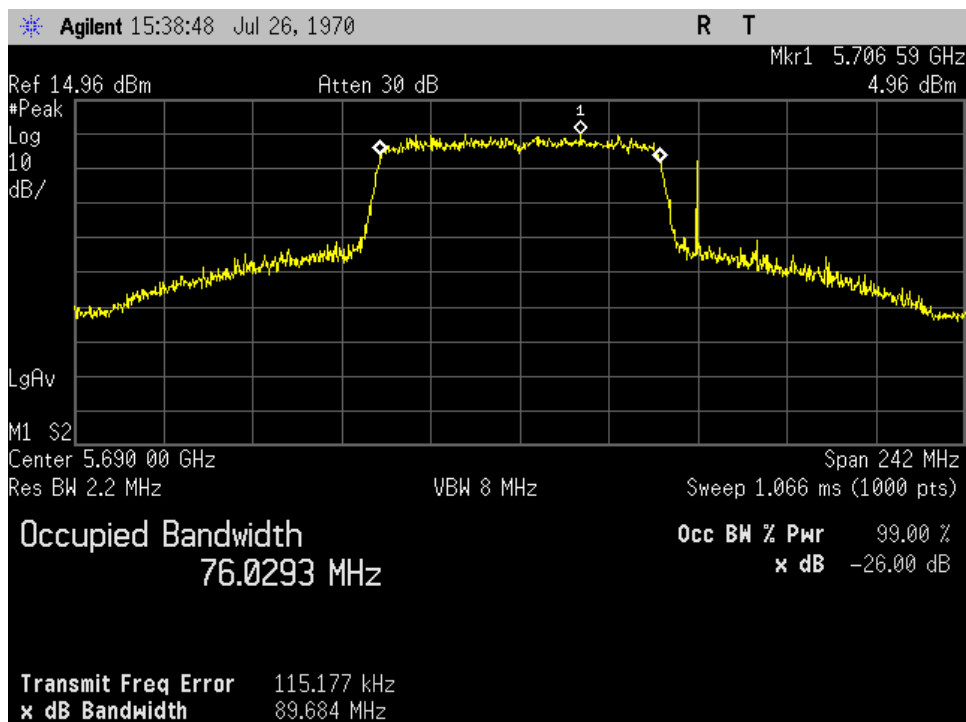
U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2



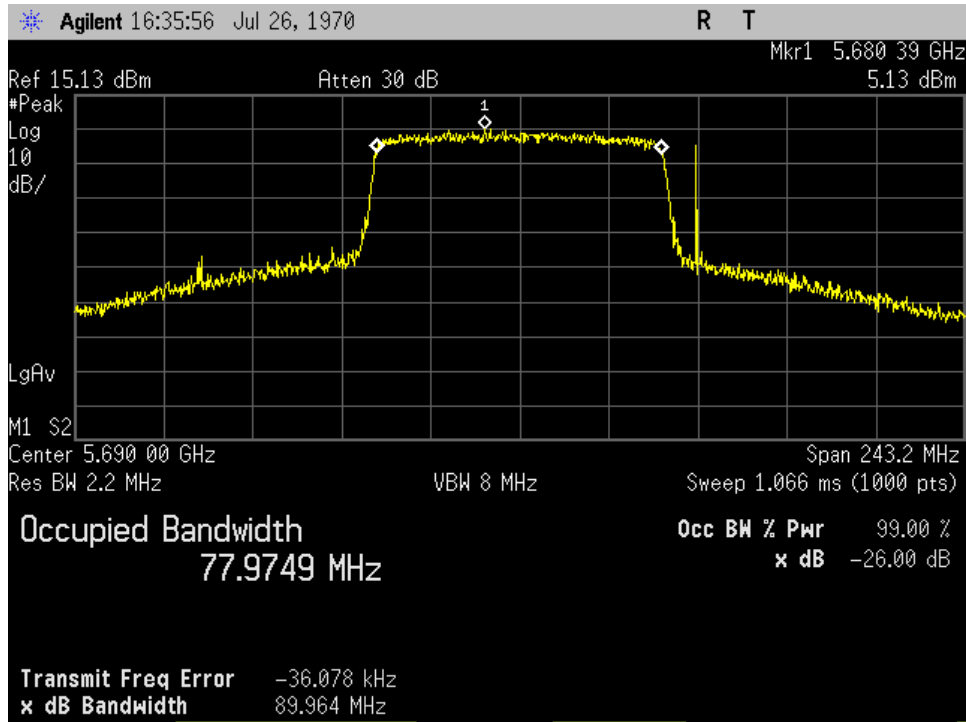
U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1



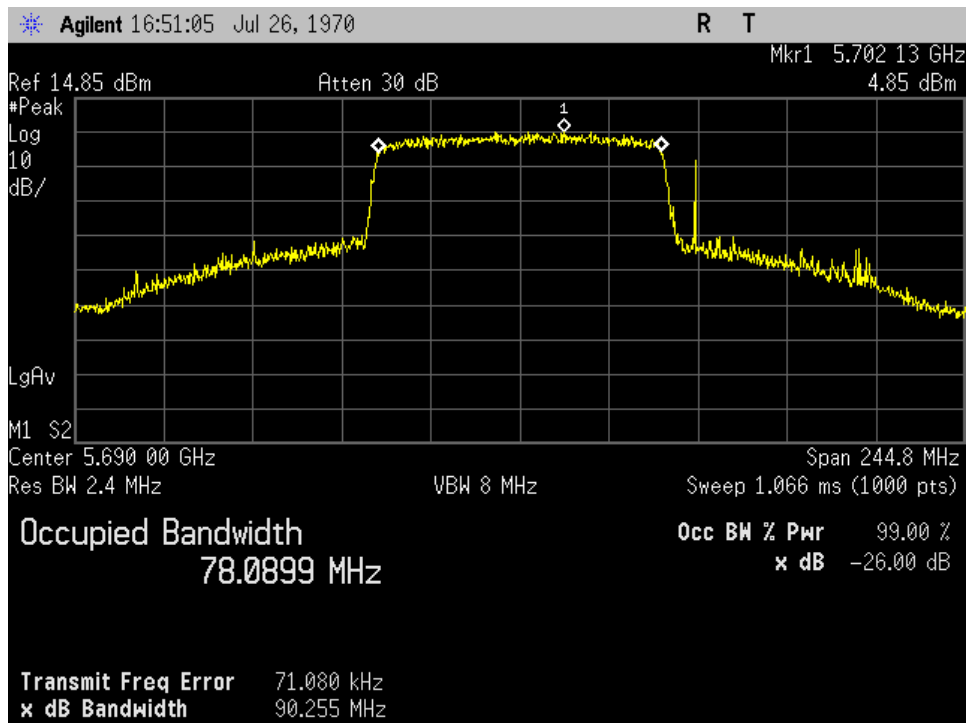
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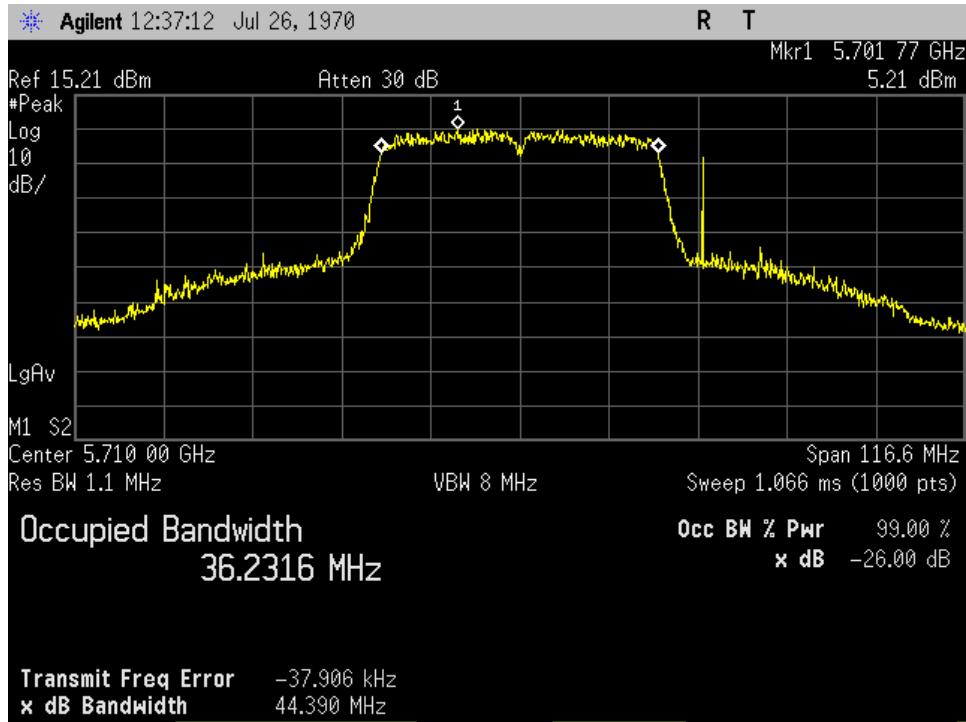
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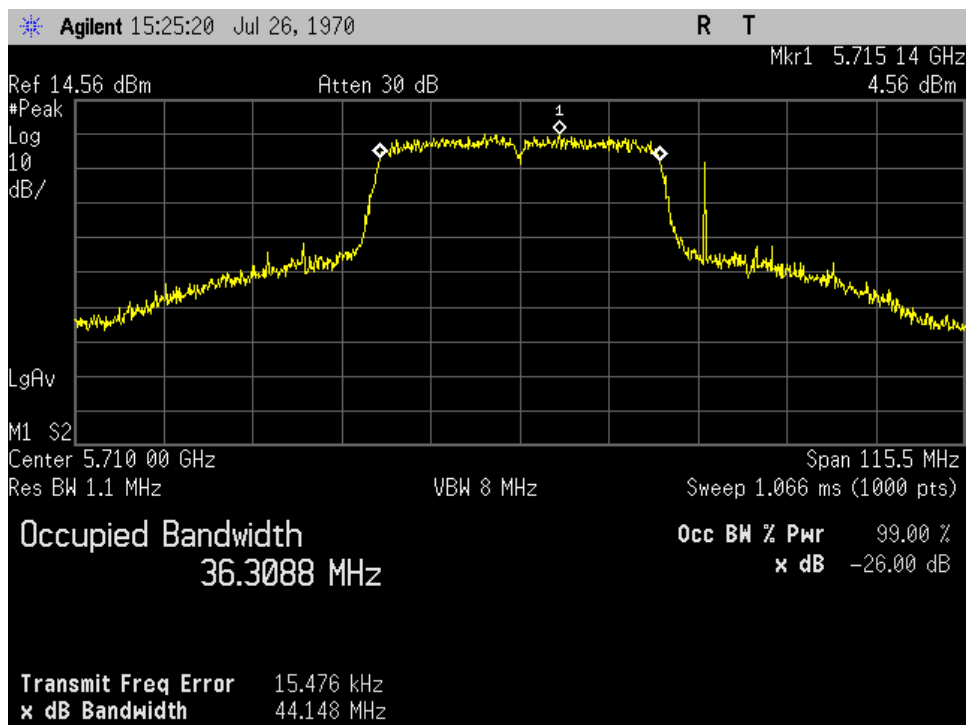
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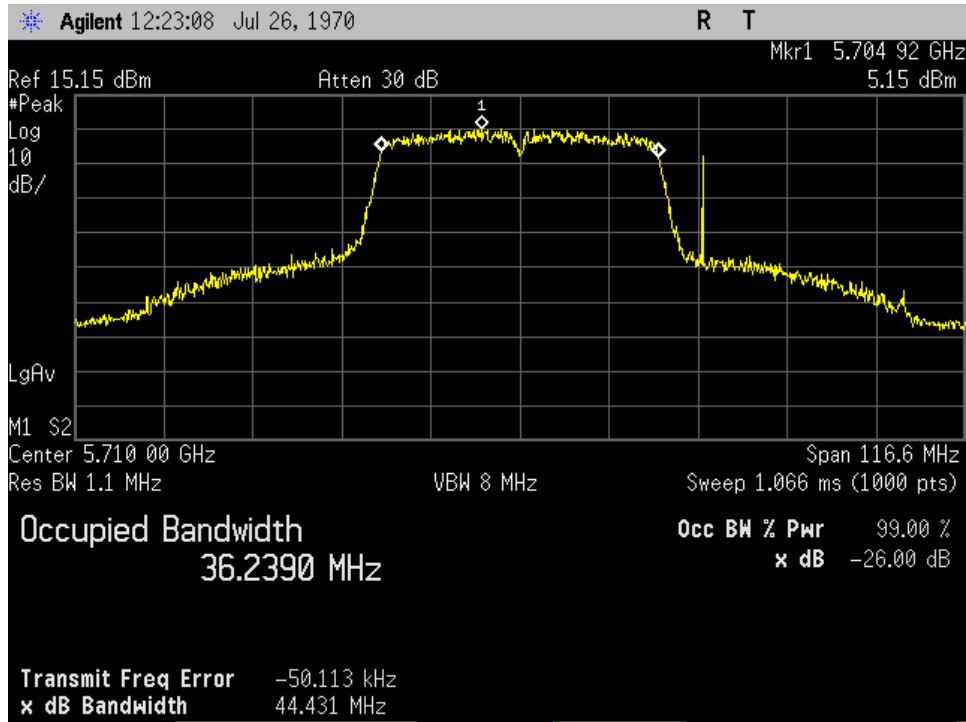
U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1



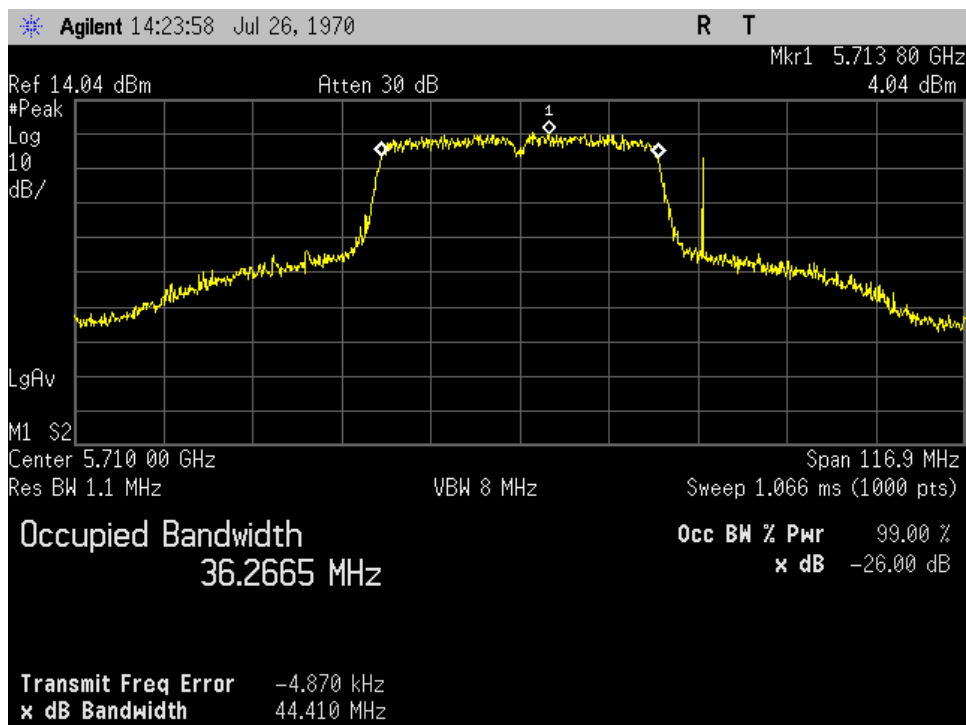
U-NII-2C_5710MHz_High Ch_142_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2



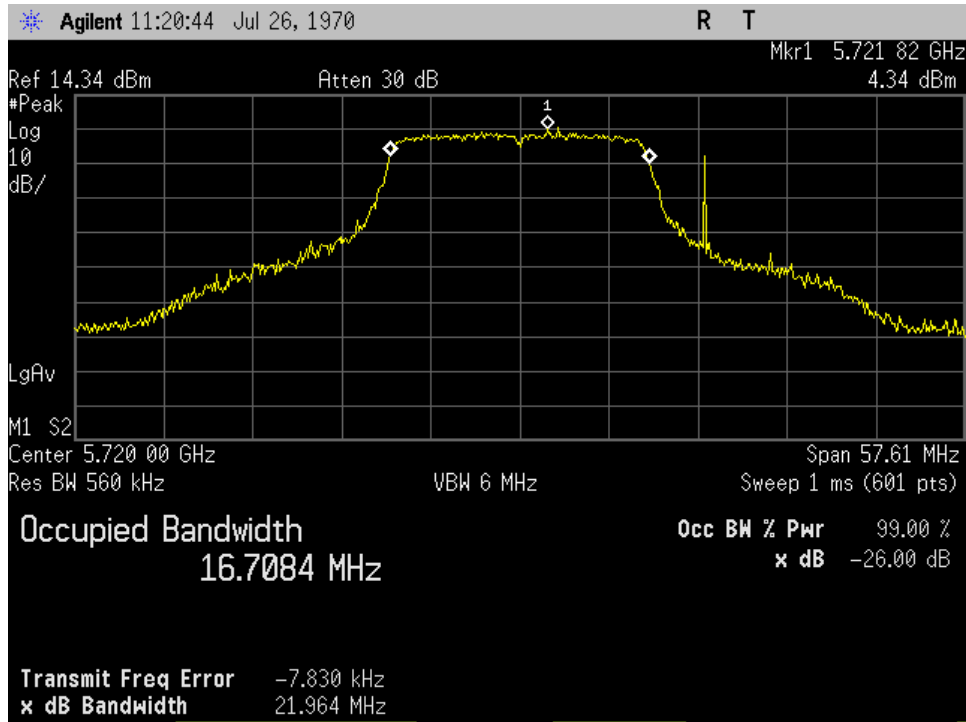
U-NII-2C_5710MHz_Low Ch_142_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



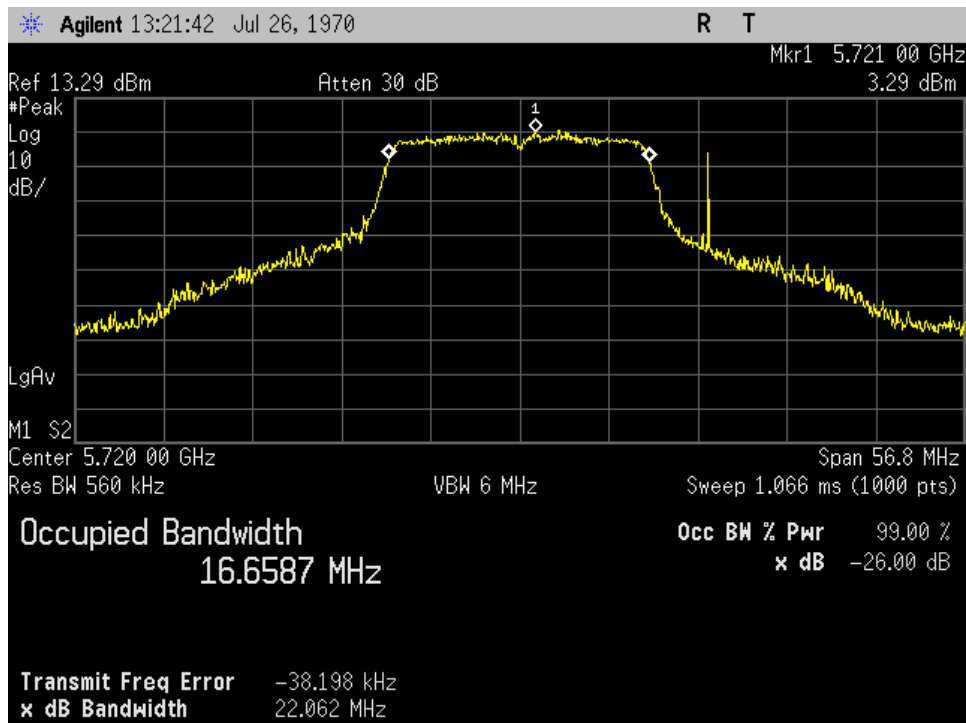
U-NII-2C_5710MHz_Low Ch_142_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2



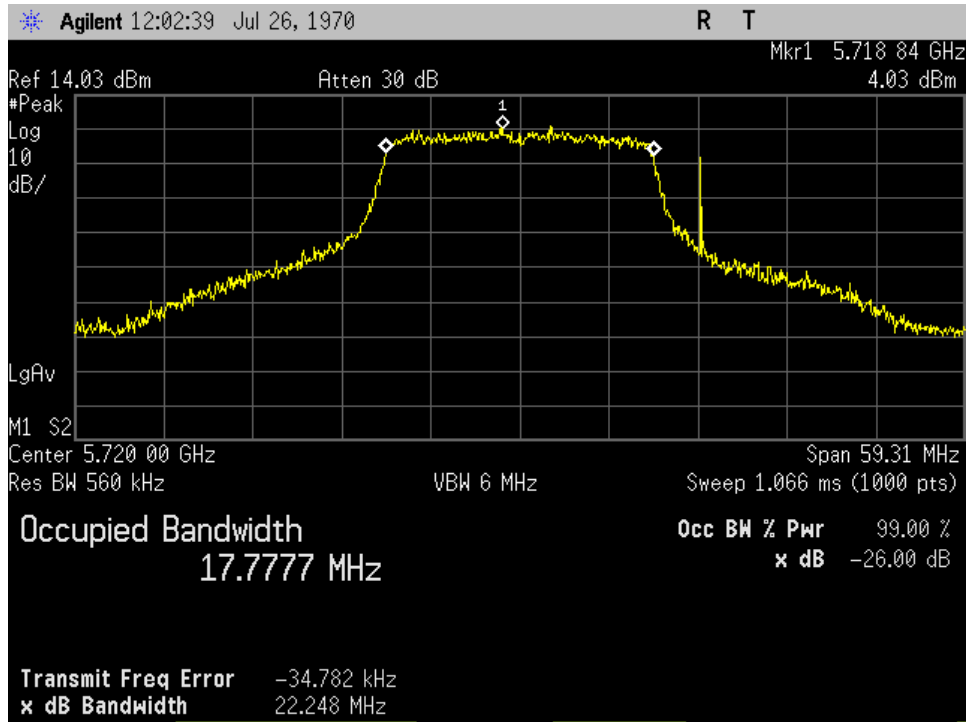
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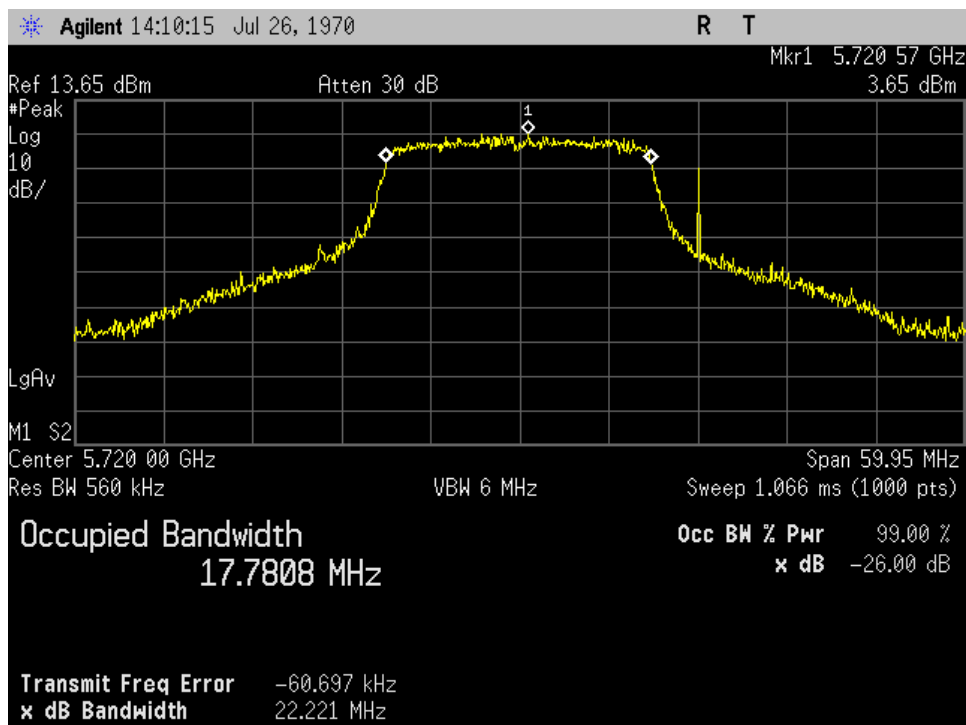
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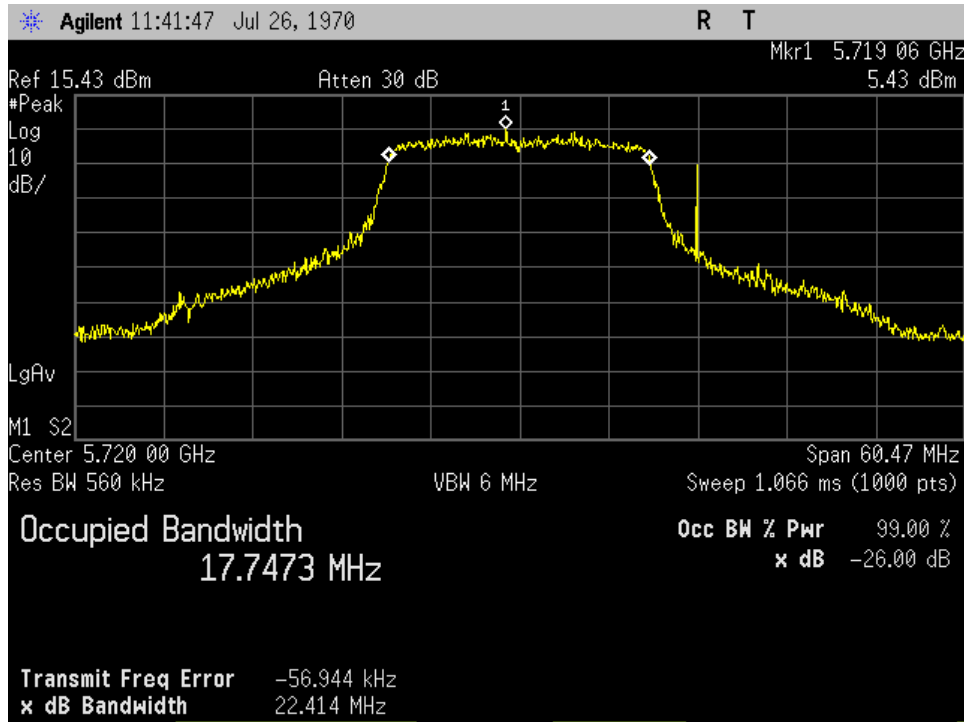
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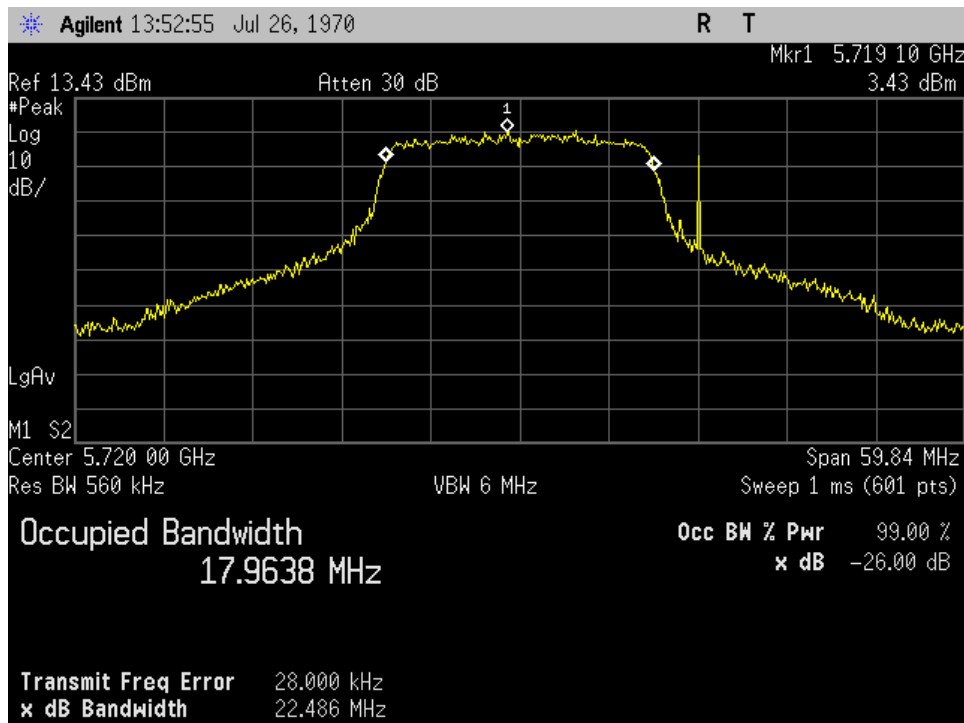
U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2



U-NII-2C_5720MHz_Low Ch_144_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1

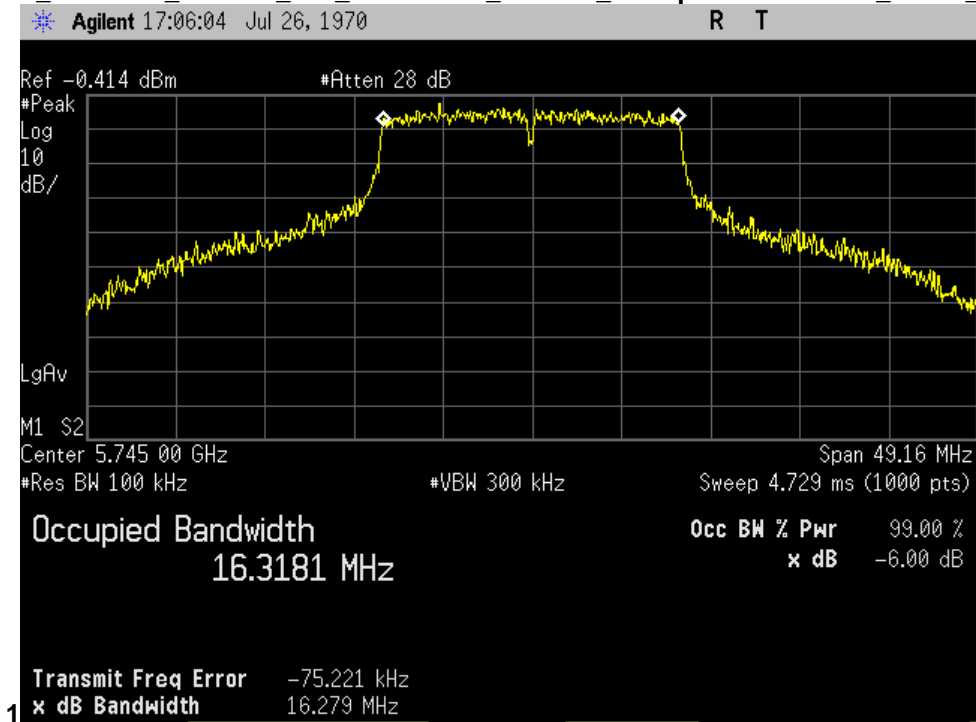


U-NII-2C_5720MHz_Low Ch_144_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2

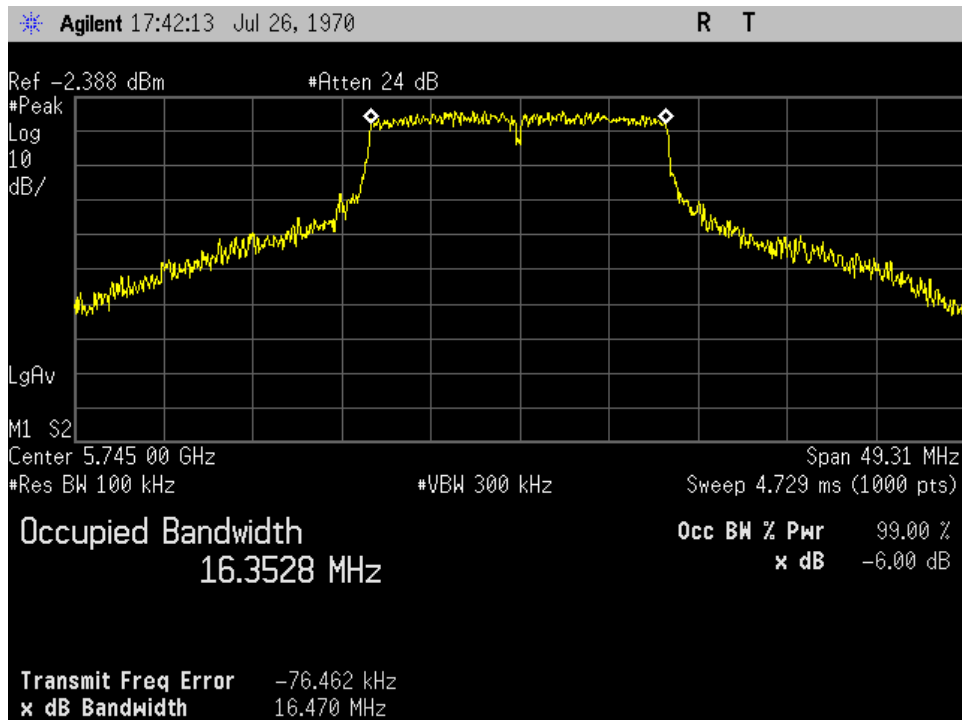


U-NII-3

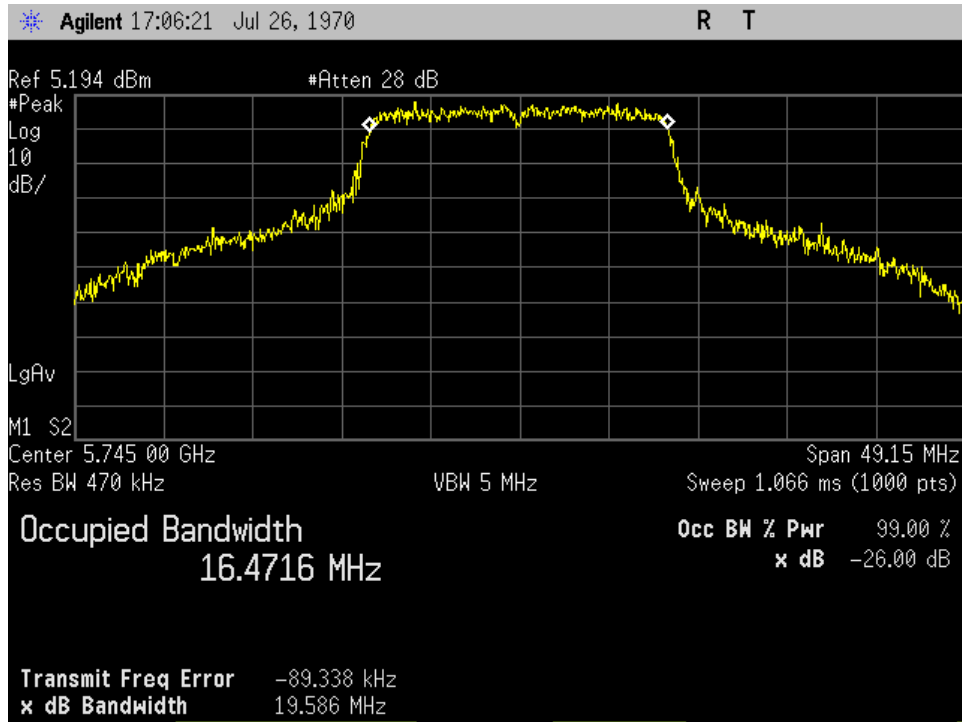
U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port



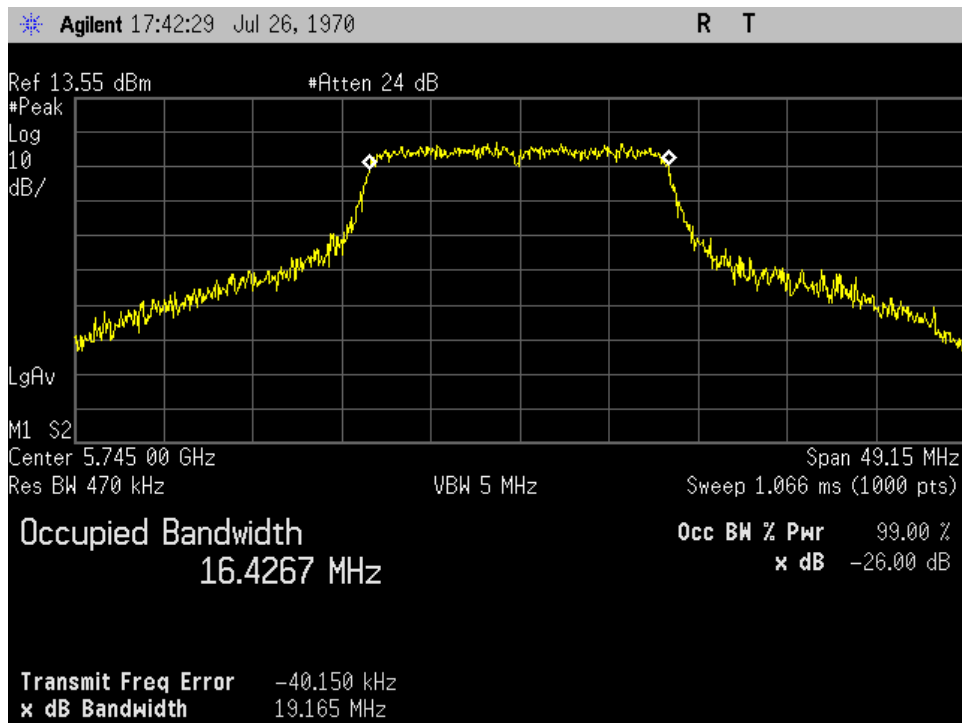
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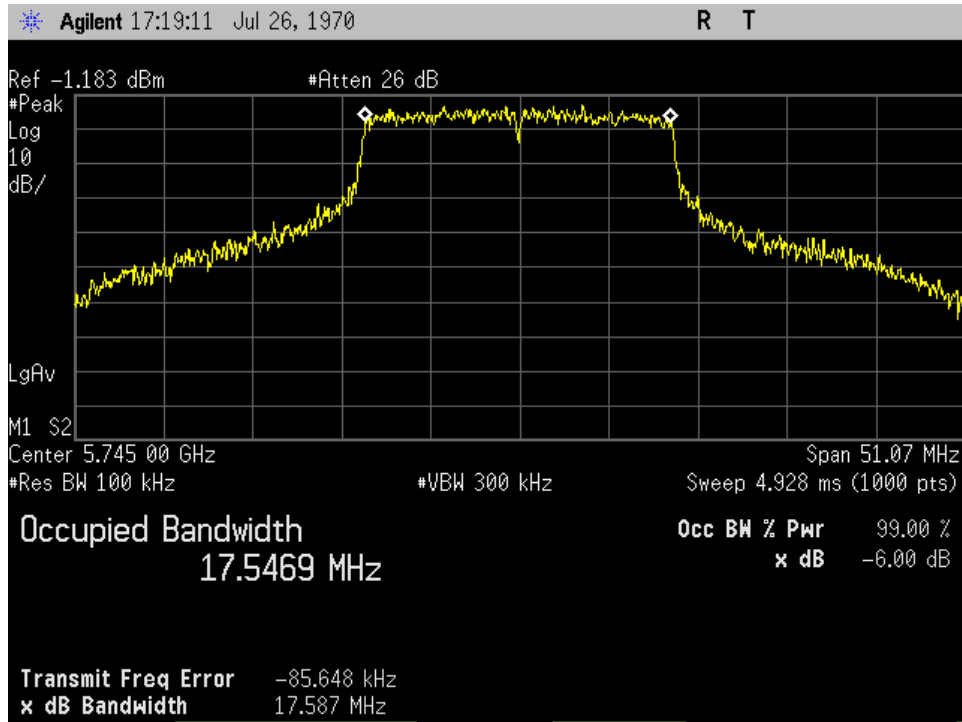
U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 1



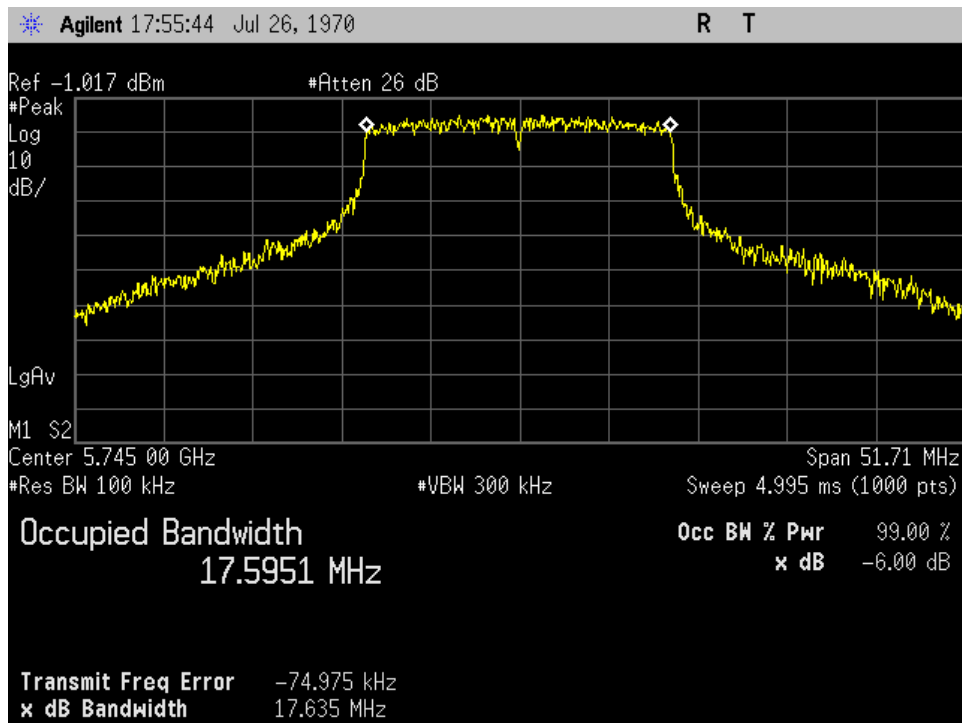
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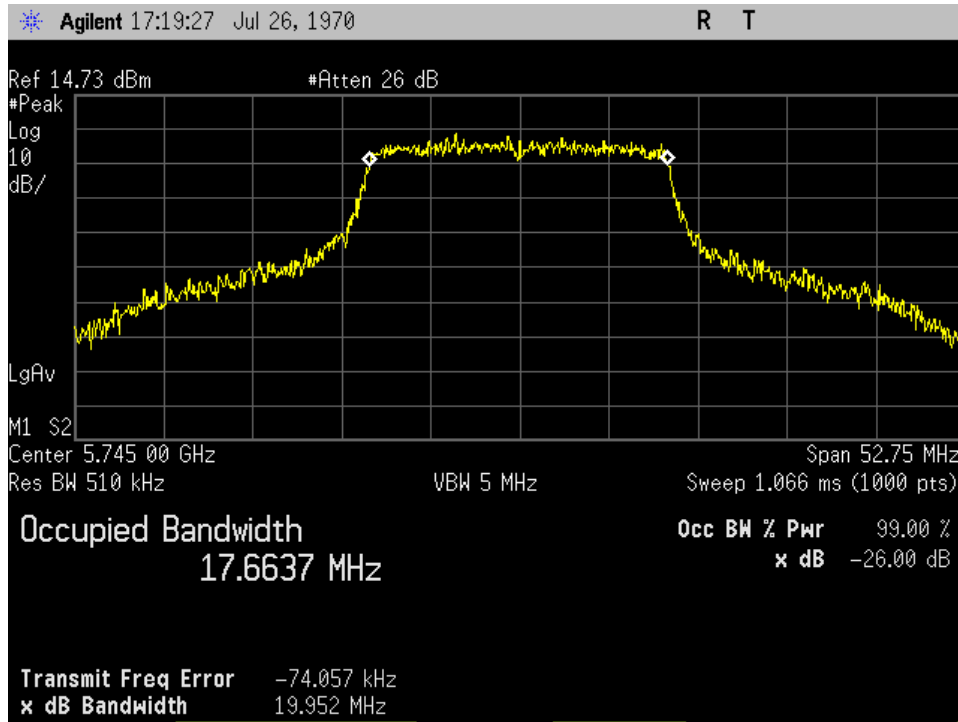
U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



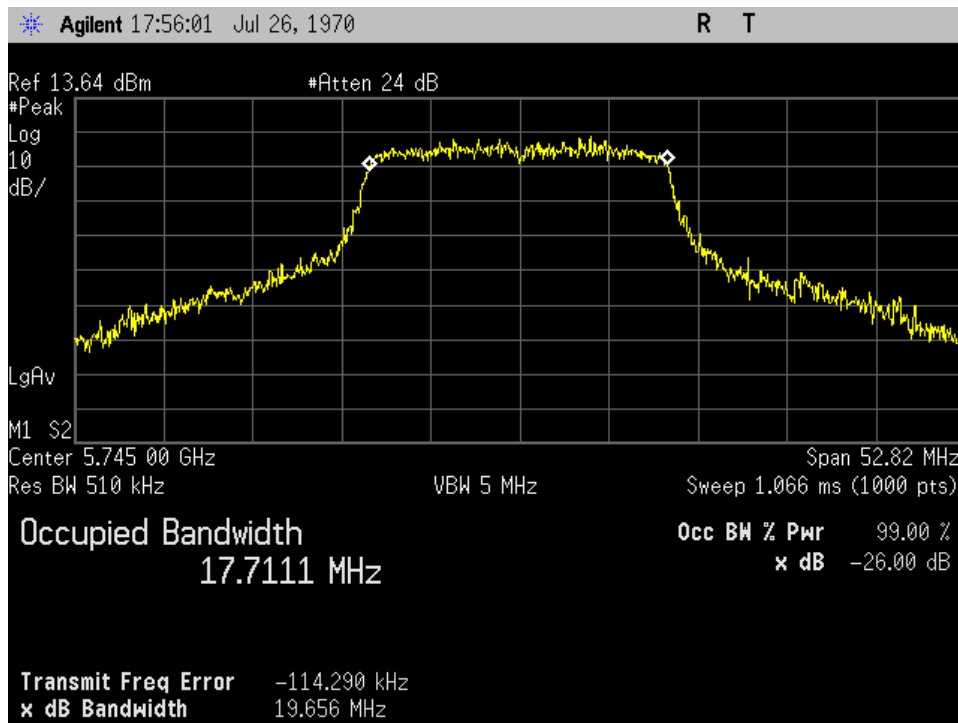
U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2



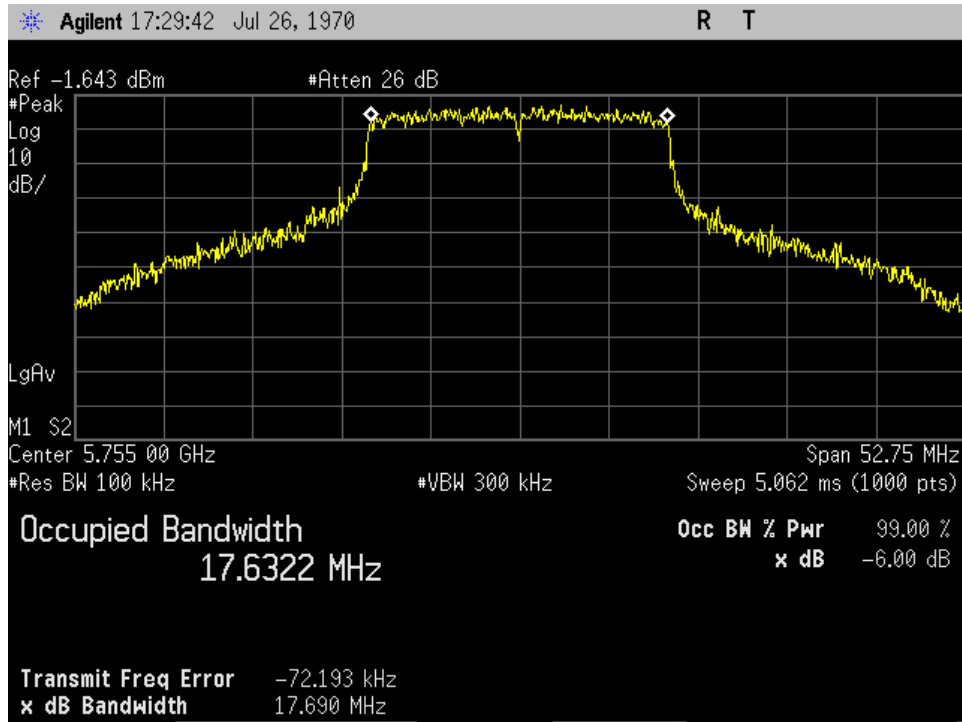
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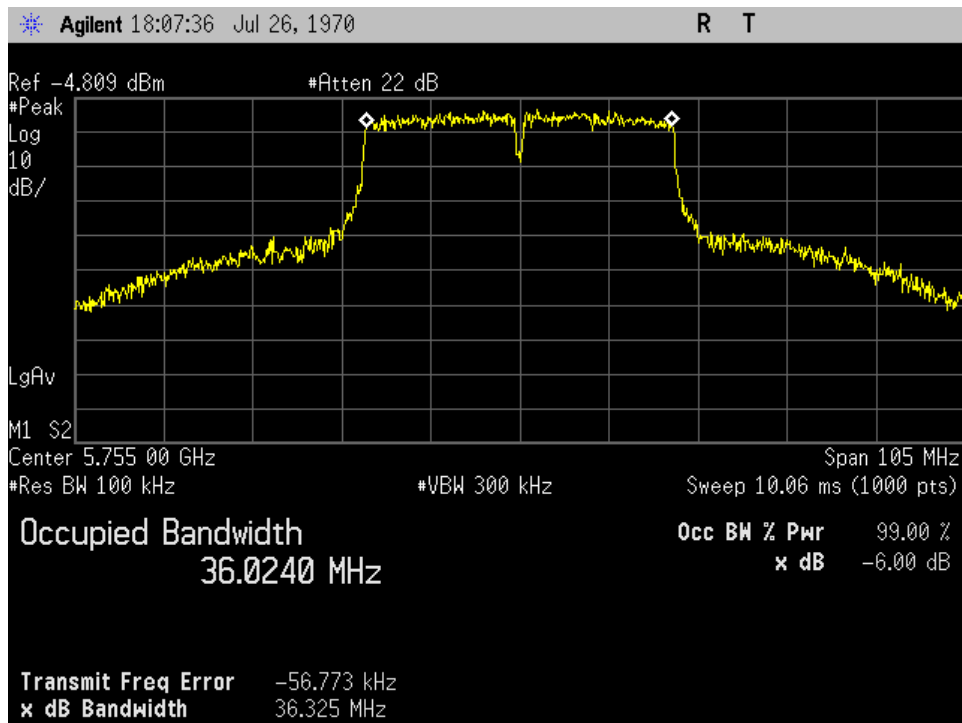
U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2



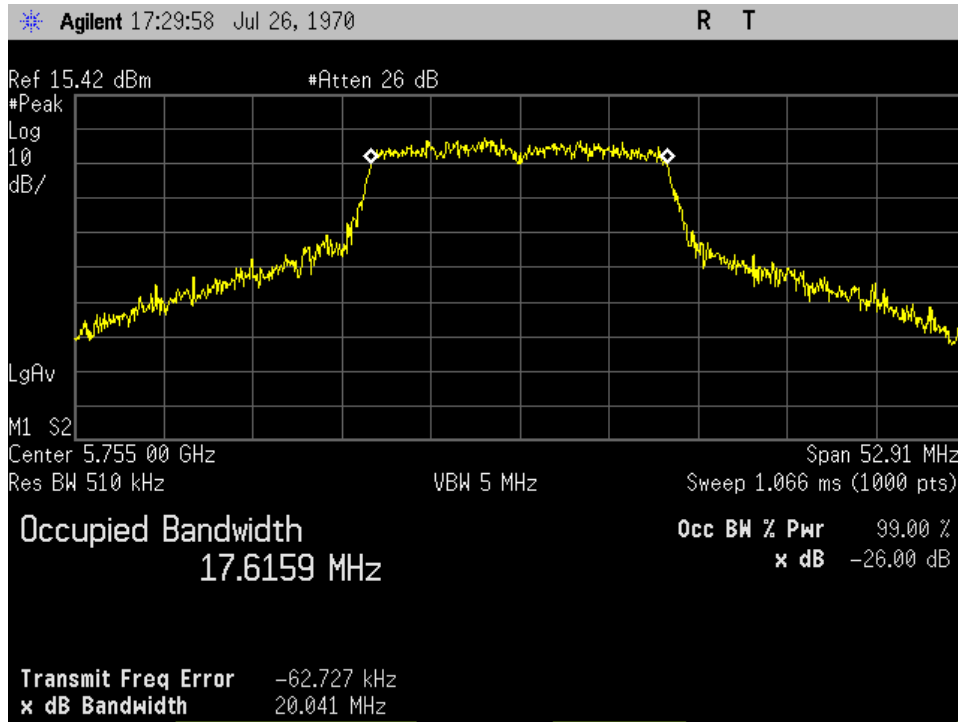
U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



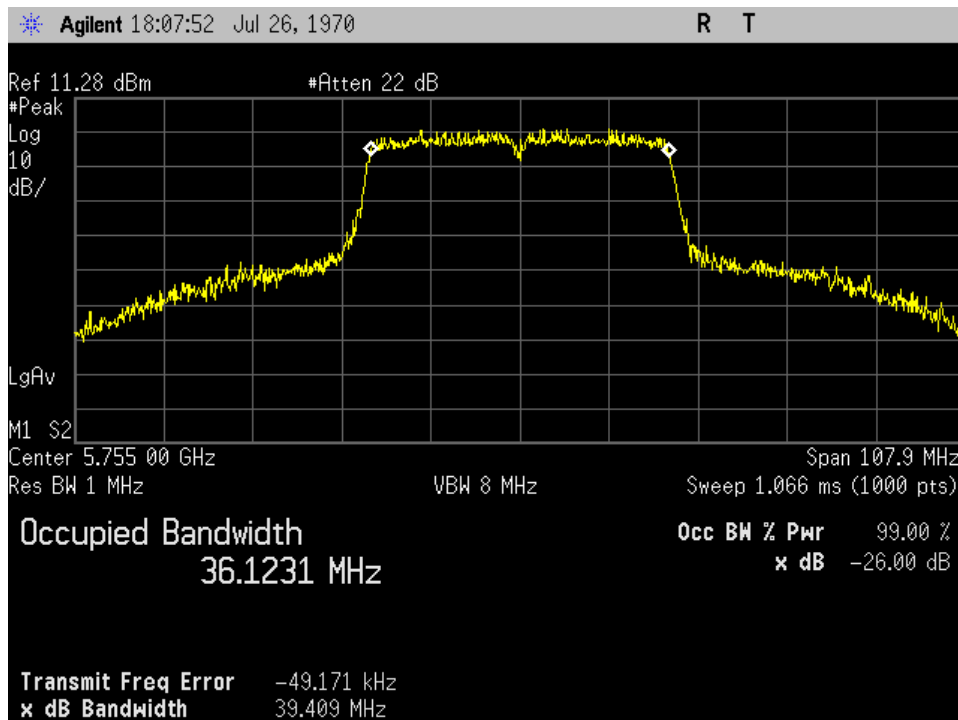
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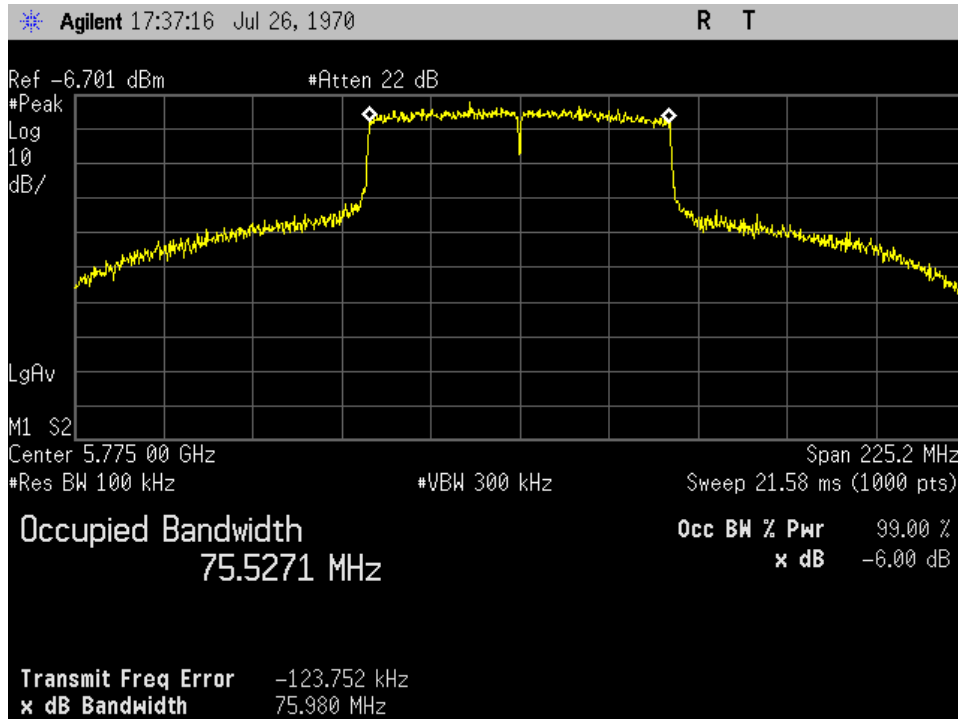
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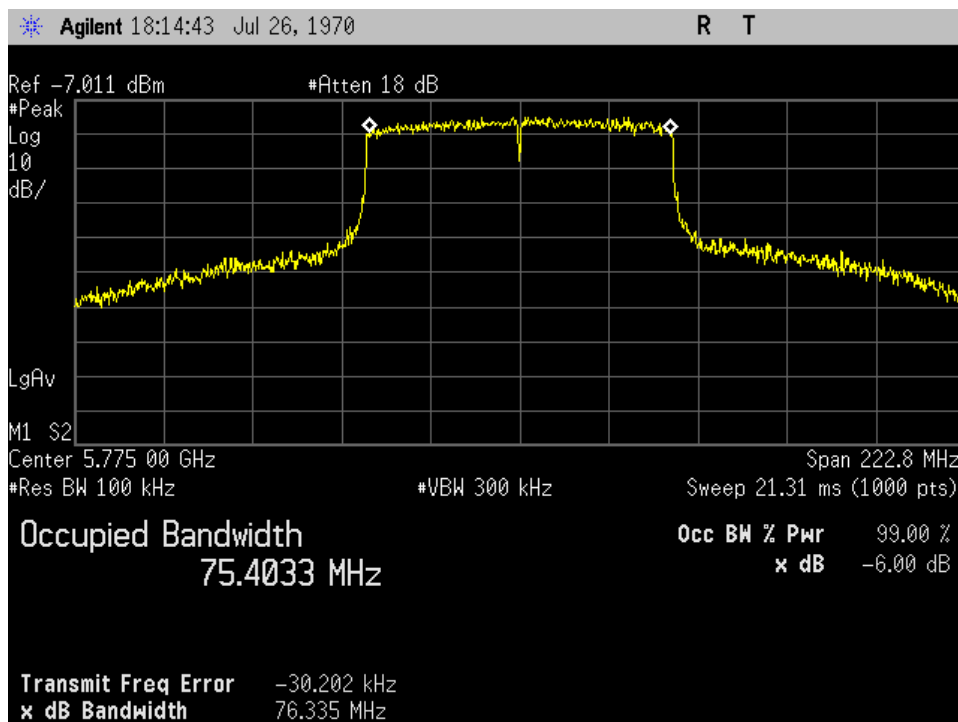
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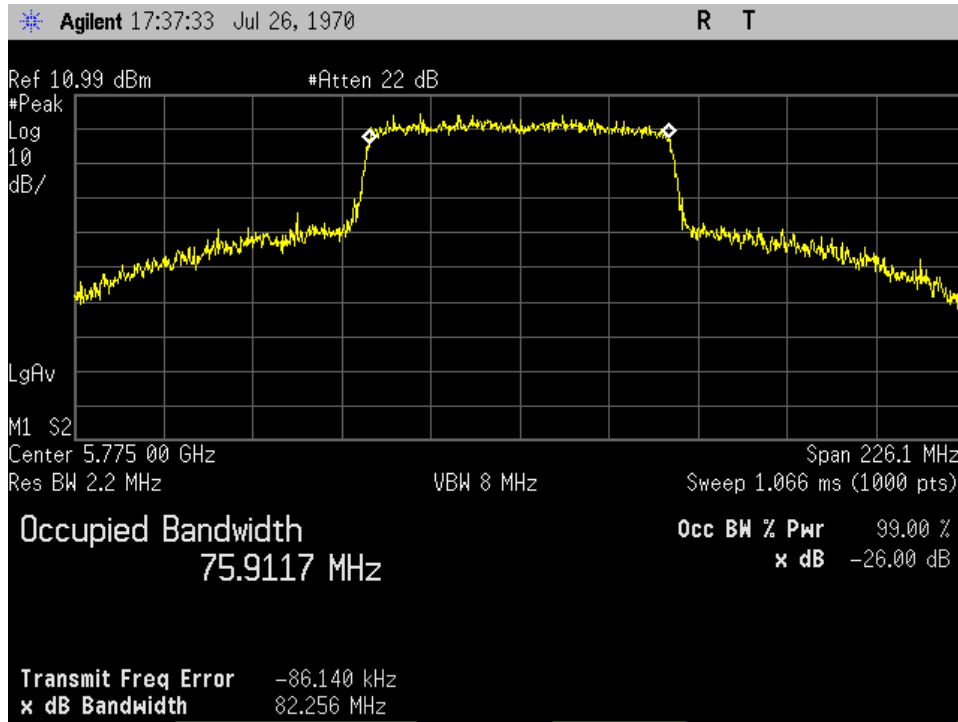
U-NII-3_5775MHz_High Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1



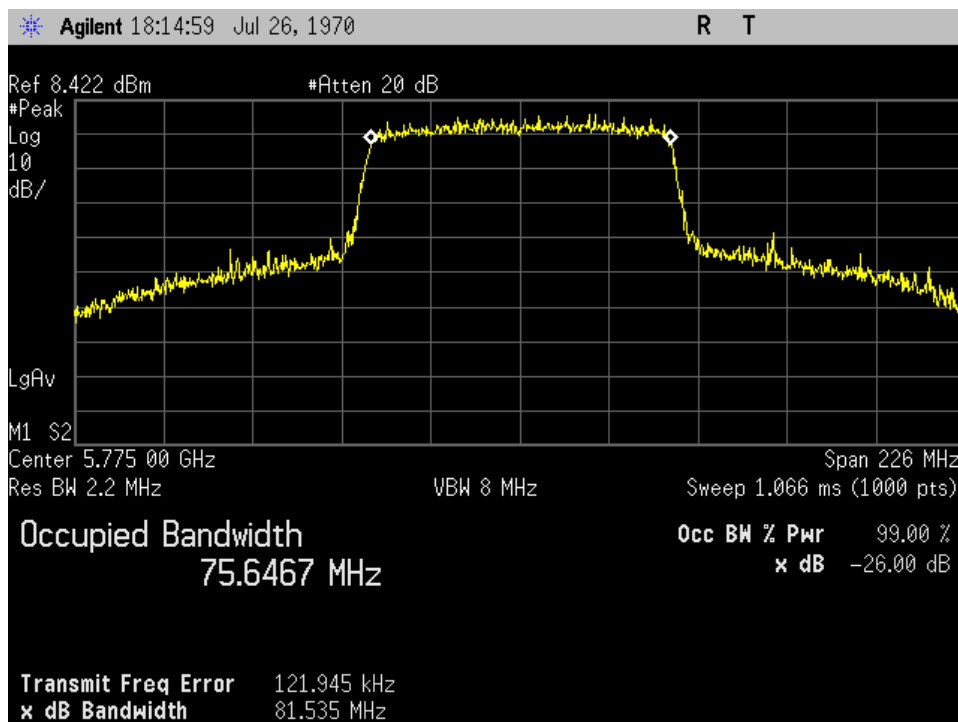
U-NII-3_5775MHz_High Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2



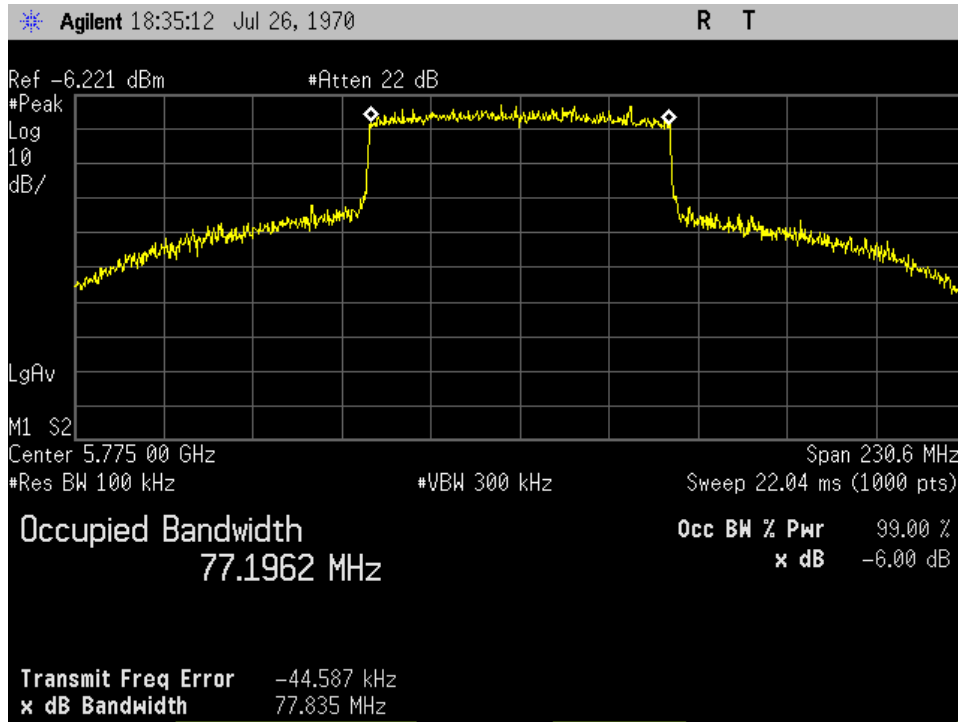
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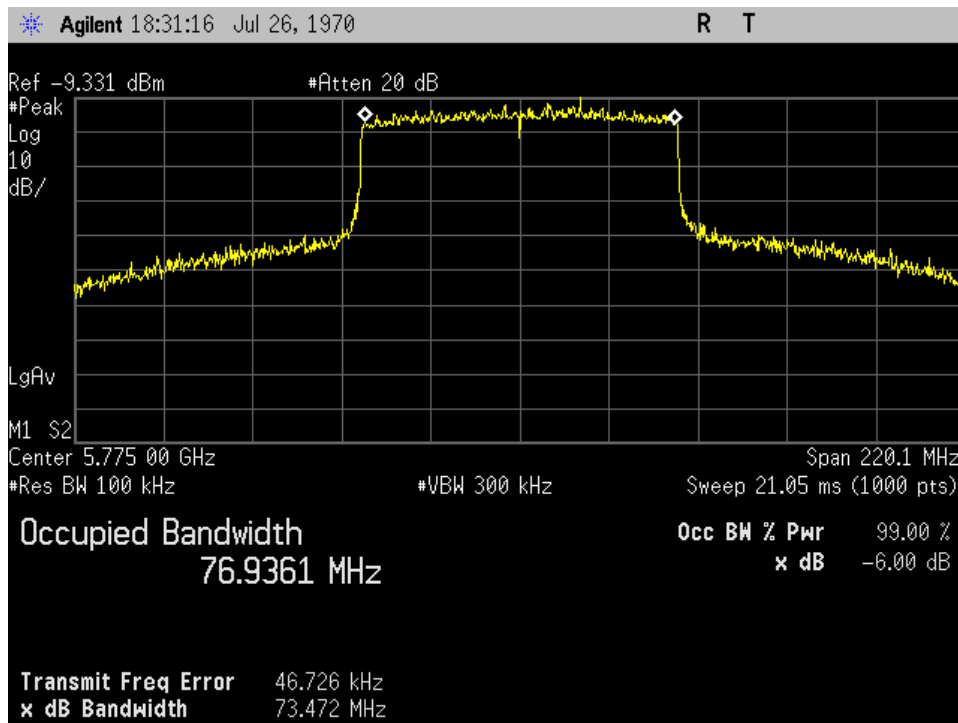
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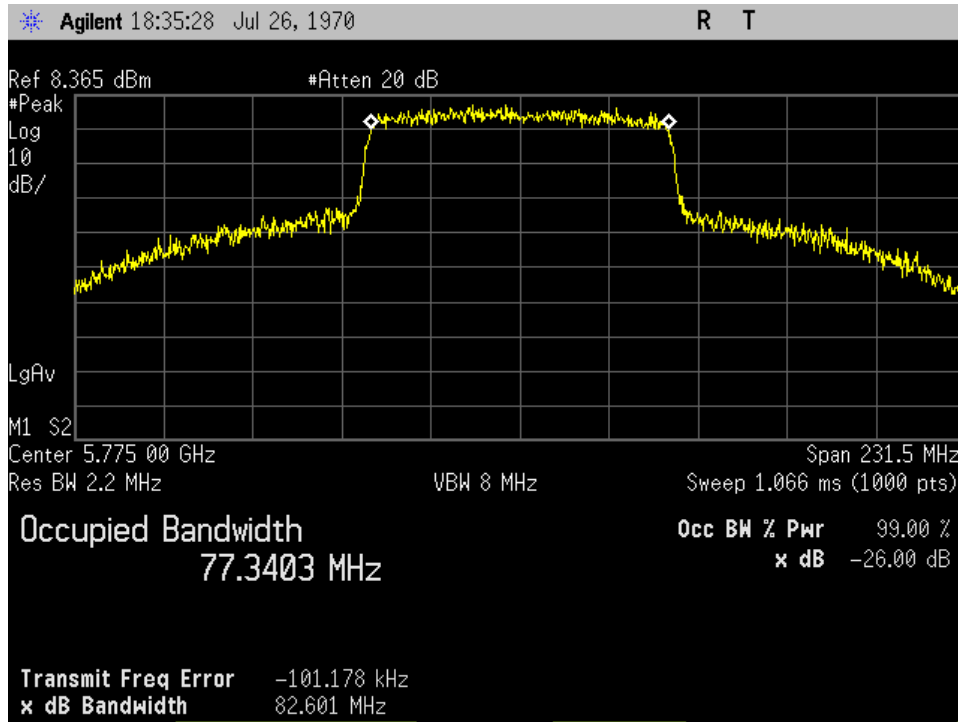
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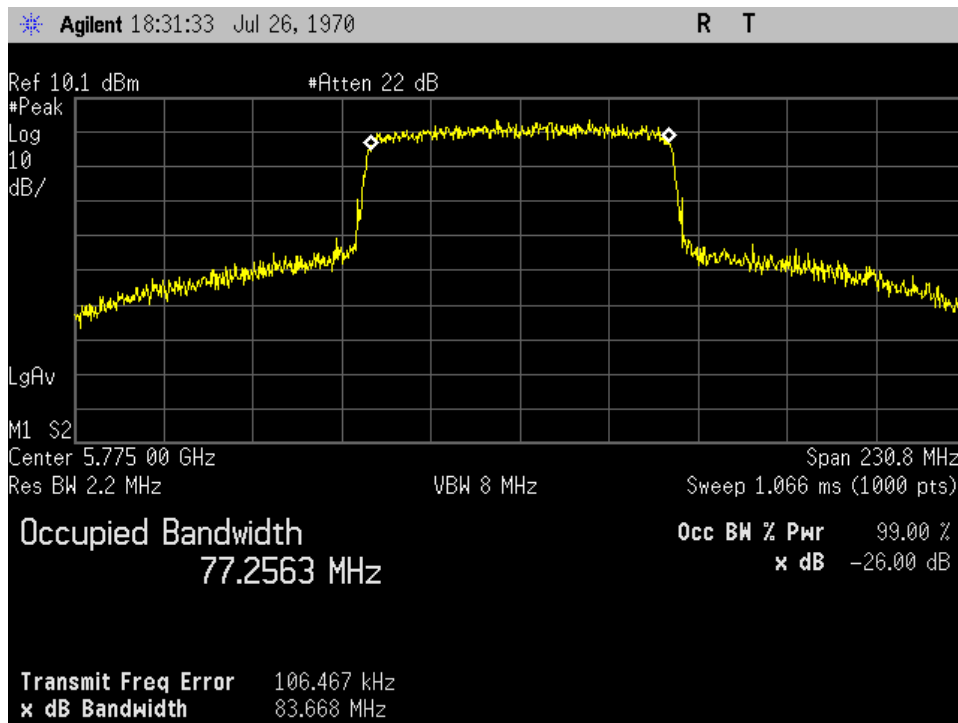
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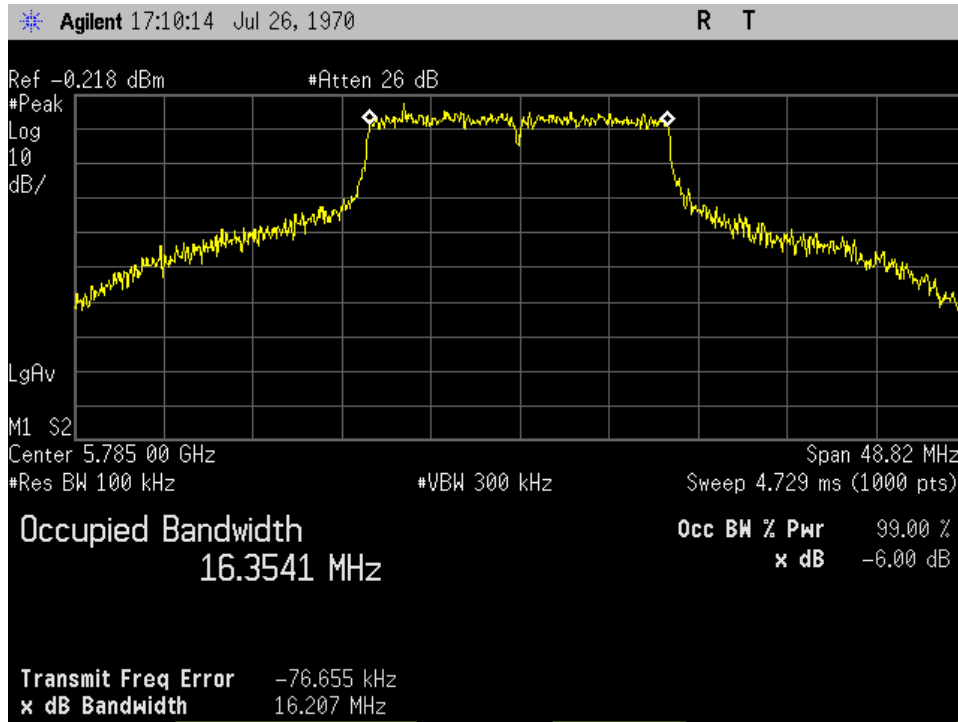
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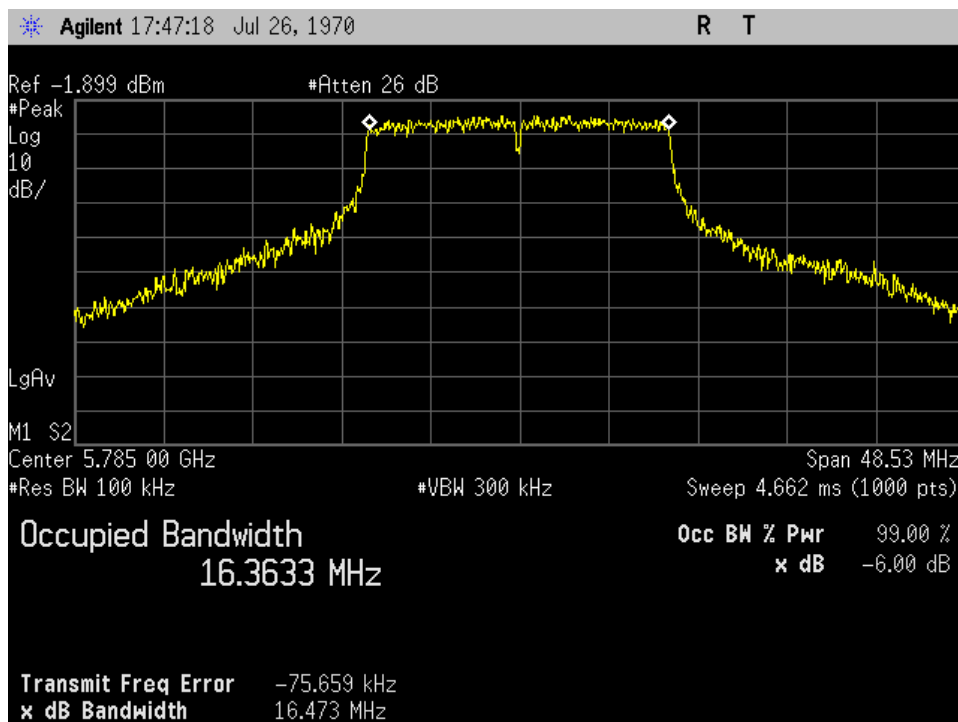
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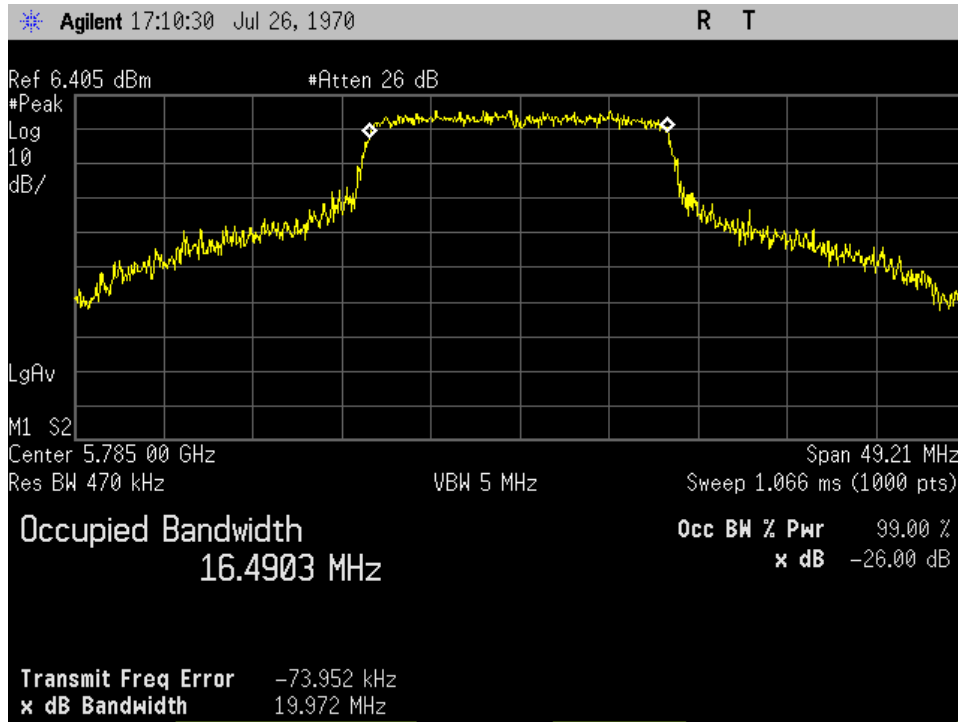
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1



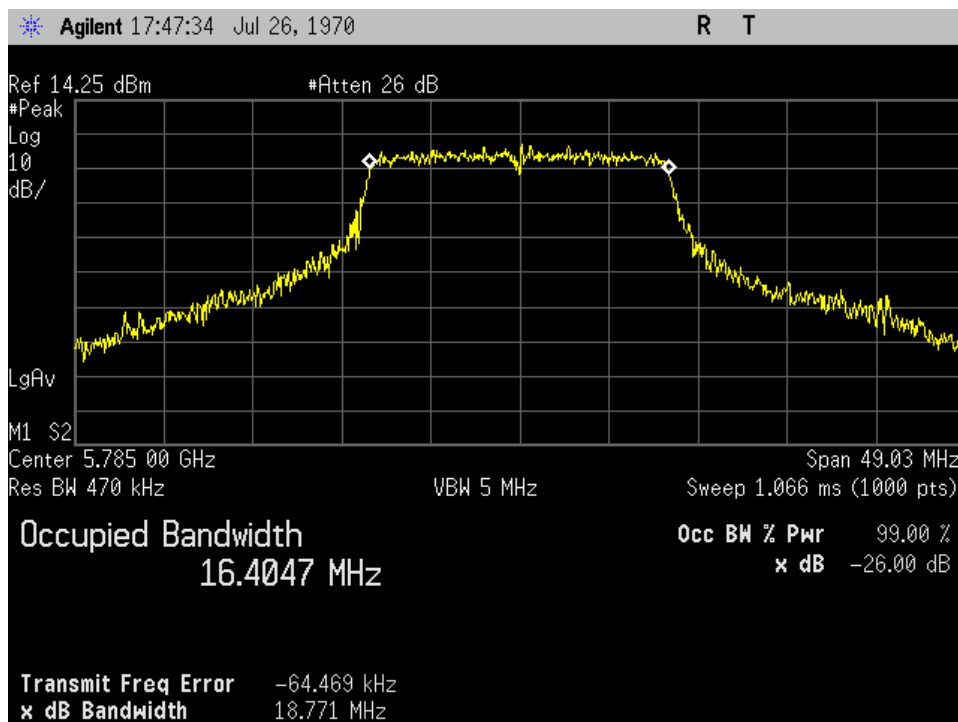
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2



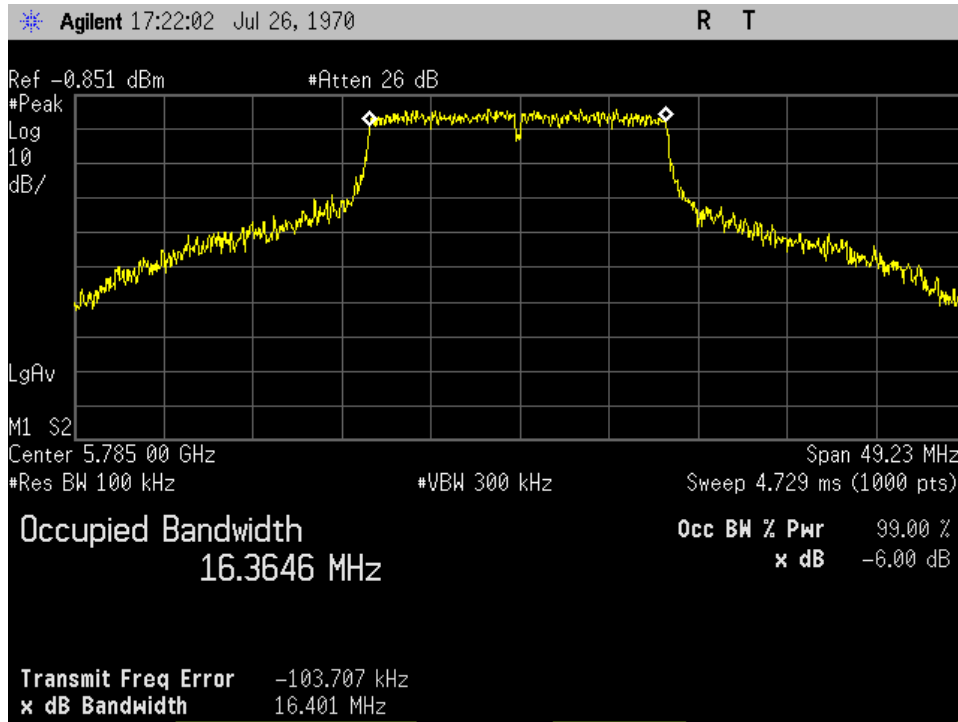
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 1



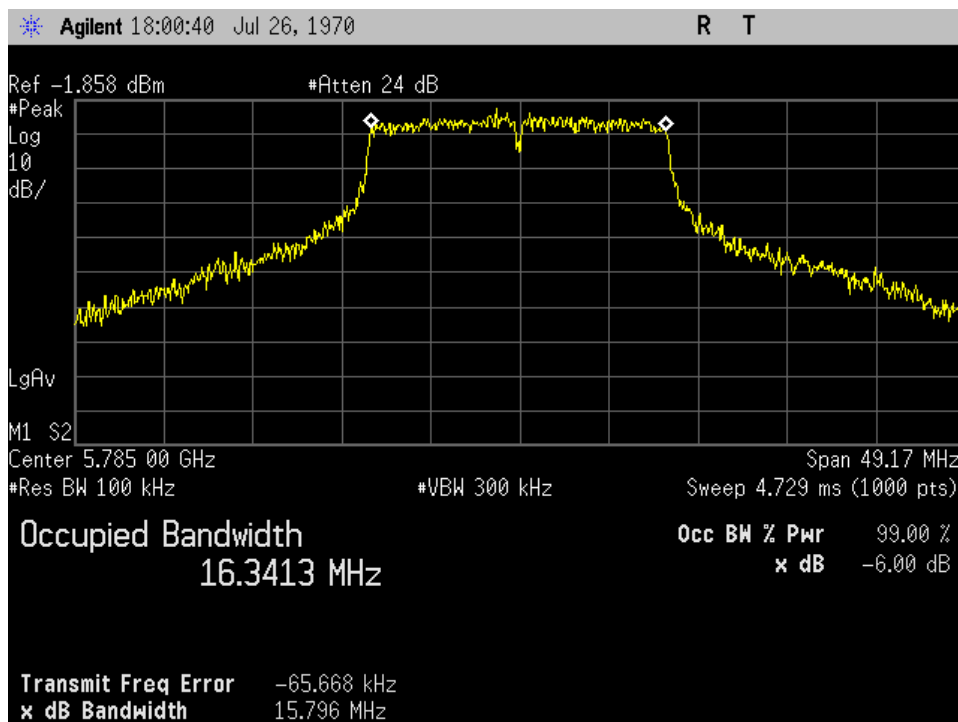
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 2



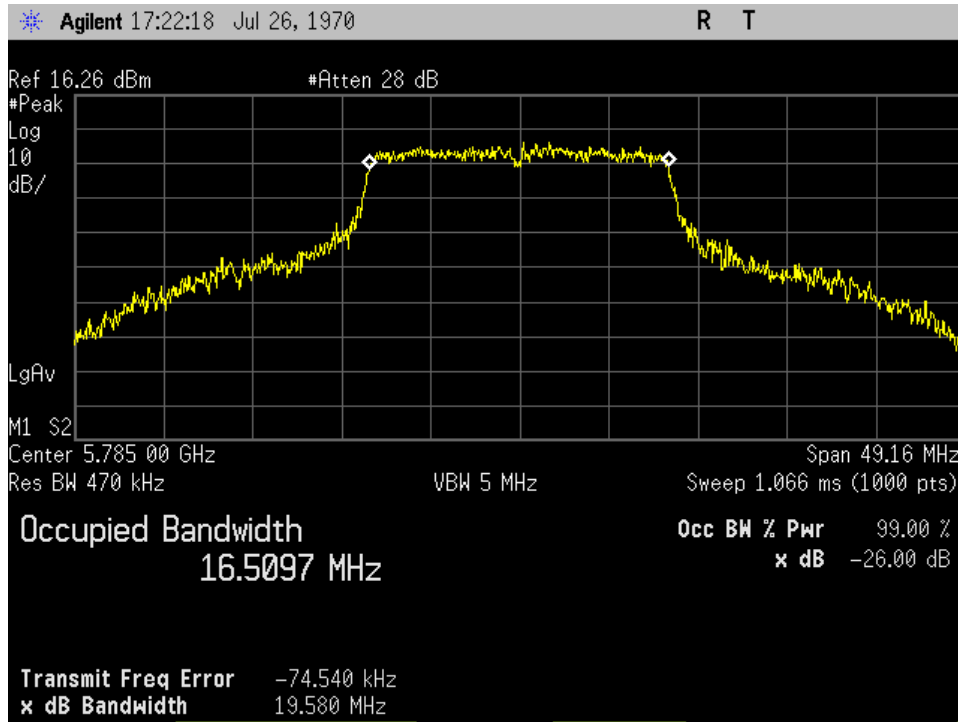
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



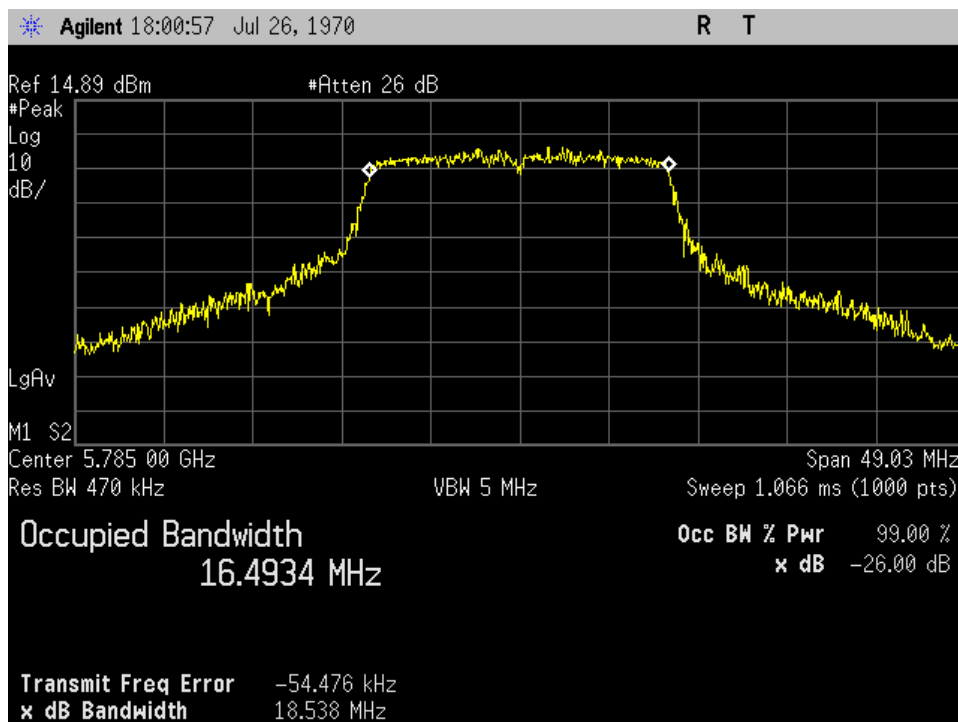
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2



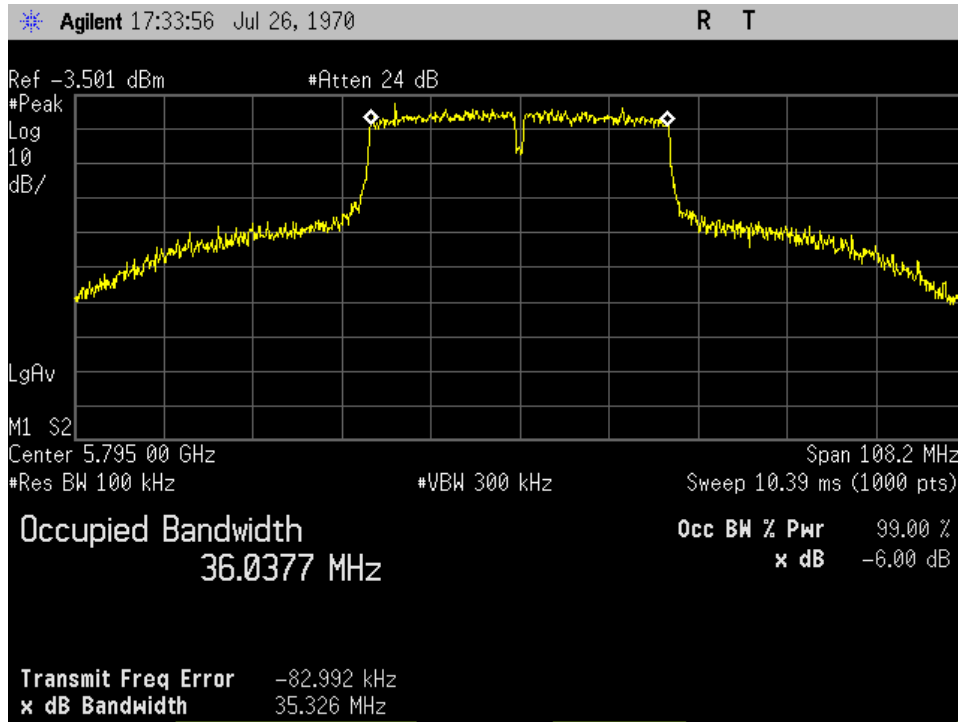
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1



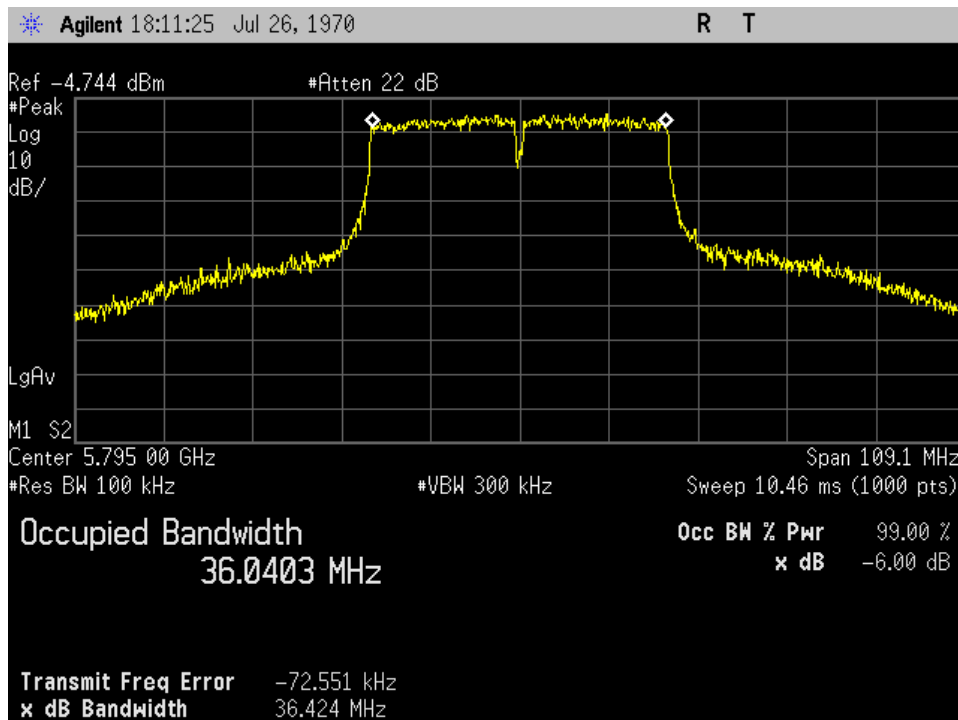
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2



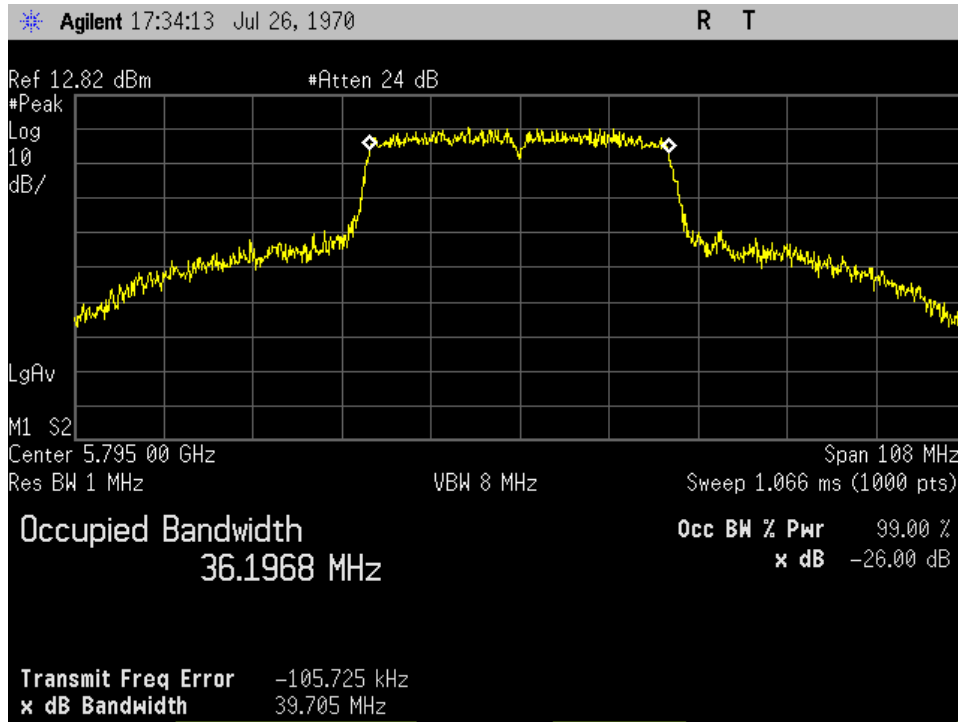
U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



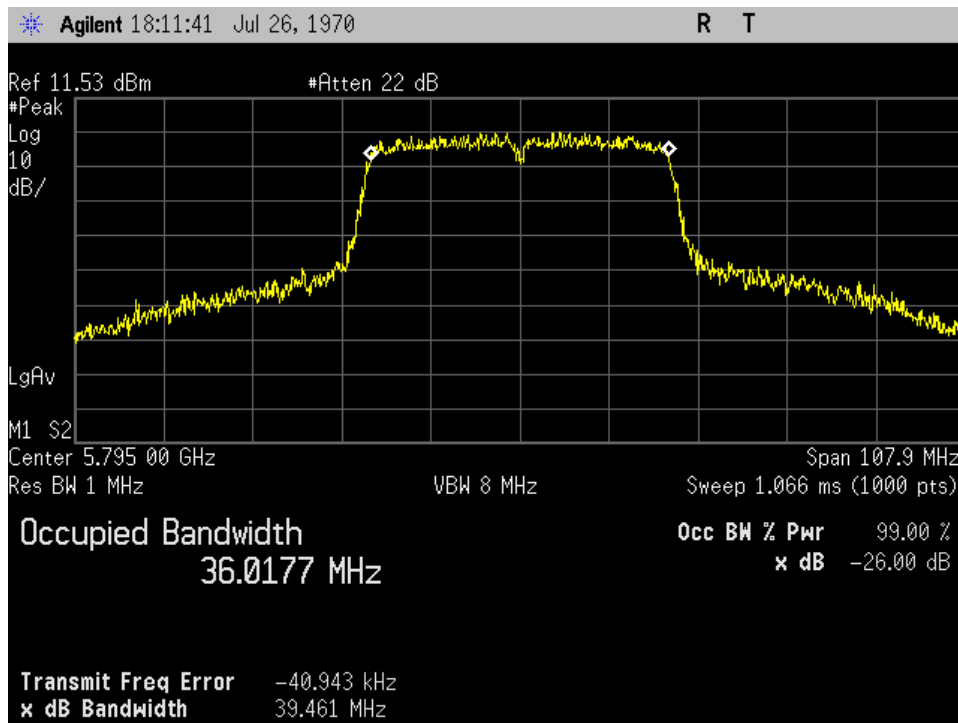
U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2



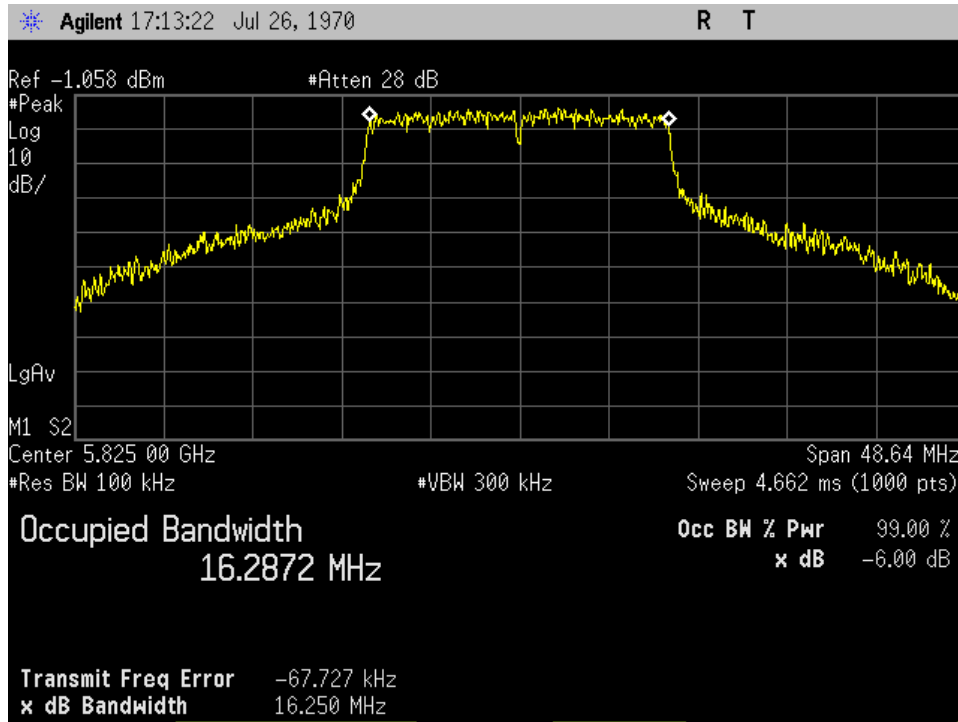
U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1



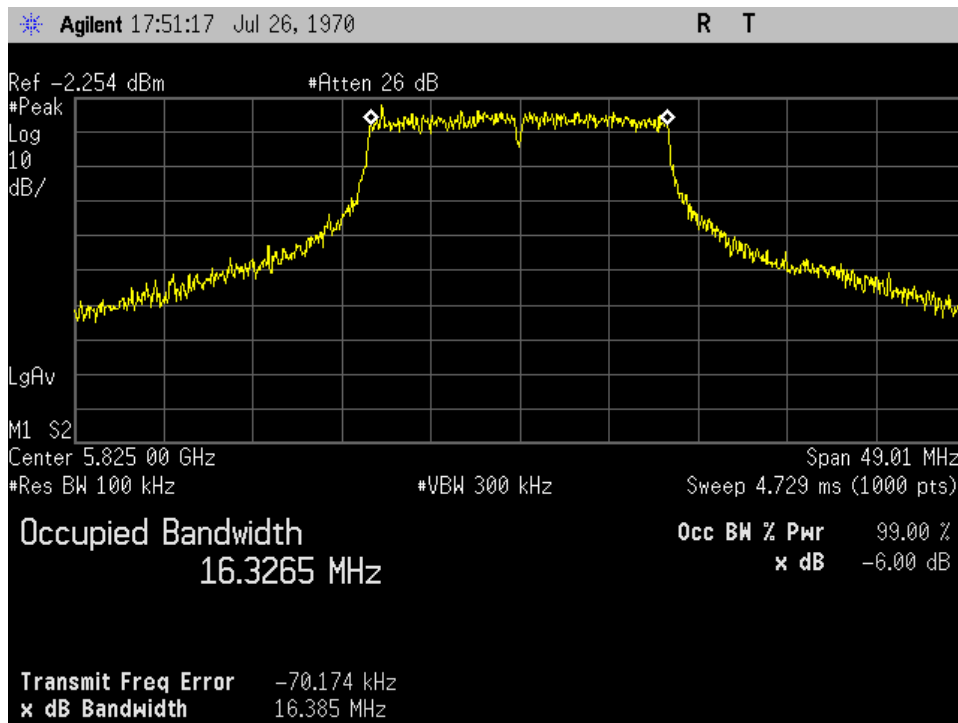
U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2



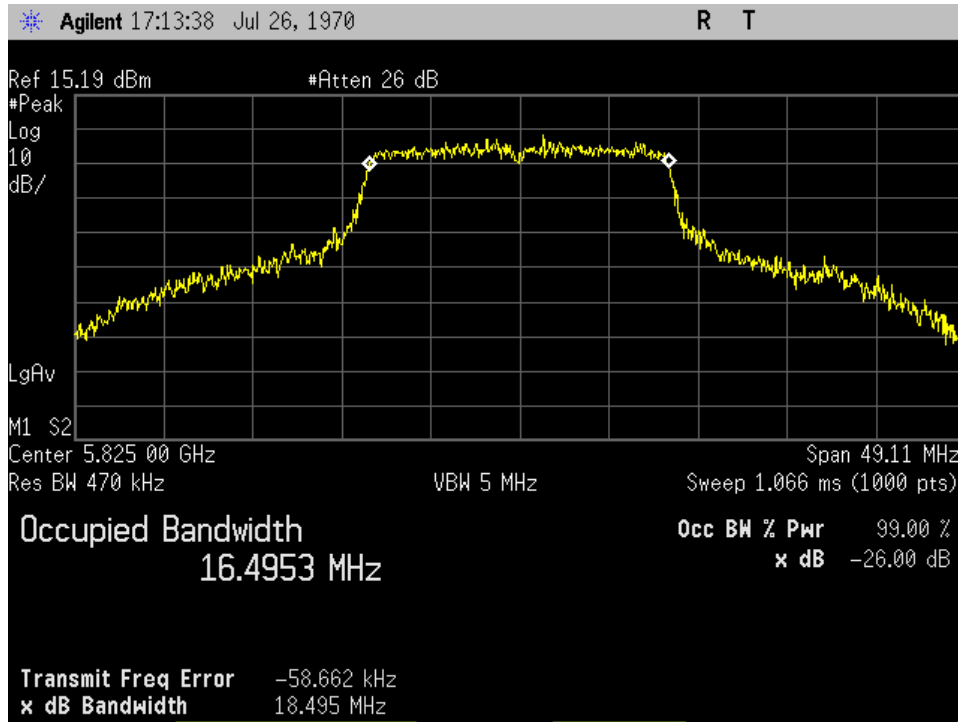
U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1



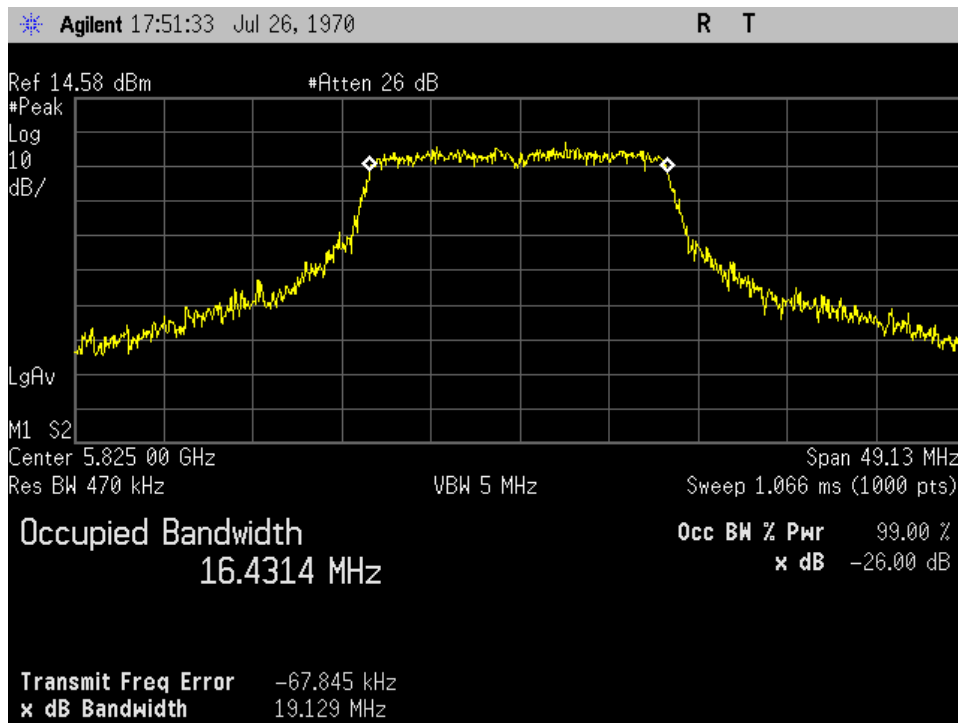
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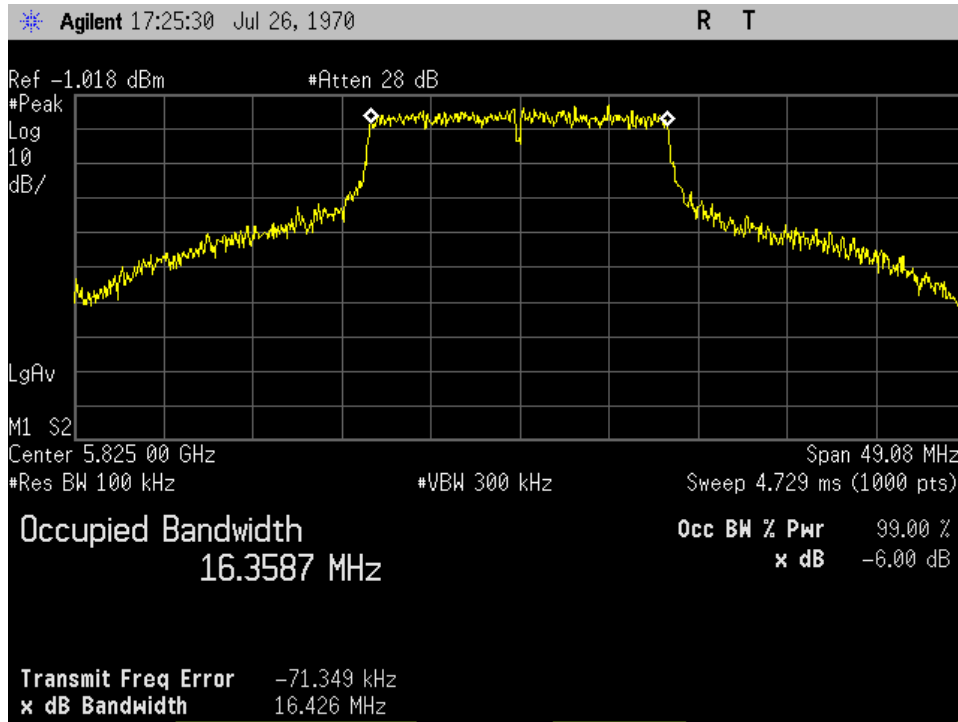
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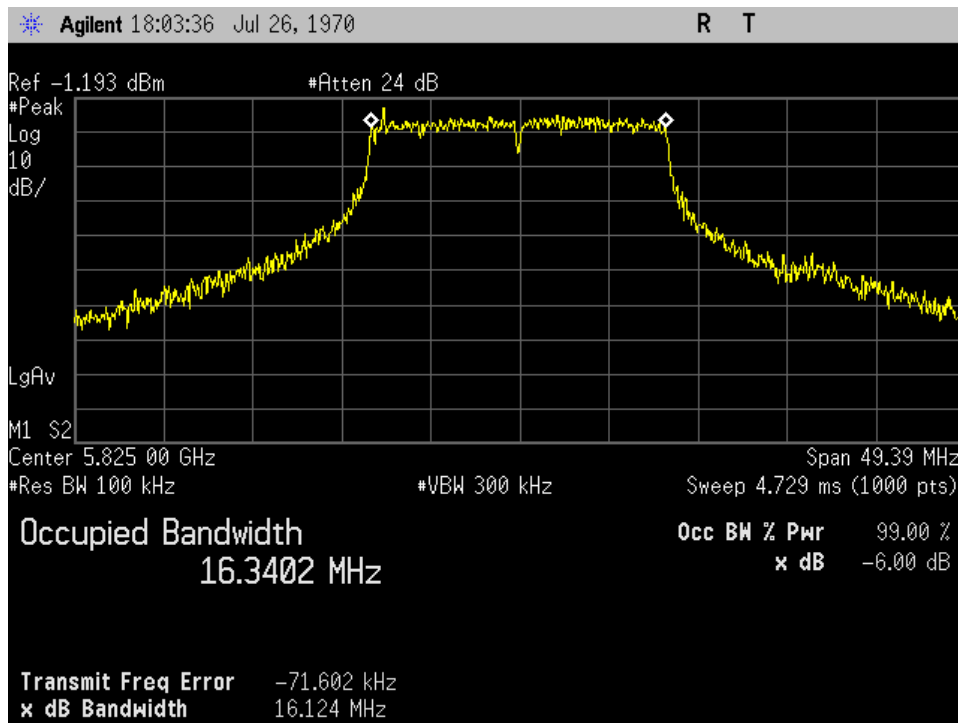
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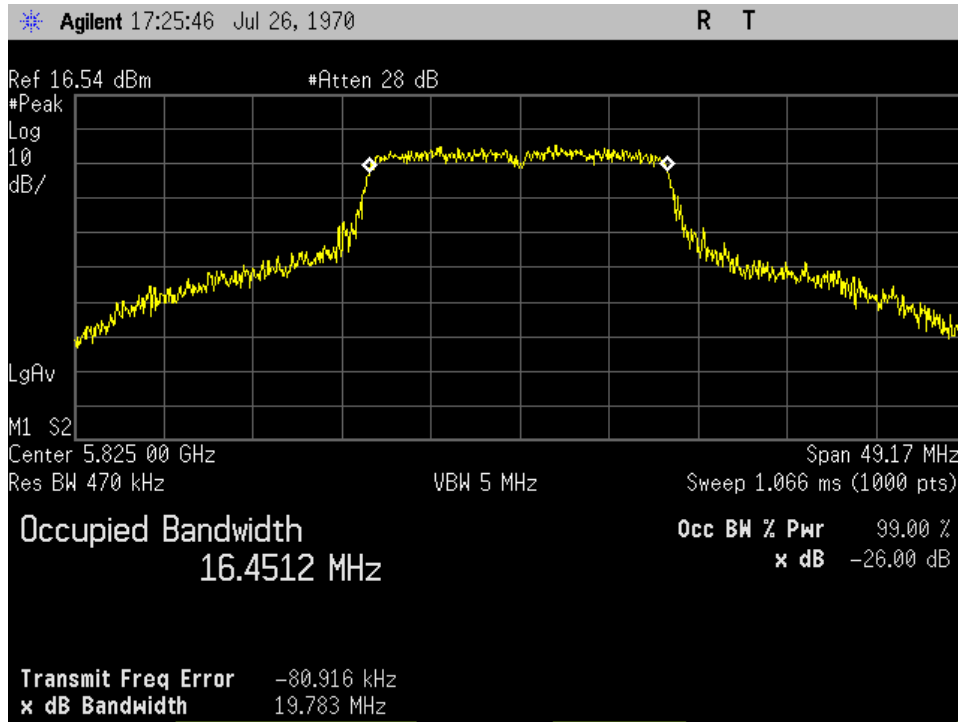
U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1



U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2



U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1



U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2

