Joe Dichoso
FCC

FA#92457

Joe,

Attached is the requested information for the LOZ102036 submittal. Please review it and feel free to contact me directly if any further information is needed.

Dave Case
Aironet Wireless Communications
330-664-7396

FCC LABORATORY
OET
FEB 9 10 27 AM '99

FCC LABORAT
OET
FEB 9 10 27 AM

From: oatech@fccsun07w.fcc.gov(OPTIONAL)
Subject: tx
To: mflom@goodnet.com

To: Morton Flom, M. Flom Associates Inc
From: Joe Dichoso
jdichoso@fcc.gov
FCC Application Processing Branch

Re: FCC ID LOZ102036
Applicant: Aironet Wireless Communications Inc
Correspondence Reference Number: 5877
731 Confirmation Number: EA92457
Date of Original E-Mail: 02/03/1999

1) Verify that the only antenna being applied for is the 2.1 dBi omni antenna.

2) FYI only, the correct output power determined from the maximum field strength is 73 mW.

3) The device is composite device consisting of a transmitter and a computer peripheral. Indicate whether the computer portion is a DOC approved or will be Certified. If it is DOC approved the label will have to be corrected and the DOC Certificate submitted. If it is to be Certified, submit an additional fee and test report for the computer peripheral portion (test must meet the minimum test configuration). If the device is strictly Class A, neither of the above applies.

4) The transmitter cannot coordinate its hopping sequence with the hopping sequence of other transmitters, or vice versa, for the purpose of avoiding the simultaneous occupancy of individual hopping frequencies by multiple transmitters. Provide a description on how the device complies with this rule.

5) Submit line conducted test data.

6) Indicate the type of antenna connector and antenna cable connectors and why they are unique.

7) The system shall hop to channel frequencies that are selected at the system hopping rate from a pseudorandomly ordered list of hopping frequencies. Indicate how the pseudorandom hopping sequence is derived. Provide a list of channel frequencies and a sample of a few sequences.

8) Each frequency must be used equally on the average by each transmitter. Each new transmission must start on a different frequency and must use all frequencies before repeating a new sequence. Therefore, Describe where the next transmission starts when all frequencies are not used for a previous message. This is required because some transmissions may needonly a few frequency hops to be completed. i.e. If the transmission started on the same frequency each time, this frequency would be used more than the others if many short transmissions were sent.

9) Section 15.247(a)1 indicates that the system receivers shall have input bandwidths that match the hopping channel bandwidths of their corresponding transmitters and shall shift frequencies in synchronization with the transmitted signals. Please explain how the device complies with this rule when a packet is repeated or when multiple packets are sent. How does the receiver shift frequencies and determine which frequency to shift to in order to sychronize with this transmitter?

10) Please indicate compliance with Section 15.247(a)1ii which states that the average time of occupancy on any frequency shall not be greater than 0.4 seconds within a 30 second period.
A Bandwidth plot showing the dwell time of a single channel with the span=0 is

submitted.

11) Transmitter Modules are not specifically addressed in our Part 15 Rules, and we do not normally authorize them. However, we will consider modular approval under the following
conditions. A module must have its own shielding, power supply regulation , input data-line buffering(input signal changes must not affect compliance) if such inputs are provided, and an antenna which complies with 15.203.
Please specifically address each item. Also, a module must be labelled with its own FCC ID number, and if the FCC ID is not visible when the module is installed in another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. The exterior label can use wording such as the following: " Transmitter Module FCC ID: xyz1234" or " This device Contains Transmitter Module FCC ID: xyz1234." The exact wording is not specified in our Rules (since modules are not specifically addressed), so you may use similar wording which expresses the same meaning.

12) Photo's of both sides of the circuit board without the shields.

The items indicated above must be submitted before processing can continue on the above referenced application. Failure to provide the requested information within 60 days of the original e-mail date may result in application dismissal pursuant to Section 2.917 (c) and forfeiture of the filing fee pursuant to section 1.1106.

DO NOT reply to this e-mail by using the Reply button. In order for your response to be processed expeditiously, you must upload your response via the Internet at www.fcc.gov, Electronic Filing, OET Equipment Authorization Electronic Filing. If the response is submitted through Add Attachments, in order to expedite processing, a message which informs the processing staff that a new exhibit has been submitted must also be submitted via Submit Correspondence. Also, please note that partial responses increase processing time and should not be submitted.

Any questions about the content of this correspondence should be directed to the e-mail address listed below the name of the sender.

Aironet

2/3/99

LOZ102036 – Additional Infromation

1) Currently the only antenna that Aironet is offering with this module is the 2.1 dBi dipole. Any additional antennas will be submitted via a Class II Change.
2) Thanks for the correction
3) This is not a composite device. The ISA PC card is a separate Aironet product that is designed for use with our PCMCIA cards. This card was modified by engineering to serve as a host \ adapter to run the MicroISA radio module. The ISA PC card itself has been approved under the FCC DOC requirements at AHD EMC labs in Michigan. The PC card is not sold with this radio (only the PCMCIA version) and currently is only offered without a radio to our major VAR's but not configured to connect to a microISA card. Copy of the DOC certificate is attached.
4) See attached explanation from LM3100 – the PCMCIA version of this radio that is currently pending approval at the FCC.
5) Line conducted data attached
6) The antenna connector on the radio uses a unique MCMX connector. The connector on the antenna was the standard Reverse TNC connector. Both of these connectors have been approved for use on all Aironet products by the FCC.
7) See attached pages and explanation from LM3100 file attached.
8) Each channel frequency is used equally by the transmitter. The dwell time is a system wide parameter. The default is 100 microseconds. In an infrastructure network, the Access Point controls this. Dwell time can be set to any value in the legal range through a management utility.
9) The receiver bandwidth is 1 Mhz . timing and frequency hopping per the 802.11 requirements – see information in confidential filing of theory of operation.
10) See number 8 for explanation. Maximum dwell time is 400 microseconds per FCC Part 15.247.
11) Transmitter module : The PC card that the radio is installed on has no voltage regulation, clocks, or buffering on board. The PC card only has the PCMCIA controller and memory, all intelligence is programmed into the radio. The radio has it's own shielding, on board voltage regulation (where required), data buffers, and unique antenna connector per 15.203. We are providing a label for the customer to apply to the outside of his unit which will state : This device contains Aironet Radio module FCC ID: LOZ102036.
12) Photos are being sent via courier.

#3

AHD LC

EMC Testing/Engineering Services

STATEMENT OF COMPLIANCE

REGULATORY STANDARD _____ FCC Part 15, Subpart B, Class B

NAME OF EQUIPMENT _____ FCI PCMCIA card, ISA PCMCIA card

MODEL NUMBER(S) _____ 410-004316
410-004416

MANUFACTURER _____ AIRONET Wireless Communications, Inc.
3875 Embassy Parkway
Akron, OH 44333

TEST DATE _____ August 21, 1998

According to testing performed at the AHD Open Area Test Site, the above-mentioned unit is in compliance with Class B limits defined in Part 15 of FCC Rules and Regulations. Compliance is outlined in AHD test report #09800142F issued September 22, 1998.

It is the manufacturer's responsibility to assure that additional production units of this model are manufactured with identical electrical and mechanical components.

As the responsible EMC Test Engineer, I hereby declare that the equipment, specified above, conforms to the Class B RFI Suppression Requirements of FCC Rules Part 15.

Ted Chaffee

Ted Chaffee, NCE
Lab Manager

Date : 9-22-98

Requirements of Frequency Hopping systems [15.247(a1,a1ii)]

Manufacturer's Statements

"The ~~LM~~3100 frequency hopping transceiver adheres to the requirement of transmissions in a truly psuedorandom manner by ensuring that the message traffic operates over a continuous hopping pattern. The sequence does not "reset" after a message transaction; rather, it continues in the hop pattern through any waiting time for the next transaction. In this manner, the participants in the wireless LAN synchronize with each other and follow the continual hop pattern selected at initialization regardless of the network traffic. Any beacon messages which are broadcast to all participants also appear in uniform distribution throughout all channels."

"The ~~LM3100~~ adheres to the IEEE 802.11 standard in that it uses the North American Base Hopping Sequence and the Hop Sequence Calculation Algorithm specified in IEEE P802.11 standard for Wireless LAN. The LM3100 hopping sequences use 79 frequencies."

"Each channel frequency is used equally by the transmitter."

B. Annex - Hopping Sequences (informative)

The following tables pertain to the hopping sequences for North America and ETSI.

Table B-1, Hopping Sequence Set 1

index	0	3	6	9	12	15	18	21	24	27	30	33	36
1	2	5	8	11	14	17	20	23	26	29	32	35	38
2	25	28	31	34	37	40	43	46	49	52	55	58	61
3	64	67	70	73	76	79	3	6	9	12	15	18	21
4	10	13	16	19	22	25	28	31	34	37	40	43	46
5	45	48	51	54	57	60	63	66	69	72	75	78	2
6	18	21	24	27	30	33	36	39	42	45	48	51	54
7	73	76	79	3	6	9	12	15	18	21	24	27	30
8	49	52	55	58	61	64	67	70	73	76	79	3	6
9	21	24	27	30	33	36	39	42	45	48	51	54	57
10	63	66	69	72	75	78	2	5	8	11	14	17	20
11	78	2	5	8	11	14	17	20	23	26	29	32	35
12	31	34	37	40	43	46	49	52	55	58	61	64	67
13	61	64	67	70	73	76	79	3	6	9	12	15	18
14	24	27	30	33	36	39	42	45	48	51	54	57	60
15	54	57	60	63	66	69	72	75	78	2	5	8	11
16	65	68	71	74	77	80	4	7	10	13	16	19	22
17	28	31	34	37	40	43	46	49	52	55	58	61	64
18	79	3	6	9	12	15	18	21	24	27	30	33	36
19	33	36	39	42	45	48	51	54	57	60	63	66	69
20	4	7	10	13	16	19	22	25	28	31	34	37	40
21	20	23	26	29	32	35	38	41	44	47	50	53	56
22	13	16	19	22	25	28	31	34	37	40	43	46	49
23	38	41	44	47	50	53	56	59	62	65	68	71	74
24	74	77	80	4	7	10	13	16	19	22	25	28	31
25	56	59	62	65	68	71	74	77	80	4	7	10	13
26	71	74	77	80	4	7	10	13	16	19	22	25	28
27	23	26	29	32	35	38	41	44	47	50	53	56	59
28	5	8	11	14	17	20	23	26	29	32	35	38	41
29	39	42	45	48	51	54	57	60	63	66	69	72	75
30	12	15	18	21	24	27	30	33	36	39	42	45	48
31	36	39	42	45	48	51	54	57	60	63	66	69	72
32	68	71	74	77	80	4	7	10	13	16	19	22	25
33	9	12	15	18	21	24	27	30	33	36	39	42	45
34	70	73	76	79	3	6	9	12	15	18	21	24	27
35	77	80	4	7	10	13	16	19	22	25	28	31	34
36	6	9	12	15	18	21	24	27	30	33	36	39	42
37	62	65	68	71	74	77	80	4	7	10	13	16	19
38	29	32	35	38	41	44	47	50	53	56	59	62	65
39	14	17	20	23	26	29	32	35	38	41	44	47	50

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index	0	3	6	9	12	15	18	21	24	27	30	33	36
40	27	30	33	36	39	42	45	48	51	54	57	60	63
41	16	19	22	25	28	31	34	37	40	43	46	49	52
42	59	62	65	68	71	74	77	80	4	7	10	13	16
43	43	46	49	52	55	58	61	64	67	70	73	76	79
44	76	79	3	6	9	12	15	18	21	24	27	30	33
45	34	37	40	43	46	49	52	55	58	61	64	67	70
46	72	75	78	2	5	8	11	14	17	20	23	26	29
47	11	14	17	20	23	26	29	32	35	38	41	44	47
48	60	63	66	69	72	75	78	2	5	8	11	14	17
49	80	4	7	10	13	16	19	22	25	28	31	34	37
50	47	50	53	56	59	62	65	68	71	74	77	80	4
51	22	25	28	31	34	37	40	43	46	49	52	55	58
52	75	78	2	5	8	11	14	17	20	23	26	29	32
53	66	69	72	75	78	2	5	8	11	14	17	20	23
54	41	44	47	50	53	56	59	62	65	68	71	74	77
55	15	18	21	24	27	30	33	36	39	42	45	48	51
56	35	38	41	44	47	50	53	56	59	62	65	68	71
57	67	70	73	76	79	3	6	9	12	15	18	21	24
58	52	55	58	61	64	67	70	73	76	79	3	6	9
59	58	61	64	67	70	73	76	79	3	6	9	12	15
60	44	47	50	53	56	59	62	65	68	71	74	77	80
61	50	53	56	59	62	65	68	71	74	77	80	4	7
62	17	20	23	26	29	32	35	38	41	44	47	50	53
63	7	10	13	16	19	22	25	28	31	34	37	40	43
64	19	22	25	28	31	34	37	40	43	46	49	52	55
65	8	11	14	17	20	23	26	29	32	35	38	41	44
66	69	72	75	78	2	5	8	11	14	17	20	23	26
67	51	54	57	60	63	66	69	72	75	78	2	5	8
68	42	45	48	51	54	57	60	63	66	69	72	75	78
69	3	6	9	12	15	18	21	24	27	30	33	36	39
70	30	33	36	39	42	45	48	51	54	57	60	63	66
71	57	60	63	66	69	72	75	78	2	5	8	11	14
72	37	40	43	46	49	52	55	58	61	64	67	70	73
73	55	58	61	64	67	70	73	76	79	3	6	9	12
74	26	29	32	35	38	41	44	47	50	53	56	59	62
75	46	49	52	55	58	61	64	67	70	73	76	79	3
76	53	56	59	62	65	68	71	74	77	80	4	7	10
77	40	43	46	49	52	55	58	61	64	67	70	73	76
78	32	35	38	41	44	47	50	53	56	59	62	65	68
79	48	51	54	57	60	63	66	69	72	75	78	2	5

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index	39	42	45	48	51	54	57	60	63	66	69	72	75
40	66	69	72	75	78	2	5	8	11	14	17	20	23
41	55	58	61	64	67	70	73	76	79	3	6	9	12
42	19	22	25	28	31	34	37	40	43	46	49	52	55
43	3	6	9	12	15	18	21	24	27	30	33	36	39
44	36	39	42	45	48	51	54	57	60	63	66	69	72
45	73	76	79	3	6	9	12	15	18	21	24	27	30
46	32	35	38	41	44	47	50	53	56	59	62	65	68
47	50	53	56	59	62	65	68	71	74	77	80	4	7
48	20	23	26	29	32	35	38	41	44	47	50	53	56
49	40	43	46	49	52	55	58	61	64	67	70	73	76
50	7	10	13	16	19	22	25	28	31	34	37	40	43
51	61	64	67	70	73	76	79	3	6	9	12	15	18
52	35	38	41	44	47	50	53	56	59	62	65	68	71
53	26	29	32	35	38	41	44	47	50	53	56	59	62
54	80	4	7	10	13	16	19	22	25	28	31	34	37
55	54	57	60	63	66	69	72	75	78	2	5	8	11
56	74	77	80	4	7	10	13	16	19	22	25	28	31
57	27	30	33	36	39	42	45	48	51	54	57	60	63
58	12	15	18	21	24	27	30	33	36	39	42	45	48
59	18	21	24	27	30	33	36	39	42	45	48	51	54
60	4	7	10	13	16	19	22	25	28	31	34	37	40
61	10	13	16	19	22	25	28	31	34	37	40	43	46
62	56	59	62	65	68	71	74	77	80	4	7	10	13
63	46	49	52	55	58	61	64	67	70	73	76	79	3
64	58	61	64	67	70	73	76	79	3	6	9	12	15
65	47	50	53	56	59	62	65	68	71	74	77	80	4
66	29	32	35	38	41	44	47	50	53	56	59	62	65
67	11	14	17	20	23	26	29	32	35	38	41	44	47
68	2	5	8	11	14	17	20	23	26	29	32	35	38
69	42	45	48	51	54	57	60	63	66	69	72	75	78
70	69	72	75	78	2	5	8	11	14	17	20	23	26
71	17	20	23	26	29	32	35	38	41	44	47	50	53
72	76	79	3	6	9	12	15	18	21	24	27	30	33
73	15	18	21	24	27	30	33	36	39	42	45	48	51
74	65	68	71	74	77	80	4	7	10	13	16	19	22
75	6	9	12	15	18	21	24	27	30	33	36	39	42
76	13	16	19	22	25	28	31	34	37	40	43	46	49
77	79	3	6	9	12	15	18	21	24	27	30	33	36
78	71	74	77	80	4	7	10	13	16	19	22	25	28
79	8	11	14	17	20	23	26	29	32	35	38	41	44

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index	39	42	45	48	51	54	57	60	63	66	69	72	75
1	41	44	47	50	53	56	59	62	65	68	71	74	77
2	64	67	70	73	76	79	3	6	9	12	15	18	21
3	24	27	30	33	36	39	42	45	48	51	54	57	60
4	49	52	55	58	61	64	67	70	73	76	79	3	6
5	5	8	11	14	17	20	23	26	29	32	35	38	41
6	57	60	63	66	69	72	75	78	2	5	8	11	14
7	33	36	39	42	45	48	51	54	57	60	63	66	69
8	9	12	15	18	21	24	27	30	33	36	39	42	45
9	60	63	66	69	72	75	78	2	5	8	11	14	17
10	23	26	29	32	35	38	41	44	47	50	53	56	59
11	38	41	44	47	50	53	56	59	62	65	68	71	74
12	70	73	76	79	3	6	9	12	15	18	21	24	27
13	21	24	27	30	33	36	39	42	45	48	51	54	57
14	63	66	69	72	75	78	2	5	8	11	14	17	20
15	14	17	20	23	26	29	32	35	38	41	44	47	50
16	25	28	31	34	37	40	43	46	49	52	55	58	61
17	67	70	73	76	79	3	6	9	12	15	18	21	24
18	39	42	45	48	51	54	57	60	63	66	69	72	75
19	72	75	78	2	5	8	11	14	17	20	23	26	29
20	43	46	49	52	55	58	61	64	67	70	73	76	79
21	59	62	65	68	71	74	77	80	4	7	10	13	16
22	52	55	58	61	64	67	70	73	76	79	3	6	9
23	77	80	4	7	10	13	16	19	22	25	28	31	34
24	34	37	40	43	46	49	52	55	58	61	64	67	70
25	16	19	22	25	28	31	34	37	40	43	46	49	52
26	31	34	37	40	43	46	49	52	55	58	61	64	67
27	62	65	68	71	74	77	80	4	7	10	13	16	19
28	44	47	50	53	56	59	62	65	68	71	74	77	80
29	78	2	5	8	11	14	17	20	23	26	29	32	35
30	51	54	57	60	63	66	69	72	75	78	2	5	8
31	75	78	2	5	8	11	14	17	20	23	26	29	32
32	28	31	34	37	40	43	46	49	52	55	58	61	64
33	48	51	54	57	60	63	66	69	72	75	78	2	5
34	30	33	36	39	42	45	48	51	54	57	60	63	66
35	37	40	43	46	49	52	55	58	61	64	67	70	73
36	45	48	51	54	57	60	63	66	69	72	75	78	2
37	22	25	28	31	34	37	40	43	46	49	52	55	58
38	68	71	74	77	80	4	7	10	13	16	19	22	25
39	53	56	59	62	65	68	71	74	77	80	4	7	10

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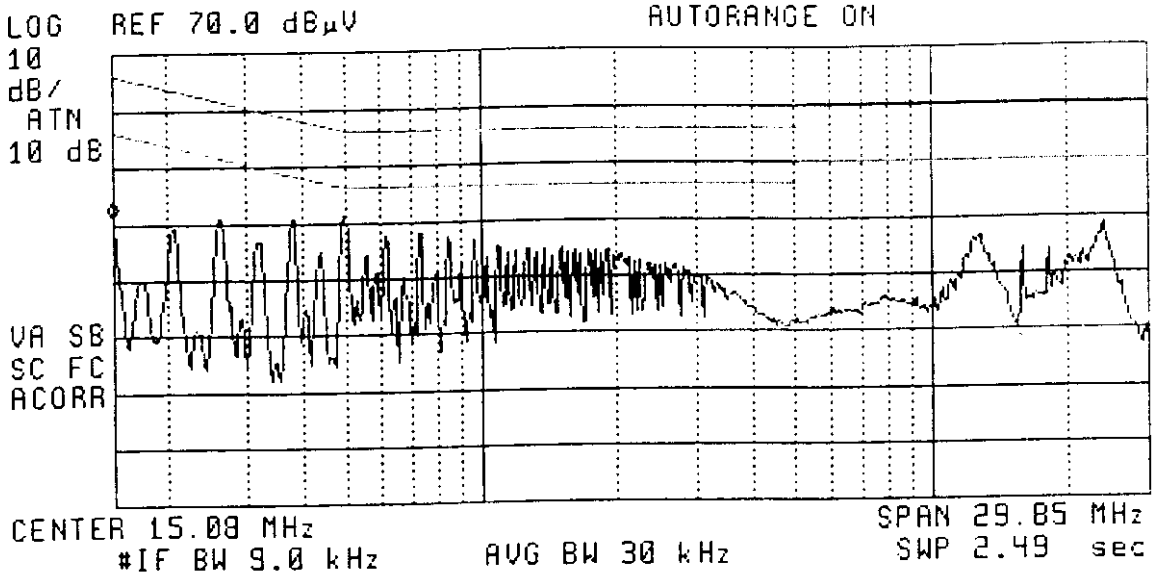
Test Data

Line Conducted

NEUTRAL to Ground Measurement.
Class B
Plot of Peak Values

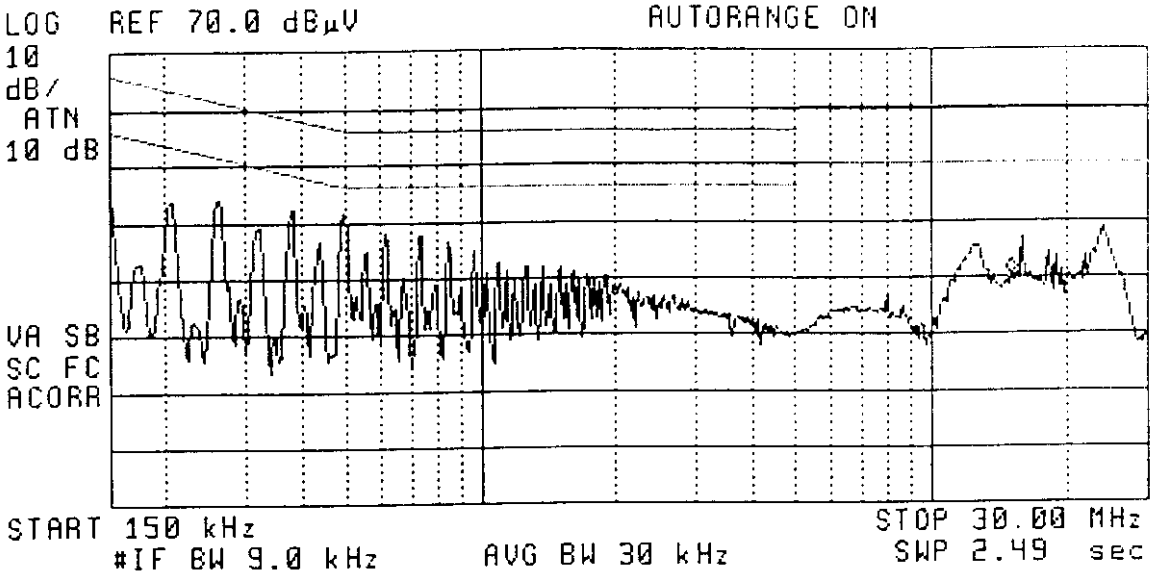


Tabulated Quasi-Peak/Average Measurements.

Frequency	dBuV Reading		dBuV EN55022 B Limit		dB Margin	
MHz	QP	Avg	QP	Avg	QP	Avg
0.150	41.93	41.70	66.00	56.00	-24.07	-14.30
0.266	41.53	41.48	61.25	51.25	-19.72	-9.77
0.384	41.13	41.01	58.21	48.21	-17.08	-7.20
0.501	41.85	41.80	56.00	46.00	-14.15	-4.20
0.737	38.44	38.41	56.00	46.00	-17.56	-7.59
0.855	37.91	37.87	56.00	46.00	-18.09	-8.13
0.973	36.86	36.83	56.00	46.00	-19.14	-9.17
12.428	35.95	33.86	60.00	50.00	-24.05	-16.14
16.077	35.51	33.48	60.00	50.00	-24.49	-16.52
18.068	34.99	34.48	60.00	50.00	-25.01	-15.52
24.002	39.23	36.84	60.00	50.00	-20.77	-13.16

Signature: Ted Chaffee

PHASE to Ground Measurement.
Class B
Plot of Peak Values



Tabulated Quasi-Peak/Average Measurements.

Frequency	dBuV Reading		dBuV EN55022 B Limit		dB Margin	
MHz	QP	Avg	QP	Avg	QP	Avg
0.150	47.61	47.53	66.00	56.00	-18.39	-8.47
0.207	44.43	44.24	63.33	53.33	-18.90	-9.09
0.266	44.99	44.88	61.26	51.26	-16.27	-6.38
0.383	42.85	42.74	58.21	48.21	-15.36	-5.47
0.501	42.24	42.12	56.00	46.00	-13.76	-3.88
0.620	38.71	38.58	56.00	46.00	-17.29	-7.42
0.737	38.30	38.26	56.00	46.00	-17.70	-7.74
12.428	36.78	34.81	60.00	50.00	-23.22	-15.19
16.077	36.53	33.37	60.00	50.00	-23.47	-16.63
24.003	39.59	36.93	60.00	50.00	-20.41	-13.07

Signature: _____

Lower Limit	Upper Limit	Regulatory Range	Geography
2.402 GHz	2.480 GHz	2.400-2.4835 GHz	North America*
2.402 GHz	2.480 GHz	2.400-2.4835 GHz	Europe*
2.473 GHz	2.495 GHz	2.471-2.497 GHz	Japan*
2.447 GHz	2.473 GHz	2.445-2.475 GHz	Spain*
2.448 GHz	2.482 GHz	2.4465-2.4835 GHz	France*

Table 36, Operating Frequency Range

* The frequency ranges in this table are subject to the geographic specific regulatory authorities

14.6.4 Number of Operating Channels

The number of transmit and receive frequency channels used for operating the PMD entity is 79 for the US and Europe and 23 channels for Japan. This is more fully defined in Table 38 and Table 39 of 14.6.5.

Minimum*	Hopping Set	Geography
75	79	North America*
20	79	Europe*
Not Applicable	23	Japan*
20	27	Spain*
20	35	France*

Table 37, Number of Operating Channels

* The number of required hopping channels are subject to the geographic specific Regulatory Authorities

14.6.5 Operating Channel Center Frequency

The channel center frequency is defined in sequential 1.0 MHz steps beginning with the first channel, channel 2.402 GHz for the U.S.A. and Europe, as listed in Table 38. The channel centers for Japan, starting at 2.473 GHz with 1 MHz increments, is listed in Table 39.

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Channel #	Value	Channel #	Value	Channel #	Value
2	2.402	28	2.428	54	2.454
3	2.403	29	2.429	55	2.455
4	2.404	30	2.430	56	2.456
5	2.405	31	2.431	57	2.457
6	2.406	32	2.432	58	2.458
7	2.407	33	2.433	59	2.459
8	2.408	34	2.434	60	2.460
9	2.409	35	2.435	61	2.461
10	2.410	36	2.436	62	2.462
11	2.411	37	2.437	63	2.463
12	2.412	38	2.438	64	2.464
13	2.413	39	2.439	65	2.465
14	2.414	40	2.440	66	2.466
15	2.415	41	2.441	67	2.467
16	2.416	42	2.442	68	2.468
17	2.417	43	2.443	69	2.469
18	2.418	44	2.444	70	2.470
19	2.419	45	2.445	71	2.471
20	2.420	46	2.446	72	2.472
21	2.421	47	2.447	73	2.473
22	2.422	48	2.448	74	2.474
23	2.423	49	2.449	75	2.475
24	2.424	50	2.450	76	2.476
25	2.425	51	2.451	77	2.477
26	2.426	52	2.452	78	2.478
27	2.427	53	2.453	79	2.479
				80	2.480

Table 38, North American and European Requirements

(Values specified in GHz)

Channel #	Value	Channel #	Value	Channel #	Value
73	2.473	81	2.481	89	2.489
74	2.474	82	2.482	90	2.490
75	2.475	83	2.483	91	2.491
76	2.476	84	2.484	92	2.492
77	2.477	85	2.485	93	2.493
78	2.478	86	2.486	94	2.494
79	2.479	87	2.487	95	2.495
80	2.480	88	2.488	-	-

Table 39, Japan Requirements

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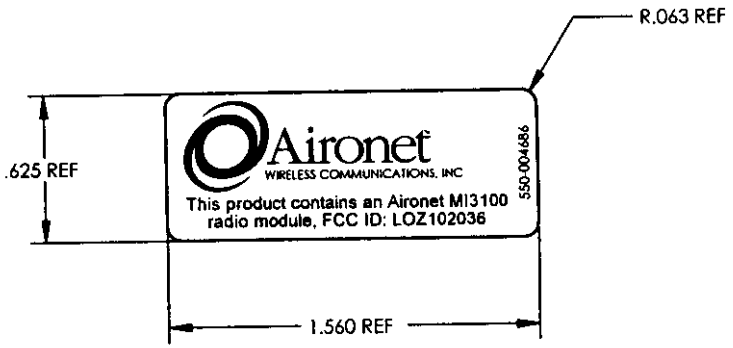
REVISIONS				
REV	ECN NO	DESCRIPTION	DATE	APPROVED
A0	1649	INITIAL RELEASE	1/21/99	JAM

NOTES:

1) POLYESTER FILM PER LABEL STOCK SPECIFICATION
AIRONET P/N 550-003727

2) COLOR:
BACKGROUND: SILVER REF.
LETTERING AND SYMBOLS: BLACK

2) LETTERING TO BE HELVETICA NORMAL OR EQUIVALENT
WHERE APPLICABLE



UNLESS OTHERWISE SPECIFIED:
DEBURR & BREAK SHARP EDGES
DIMENSIONS ARE IN INCHES
TOLERANCES:
X.X = ±0.030
X.XX = ±0.015
X.XXX= ±0.005
ANGULAR = ±0°-30'
MACHINED SURFACES √ MAX
3RD ANGLE PROJECTION
DO NOT SCALE DRAWING

CAD GENERATED DRAWING
DO NOT MANUALLY UPDATE

		DATE
DRAWN	JAM	1/19/99
CHECKED		DATE
APPROVAL		DATE
APPROVAL		DATE

PROPRIETARY INFORMATION

Aironet WIRELESS COMMUNICATIONS INC AKRON, OH

LABEL,
EXTERNAL FCC ID, PCI & ISA,
MI3100 SERIES RADIO

SIZE	PCS or NO	DWG NO	REV
B		LBL 550-004686	A0

SCALE 2/1 MODEL REF 550-004686 SHEET 1 OF 1

DWG NO: LBL 550-004686

REVISIONS				
REV	ECN NO	DESCRIPTION	DATE	APPROVED
A0	0	INITIAL RELEASE	2/5/99	JAM

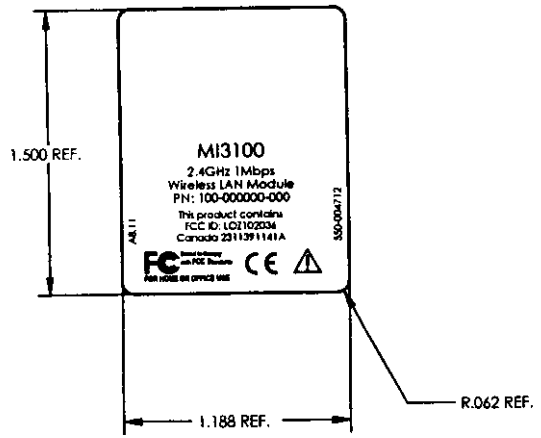
INCORPORATED ECN

NOTES:

1) POLYESTER FILM PER LABEL STOCK SPECIFICATION
AIRONET P/N 550-004711

2) COLOR:
PRODUCT ID LETTERING AND SYMBOLS: BLACK

2) LETTERING TO BE HELVETICA NORMAL OR EQUIVALENT
WHERE APPLICABLE



UNLESS OTHERWISE SPECIFIED
DEBURR & BREAK SHARP EDGES
DIMENSIONS ARE IN INCHES
TOLERANCES:

C.A.D. GENERATED DRAWING
DO NOT MANUALLY UPDATE

DRAWN JAM 2/3/99
CHECKED
ENG APPR.
MFG APPR.

PROPRIETARY INFORMATION

Aironet

LABEL, LOGO & PRODUCT, MI3100

SIZE B | DWG NO LBL 550-004712 | REV A0

NEXT ASSY 550-004711 | SHEET 1

DWG NO 550-004712

11 of 12

AHD LC

EMC Testing/Engineering Services

STATEMENT OF COMPLIANCE

REGULATORY STANDARD _____ FCC Part 15, Subpart B, Class B

NAME OF EQUIPMENT _____ Low Power Transceiver

MODEL NUMBER(S) _____ MI-3100

MANUFACTURER _____ AIRONET Wireless Communications, Inc.
3875 Embassy Parkway
Akron, OH 44333

TEST DATE _____ November 2, 1998

According to testing performed at the AHD Open Area Test Site, the above-mentioned unit is in compliance with Class B limits defined in Part 15 of FCC Rules and Regulations. Compliance is outlined in AHD test report #09800162F issued November 10, 1998.

It is the manufacturer's responsibility to assure that additional production units of this model are manufactured with identical electrical and mechanical components.

As the responsible EMC Test Engineer, I hereby declare that the equipment, specified above, conforms to the Class B RFI Suppression Requirements of FCC Rules Part 15.

Ted Chaffee

Ted Chaffee, NCE
Lab Manager

Date : 11-12-98