

Addendum to Test Report for BreezeCOM Radio LAN PCMCIA Card, model PC-DS.11.

Theoretical Processing Gain and Jammer to Signal Ratios 11Mbps DSSS Radio

| Data Rate | Modulation | Symbol Rate (SyR) Sym per second | Spread Rate (SpR) chips per Symbol | Processing Gain $G_p=10\text{LOG}(\text{SpR}/\text{SyR})$ | Es/No+Lsys | Coding Gain | Calculated J/S Incl. Code gain | Allowed J/S for 10dB [10dB-Es/No+Lsys] |
|-----------|------------|-------------------------------------|---------------------------------------|--|------------|-------------|-----------------------------------|--|
| 1Mbps | BPSK | 1MSps | 11McpS | 10.4 | 12.4 | 0.0 | -2.0 | -2.4 |
| 2Mbps | QPSK | 1MSps | 11McpS | 10.4 | 15.4 | 0.0 | -5.0 | -5.4 |
| 5.5Mbps | CCK4 | 1.375MSps | 8McpS | 9.0 | 15.6 | 2.0 | -4.6 | -5.6 |
| 11Mbps | CCK64 | 1.375MSps | 8McpS | 9.0 | 18.8 | 2.0 | -7.8 | -8.8 |

* Details on CCK modulation performance provided by Harris Semiconductor and Harris HFA3860B Data Sheet.