

FIG 12. IF BLOCK 1 IN RECEIVER (DNCC)

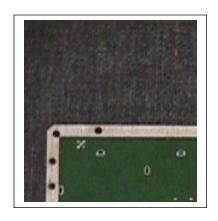


FIG 14. BACKSIDE OF RECEIVER (DNCC)

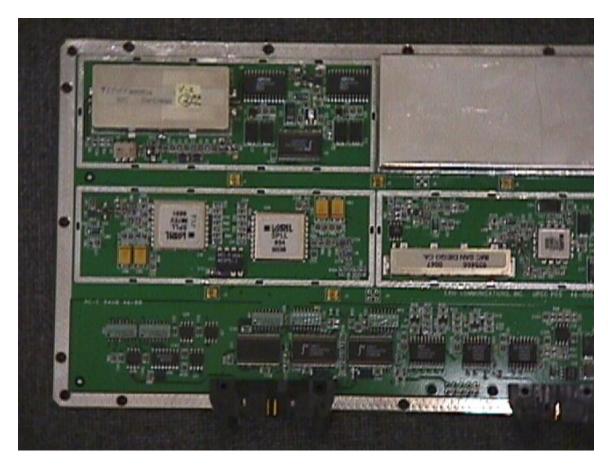


FIG 15. FRONT END BLOCK 1 OF TRANSMITTER (UPCC)

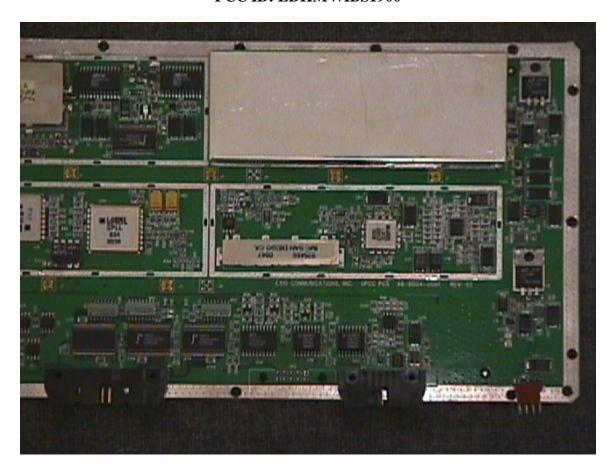


FIG 16. FRONT END BLOCK 2 OF TRANSMITTER (UPCC)

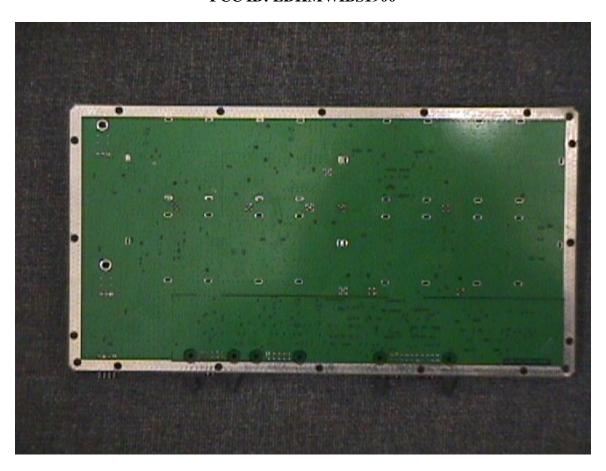


FIG 17. BACKSIDE OF TRANSMITTER (UPCC)

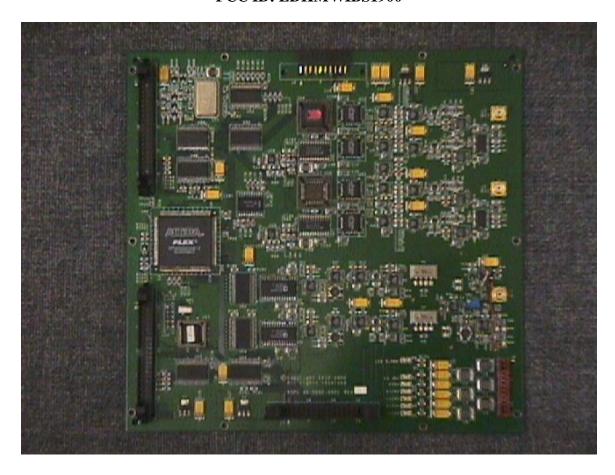


FIG 18. FRONT SIDE OF ANALOG BOARD (ASPC)

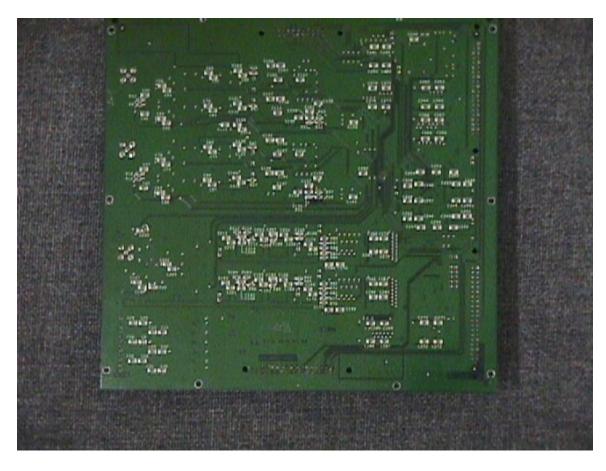


FIG 19. BACKSIDE OF ANALOG BOARD (ASPC)

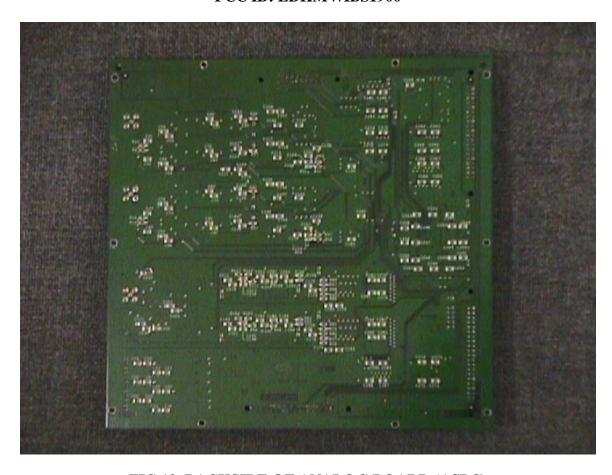


FIG 19. BACKSIDE OF ANALOG BOARD (ASPC)