

data rates	modulation used	symbol rates	chip rate	chip per bit	10 log (chip per bit)	add 3dB if unique codes for I & Q (minus 1 dB for implementation)	theoretical processing gain dB (add last two columns)	measured processing gain
1 mb/s	BPSK	1 mbps	11 mbps	11 (same code for I & Q)	10.7 dB		10.7 dB	10.8 dB
2 mb/s	QPSK	1 mbps	11 mbps	11 (same code for I & Q)	10.7 dB		10.7 dB	10.8 dB
5.5 mb/s	CCK	1.375 mbps	8 mbps	8 (8 for I & 8 for Q)	9 dB	2 dB	11 dB	10.7 dB
11 mb/s	CCK	1.375 mbps	8 mbps	8 (8 for I & 8 for Q)	9 dB	2 dB	11 dB	10.6 dB