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NVLAP ACCREDITED NARTE Certified Engineer Professional Engineer

EMC Testing/Engineering Services

May 3, 2000

Federal communications Commission Equipment Approval Services, P.O. Box 358315 Pittsburgh, PA 15251-5315 Attention: Authorization & Evaluation Division

Re: Application for Certification of Aironet Wireless Communications, Inc. under 47CFR 15.247.

FCC ID: LDK102039

Gentlemen:

On behalf of the applicant, Aironet Wireless Communications, Inc., please find attached the submittal materials for certification of the Aironet 2.4GHz Direct Sequence Spread Spectrum transmitter / receiver.

Pursuant to 47CRF 0.459, Aironet Wireless Communications requests that these listed exhibits be held confidential.

Exhibit G	Theory/Description of Operation,
Exhibit H:	Circuit Block Diagrams
Exhibit I:	Schematics
Exhibit J:	Tuneup Information

Aironet Wireless Communications has invested considerable resources into developing this DSSS. Having the listed exhibits available to 'competition' would negate the advantage achieved in developing this product. Since the DSSS devices are a major product line for Aironet, not protecting the details of the design will result in a financial hardship for the company.

The complete List of the Exhibits in this submittal package appears on Page 2 of this cover letter.

Your prompt consideration of this application for product certification will be greatly appreciated. Should you have any questions regarding the content of this report, kindly contact me.

Sincerely,

Hed Cheffee

Ted Chaffee, Technical Lab Manager Narte Certified Engineer, #EMC-002025-NE tel/fax: 616.424.7014 email: tchaffee@ahde.com, or ahd@locallink.net

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EXHIBIT A:	Cover Letter / Table of Contents		Total Pages	2
EXHIBIT B:	Statements of Attestation [2.911(d)] Attesting to Accuracy of Data Public Notice 22504 Power of Attorney AHD Accreditation	Exhibit B, File "EXB_p1DataAtts Exhibit B, File "EXB_p1DataAtts Exhibit B, File "EXB_p2PowrOA Exhibit B, File "EXB_p3NV:APC	t.jpg" ttrny.pdf"	3
EXHIBIT C:	Description of Product [2.1033(b)]		Total Pages	1
EXHIBIT D:	Processing Gain Information/Calculations [15.24 Processing Gain Jamming Test Data	ł7(e)]	Total Pages Total Pages	
EXHIBIT E:	Product photos - [2.1033(b7)]	Five photos	Total Pages	4
EXHIBIT F:	ID Label / Location [2.925,2.926,2.1033(b2,7)]		Total Pages	1
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EXHIBIT H:	Circuit Block Diagram [2.1033(b5)]		Total Pages	1
EXHIBIT I:	Schematics [2.1033(b5)]		Total Pages	1
EXHIBIT J:	Tune-up Information [2.1033(b5]		Total Pages	2
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