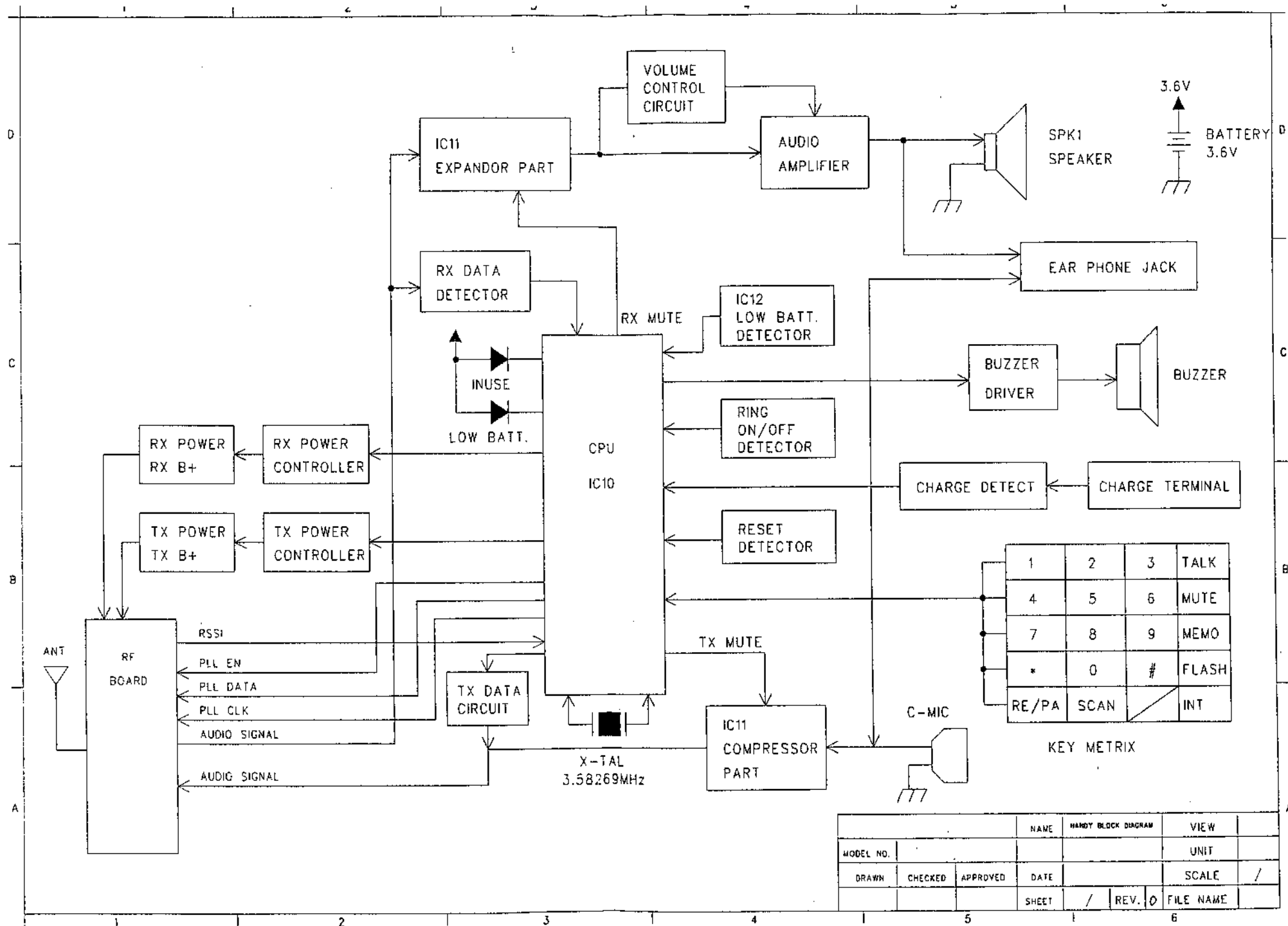


RF BOARD BLOCK DIAGRAM

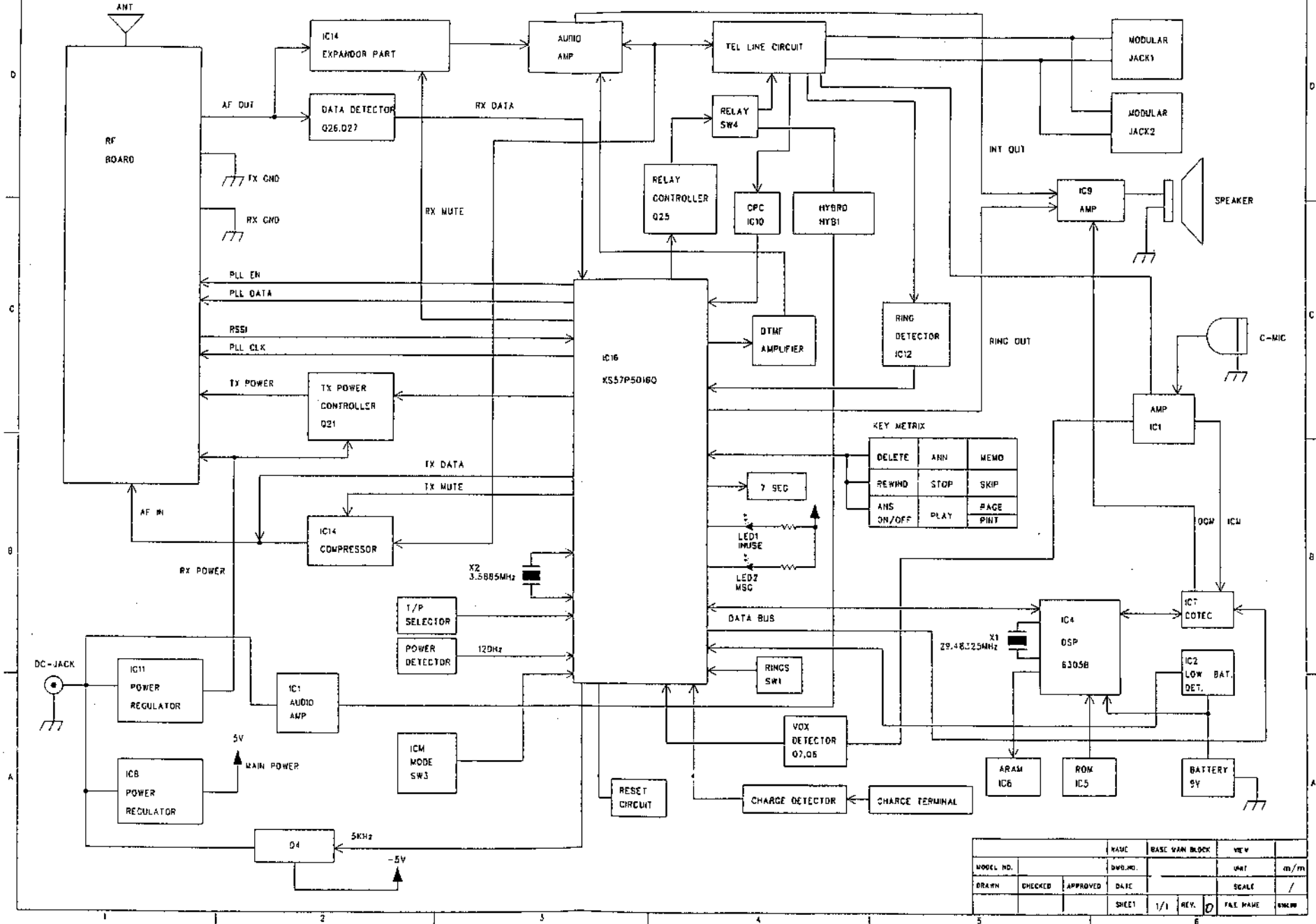
The diagram illustrates the architecture of an RF board. The receiver path starts at the ANT (Antenna) connected to a BPF (Band Pass Filter), followed by an RF AMP (Radio Frequency Amplifier). The signal then passes through RX MIXER1, CF1 (Coupling Filter 1), RX MIXER2, CF2 (Coupling Filter 2), and a DEMODULATOR. The output of the demodulator goes to an AUDIO AMPLIFIER and also provides signals to CONN1 (RSSI, AF OUT, S/D). The transmitter path starts at TX VCO (Transmit Voltage Controlled Oscillator), which is driven by PD2 from IC2. The TX VCO output goes through a TX POWER AMP and a BPF to the ANT. A 11.15MHz crystal (X1) is connected to the XIN pin of IC2. The RX VCO (Receiver Voltage Controlled Oscillator) is connected to RX MIXER1 and RX MIXER2. The central IC2 contains PD1, PD2, XOUT, and XIN pins. A 11.15MHz crystal (X1) is connected to XIN. Various control and data lines are connected to CONN1, including PLL EN, PLL DATA, PLL CLK, RX B+, RX GND, TX B+, TX GND, and AF/DATA. The diagram also shows the connection of RXB+ and TXB+ to the RX and TX paths respectively, with coupling capacitors C5 and C6.

			NAME	RF BLOCK DIAGRAM			VIEW	
MODEL NO.			DWG.NO.				UNIT	
DRAWN	CHECKED	APPROVED	DATE				SCALE	/
			SHEET	/	REV.		FILE NAME	



			NAME	HANDY BLOCK DIAGRAM	VIEW	
MODEL NO.					UNIT	
DRAWN	CHECKED	APPROVED	DATE		SCALE	/
			SHEET	/	REV. 0	FILE NAME

BASE MAIN BLOCK DIAGRAM



MODEL NO.	NAME	BASE MAIN BLOCK	VIEW	
DRAWN	CHECKED	APPROVED	DATE	SCALE
SHEET	1/1	REV.	0	FILE NAME