

## ER5845n Operation description

1. CPU: U1, The BCM5357B0KFBG Broadcom's 802.11n compliant 2T2R MAC/BBP/2.4G ISM band Transceiver , a high performance 300MHz MIPS24KEc CPU core, 5 - port integrated 10/100 Ethernet switch/PHY; An 20MHZ crystal(Y1) for CPU'S reference frequency;  
Operational Principle:
2. SDRAM: H5DU1262GTR-E3C or M13S128168A-5TG or W9412G6JH-4 is a high-speed synchronous dynamic random access memory (SDRAM), organized as 2M words  $\times$  4 banks  $\times$  16 bits.
3. FLASH: U503 W25Q16BVSSIG/Winbond FLASH, used for store firmware and user`s setting;
4. PA:Inside OF CPU
5. RF Switch: GW1179 (U23&U21)
6. Power Part: U35 FR9882SPGTR Step-Down PWM Converter, used for transfer DC.
7. WIFI802.11b/g/n(HT 20, HT40): Operating frequency range: 2412~2462MHz  
Modulation: OFDM, DSSS DBPSK, DQPSK, CCK, 16QAM, 64QAM
8. Antenna type: Welding Antenna, Antenna gain: 5dBi