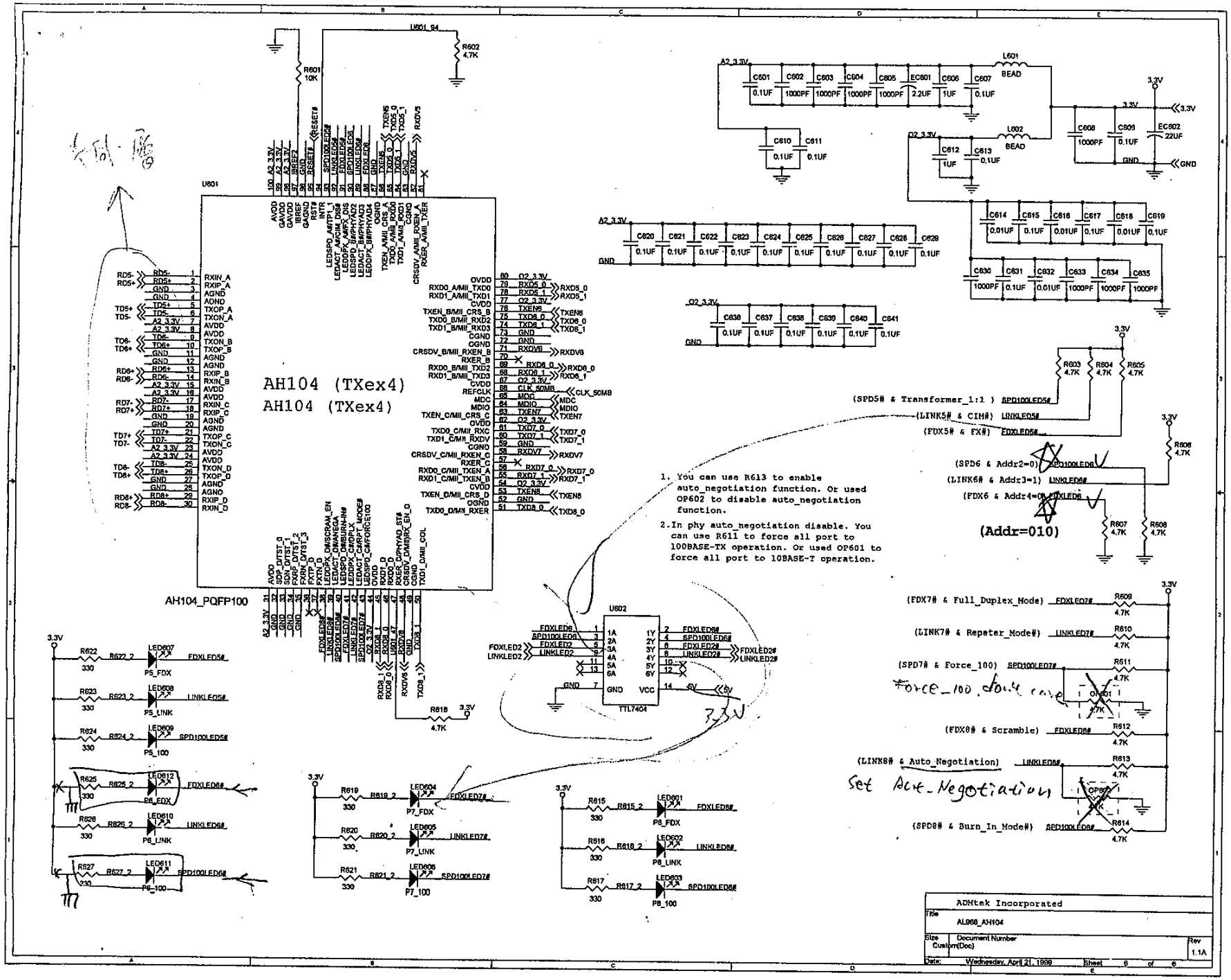
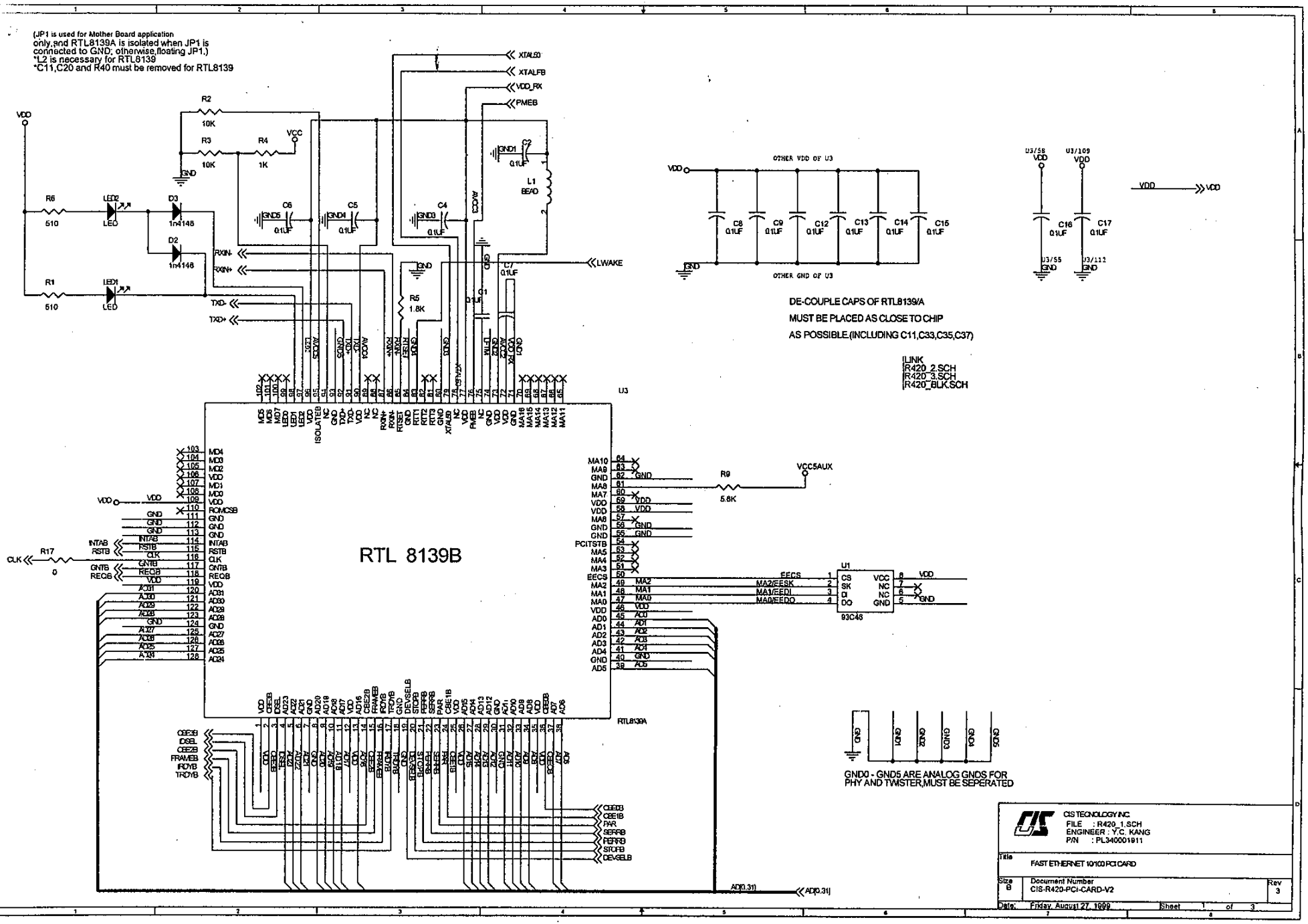
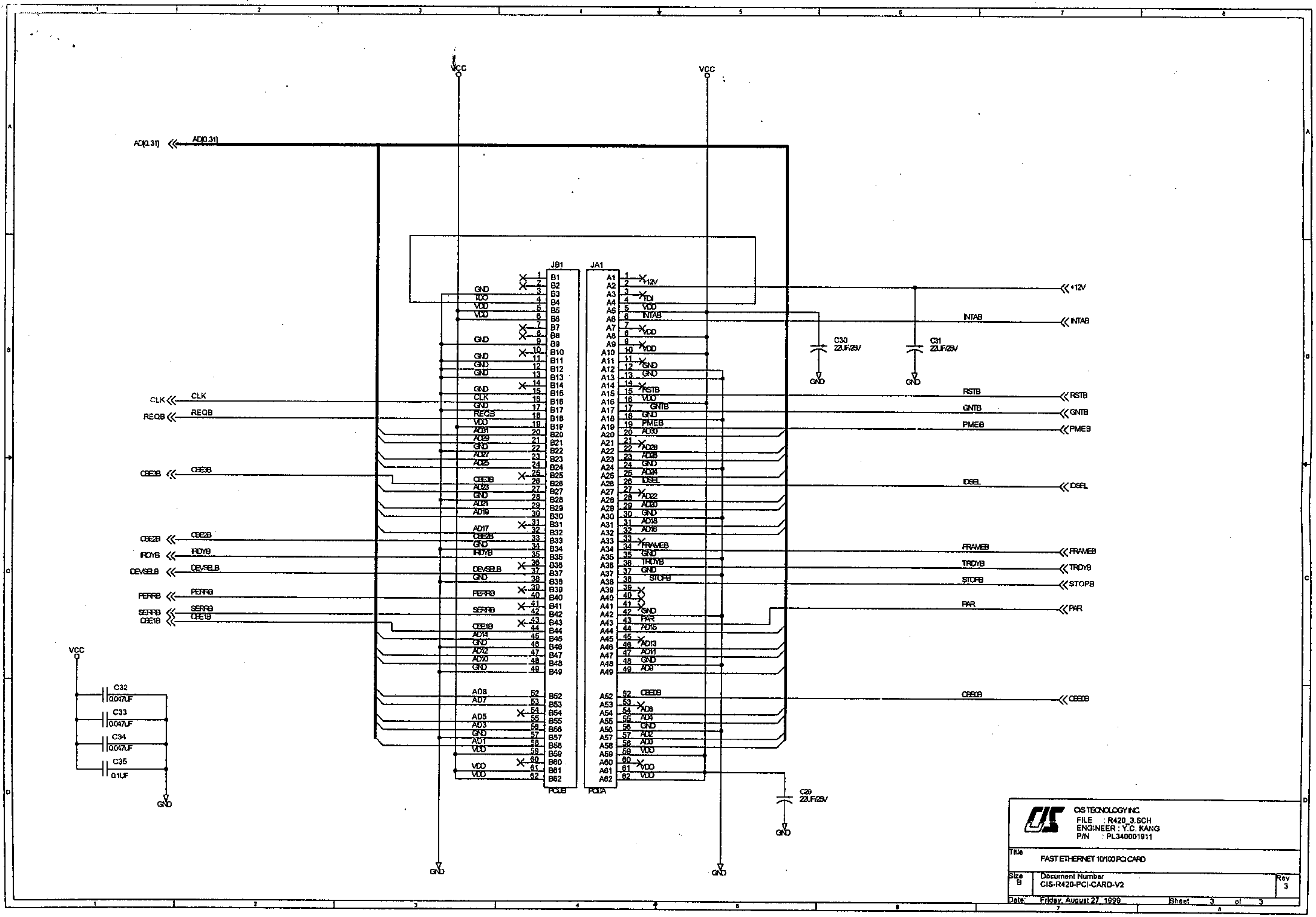
EXHIBIT D

Circuit Diagram


AH104 (TXex4)
AH104 (TXex4)
AH104_PQFP100
1. You can use R613 to enable auto_negotiation function. Or used OP602 to disable auto_negotiation function.
2.In phy auto_negotiation disable. You can use R611 to force all port to 100BASE-TX operation. Or used OP601 to force all port to 10BASE-T operation.
(Addr=010)
(SPD5# & Transformer_1:1)
(LINK5# & CIM#)
(FDX5# & FX#)
(SPD6 & Addr2=0)
(LINK6# & Addr3=1)
(FDX6 & Addr4=0)
(FDX7# & Full_Duplex_Mode)
(LINK7# & Repeter_Mode#)
(SPD7# & Force_100)
Force_100 don't care
(FDX8# & Scramble)
(LINK8# & Auto_Negotiation)
Set Aut_Negotiation
(SPD8# & Burn_In_Mode#)
TTL7404
ADMtek Incorporated
AL968_AH104
Wednesday, April 21, 1999


(JP1 is used for Mother Board application
only,and RTL8139A is isolated when JP1 is
connected to GND; otherwise,floating JP1.)
*L2 is necessary for RTL8139
*C11,C20 and R40 must be removed for RTL8139
RTL 8139B
RTL8139A
U3
U1
93C46
DE-COUPLE CAPS OF RTL8139/A
MUST BE PLACED AS CLOSE TO CHIP
AS POSSIBLE.(INCLUDING C11,C33,C35,C37)
LINK
R420_2.SCH
R420_3.SCH
R420_BLK.SCH
GND0 - GND5 ARE ANALOG GNDS FOR
PHY AND TWISTER,MUST BE SEPERATED
OTHER VDD OF U3
OTHER GND OF U3
CIS TECNOLOGY INC.
FILE : R420_1.SCH
ENGINEER : Y.C. KANG
P/N : PL340001911
FAST ETHERNET 10/100 PCI CARD
Document Number
CIS-R420-PCI-CARD-V2
Rev 3
Friday, August 27, 1999
Sheet 1 of 3



VCC
GND
AD[0..31]
CLK
REQB
CBE3B
CBE2B
IRDYB
DEVSELB
PERRB
SERRB
CBE1B
JB1
JA1
PCIJB
PCIJA
+12V
INTAB
RSTB
GNTB
PMEB
IDSEL
FRAMEB
TRDYB
STOPB
PAR
CBE0B
C29
22UF/25V
C30
22UF/25V
C31
22UF/25V
C32
0.047UF
C33
0.047UF
C34
0.047UF
C35
0.1UF
CIS TECNOLOGY INC
FILE : R420_3.SCH
ENGINEER : Y.C. KANG
P/N : PL340001911
Title
FAST ETHERNET 10/100 PCI CARD
Size B
Document Number
CIS-R420-PCI-CARD-V2
Rev 3
Date: Friday, August 27, 1999
Sheet 3 of 3